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A 4.2 K Very High-Gain/Modulation Factor Silicon Detector/Modulator

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Abstract. An n-well ion-implanted resistor is shown to work as a very-linear high-gain photodetector at 4.2 K. We take advantage of freeze-out and light-assisted carrier ionisation effects to create a photodetector with a current-gain factor G from $1 \times 10^4$ to $1.6 \times 10^6$. Experimental results show that at the current gain of $1.6 \times 10^6$, excellent linearity in optical response is obtained when the resistor is illuminated with a red-light source.

I. INTRODUCTION

Carrier freeze-out [1] is a well known effect that occurs in silicon having doping concentration levels below the degeneration level ($1 \times 10^{18}$ cm$^{-3}$). Thus for a non-degenerately doped silicon resistor, it is expected that mobile carriers will be deeply frozen when operated at temperatures below about 30 K [2], [3]. This means that a silicon resistor will behave as an insulator, unless free carriers are provided by an external source. Under deep freeze-out conditions there are three different ways to provide free carriers; Poole-Frenkel Ionisation (PFI) [4], Field-Assisted Ionisation (FAI) [4], and Light-Assisted Ionisation (LAI) [4]. PFI starts playing a relevant role for carrier ionisation at temperatures above 15 K. Below this critical temperature, under dark conditions, carriers can only be generated by applying an external electric field (FAI). Below 15 K, and for applied voltages below the ionisation voltage $V_c$, the silicon resistor behaves as an insulator. Once the voltage is above $V_c$, some initial free carriers are generated which immediately give rise to carrier multiplication by avalanche, thus creating a large number of free carriers, which again results in the ohmic behavior.

We investigate how this ion-implanted resistor device reacts to light when operating in the insulating regime, and present some possible cryogenic application in the field of optical telecommunications, space detection and astronomy instrumentation.

II. EXPERIMENTAL DETAILS

A $1 \times 10^{17}$ cm$^{-3}$ n-well implanted resistor with a 1.8 μm junction depth, a width of 70 μm, and a length of 10 μm was tested. The p-substrate has the same doping concentration of $1 \times 10^{17}$ cm$^{-3}$. The n$^+$ and p$^+$ contact regions have junction depths of 0.25 μm, and the field oxide on top of the n-well region is 650 nm thick. Measurements were performed using an HP4156A Parameter Analyzer, a closed-cycle refrigerator (at Simon Fraser University), and an open cycle cryostat LMT 500-Lakeshore (at INAOE).

The I-V characteristics of the 10 μm length resistor with and without light, operated at $T=4.2$ K, are shown in Fig. 1. Under dark conditions the ionisation voltage $V_c$ is about 0.68 V. For voltages below $V_c$ the resi-

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tor remains completely frozen, without free carriers available for electrical conduction, therefore behaving as an insulator. The ionisation voltage is a linear function of the resistor length.

**Figure 1:** Current-voltage characteristics of the n-well resistor at 4.2 K. The dashed line corresponds to dark- and the continuous one to light-conditions.

Once the voltage is above $V_c$, the resistor turns on from an off-current $I_{\text{off}}=1$ pA to an on-current $I_{\text{on}}$ in the range of 1 to 5 mA, and the ohmic regime is established. In this case the electric field $E$ has assisted carrier ionisation. The continuous line represents the current when the structure is illuminated with a 765 nm wavelength-laser diode.

The energy needed to create free carriers can be supplied with a light beam. The amount of free carriers generated can be controlled through the wavelength and the optical power. In this way, the structure may act, in general, as an optical detector with a very high photodetection gain.

The photodetection gain depends on the wavelength $\lambda$, the length between the two n-well contacts $L$, the surface n-well doping concentration $N$, the operating temperature $T$, and the n-well junction depth $X_j$.

The carrier conduction and the carrier flow distribution are depicted in Fig. 2. When the light beam comes through the structure, electron-hole pairs are generated in the silicon structure. The electrons flow towards the n-well contacts (VAR and REF), while the holes are swept towards the p-substrate contact (PSB). Four current paths are identified; 1) is the photo-electron current that flows into the VAR contact, 2) is the photo-electron current flowing into the REF contact, 3) is the photo-hole current that flows into the PSB contact, and 4) is the electron current that flows between the REF and VAR contacts, when a potential difference $(V_{\text{var}}-V_{\text{ref}})$ exists between these two contacts. It is worth mentioning that the photo-electron current splits into two branches unequally. This means more electrons are swept towards the VAR contact than to the REF contact. This unbalanced flow becomes more pronounced when the $V_{\text{var}}$ voltage starts sweeping from 0 to 1V, while $V_{\text{ref}}$ is kept constant at 0V. The carrier conduction in the structure exposed to red-light is shown in Fig. 3. In the insulator regime, the current $I_{\text{var}}$ stays increasing linearly as the
voltage $V$ ($V_{\text{var}}-V_{\text{ref}}$) is swept from 0 to 1 V. The $I_{\text{ref}}$ current is positive from 0 to about 0.5 V, then it becomes negative, while the hole current $I_{\text{psb}}$ keeps negative and increasing in value, when the voltage is swept from 0 to 1 V. The $I_{\text{ref}}$ current changes of sign because the absolute value of the current flowing between the $\text{var}$ and $\text{ref}$ contacts becomes higher than the current component 2.

**Figure 2:** Schematic representation of the n-well resistor with 4 current components identified: 1) photo-electrons sunk to the REF contact, 2) photo-electrons drained to the VAR contact, 3) photo-holes sunk to the PSB contact, and 4) the electrons that flow between the REF and VAR contacts.

**Figure 3:** Current-voltage characteristics of the n-well resistor under illumination. The dashed line corresponds to the absolute value of p-substrate current $I_{\text{psb}}$. $I_{\text{var}}$ is the current measured at the VAR contact, and $\text{abs}(I_{\text{ref}})$ is the absolute value of the current measured at the REF contact.

To investigate the influence of the optical power on the I-V characteristics, the power consump-
tion of the laser diode was varied from 85 to 765 mW. The I-V characteristics in the ohmic regime are not affected by the light. Therefore, in Fig. 4 only the insulator regime is shown. Under dark conditions, the current in the insulator regime is at the picoampere level. This means that for the highest light intensity (curve i) the gain current \( \frac{I_{\text{light}}}{I_{\text{dark}}} \) is about \( 1.6 \times 10^6 \) at \( V = 0.4 \) V.

Under illumination, the insulator region (0 to 0.7 V) becomes ohmic. This ohmic behavior is indicated with the straight line between 0 and 0.4 V. At higher voltages, the FAI mechanism starts influencing the structure, which can be observed from the deviation of the linear curve towards an exponential behavior.

Finally, the insulator-regime conductance \( g_{\text{in}} \), in the 0.0-0.4 voltage range, versus laser power consumption \( P \) is shown in Fig. 5. The open squares corresponds to experimental data, and the line is a linear fit. A transresistance \( g_r \) of \( 4.53 \times 10^8 \) \( \Omega / \text{mW} \) is extracted out of the linear fit. At present we have not characterised the light intensity (or optical power) versus power consumption of the laser at 4.2 K.

The experimental results shown in Fig. 5 indicates that this device is effectively acting as a transistor, with the \( n^+ \) ref contact as source terminal, the \( n^+ \) var contact as drain terminal, the \( p^+ \) contact as substrate terminal, and the light beam as a gate. In this case, the channel and its conductance are controlled by the amount of photoinjected electrons, with the amount of photoelectrons depending on the light beam energy and its optical power. In analogy to a MOS transistor, here one has a Light-Oxide-Silicon-CryoTransistor (LOSCT), with a current gain \( G \) as large as \( 1.6 \times 10^6 \) in the linear portion (0.0-0.4 V) of the \( V_{\text{var}} \) voltage, and a optical-to-electrical conductance \( g \left( \frac{I_{\text{var}}}{P} \right) \) of about \( 2.0 \times 10^2 \) \( \mu A / \text{mW} \).

With the above electrical/optical characteristics, this cry-optical device can be used as a very-high-gain detector. As a modulator it can be used to modulate microwave signals [7] that have to pass through the frozen region. The refraction index of the n-well silicon is controlled by the

![Figure 4](image-url)
The speed response of this photodevice is currently under investigation, and more results will be presented later.

III. CONCLUSION

A n-well implanted resistor, operated at 4.2 K in the insulator regime, has been electrically and optically characterized, and proven to operate as an optical detector with a gain as large as $1.6 \times 10^6$, and with a very good Current-Voltage linearity. The optical-to-electrical conductance $g_{oe}$ ($\Delta I_{var}/\Delta P$) is found to be $2.0 \times 10^{-2} \mu A/mW$, with an excellent I-V linearity in the 0.0-0.4 V range. In the linear region, and at a maximum gain, the power dissipation is 0.68 $\mu W$, which makes this device an excellent choice for low-power consumption.

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