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Typical Aspects of the Microwave Noise Performance of HEMTs at Decreasing Temperatures

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Abstract. In analog signal processing at microwave frequencies the noise performance of active devices is of fundamental importance for the accurate design of low-noise amplifiers. To this aim, the determination of the four noise parameters $F_0$, $\Gamma_0$ (complex variable) and $R_0$ has to be accomplished together with the usual scattering parameter measurements vs. frequency. In addition, the dependence of the device performance vs. temperature is of interest for circuit applications characterized by harsh environmental conditions. In this work the noise behavior of high electron mobility transistors has been investigated by means of measurements and modeling in the 2-18 GHz frequency range and as a function of the decreasing temperature over the -50 - +20°C range. The most interesting aspects of the observed variations occurred with temperature are explained on the basis of the device equivalent circuit.

1. INTRODUCTION

Knowledge of the noise behavior of active devices is the main concern in designing low-noise amplifiers (LNA) for analog signal processing in the microwave frequency range (1-30 GHz).

At present, conventional GaAs and pseudomorphic high electron mobility transistors (namely, HEMT and pHEMT) exhibit the best noise and gain performance among solid-state devices operating up to the millimeter wave range. Due to these features, they are widely used for the realization of LNA’s to be employed in applications such as satellite communication, radio astronomy, remote sensing, and so on, where maximum signal sensitivity is strictly required.

To the aim of obtaining a complete characterization of the active device under test (DUT) we need to measure both the scattering and the noise parameters vs. frequency. The results of the characterization can either be employed as a black-box model of the DUT or be used for the extraction of an equivalent circuit model which is a more accurate and flexible tool for describing the DUT performance in CAD applications.

In this work, the microwave performance of different series of HEMT has been investigated as a function of frequency (2-18 GHz) and decreasing temperature (from 20°C to -50°C, 10°C step) for assessing the most typical aspects of such devices from the noise viewpoint.

The devices have been tested at each temperature in terms of scattering parameters and noise figure $F_0$ in input matched conditions (i.e. @ standard source impedance of 50 $\Omega$) at the bias conditions suggested by the manufacturer for the best low-noise performance. In addition, at room temperature we performed a complete characterization of the devices by determining all the noise, gain and scattering parameters with a noise figure measuring system only, whose relevant measurement procedures and data processing techniques have been totally developed in our Lab [1].

A model extraction has then been accomplished in terms of an equivalent electrical network for computing the four noise parameters $F_0$, $\Gamma_0$ (complex variable) and $R_0$ vs. temperature by model analysis. The most interesting aspects of the observed variations occurred with temperature have been explained on the basis of the noise behavior of device model which has previously shown (at room temperature) a very good degree of reliability in predicting the actual device performance.

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2. DEVICE CHARACTERIZATION

At microwave frequencies, the noise parameters usually determined are represented by the minimum noise figure $F_0$, the relevant optimum (complex) value $\Gamma_0$ of the input termination reflection coefficient $\Gamma_S$ at which $F_0$ is located, and the equivalent noise resistance $R_n$ which is a measure of the noise figure degradation upon departure of $\Gamma_S$ from its optimum value. The noise equation linking the above parameters is:

$$F(\Gamma_S) = F_0 + 4R_n \frac{||\Gamma_S - \Gamma_0||^2}{1 + \Gamma_0^2 (1 - ||\Gamma_S||^2)}$$

Basically, the determination of the above noise parameters is accomplished by source-pull measurements, i.e. by measuring the noise figure value at properly selected input reflection coefficients $\Gamma_S$ (more than four, for redundancy) and by processing data through eq.(1).

In our Lab an original and yet well-assessed technique has been developed several years ago which allows for determining either the noise parameters and the scattering parameters of the DUT from all and only noise data. The scattering parameters, when determined by our system, are not measured directly by a network analyzer (see [1] and references therein).

The system is now operating in the updated version over the 1-40 GHz frequency range, vs. bias and temperature, for the complete characterization of low-noise devices in packaged and in chip form. A block diagram is reported in Fig.1.

As far as the present work is concerned, two different HEMT series (CFB001-03 by Celeritek, TC2623 by Thomson) have been characterized first at room temperature in terms of noise, gain and scattering parameters by means of our system in the 6-14 GHz frequency range. As a second step, their scattering parameters were measured from 2 to 18 GHz by use of the automatic network analyzer with the DUT placed in a thermostatic chamber to control its ambient temperature over the -50 - +20 °C range at 10 °C steps. In the same operating conditions, we employed a commercial noise figure meter for performing noise figure measurements at the fixed condition of $\Gamma_S = 0$ (i.e., 50 $\Omega$ impedance), which is usually referred to as $F_{50}$. The measured values of $F_{50}$ have then been used for the simplified determination of a noisy model of HEMTs, as discussed in the modeling section of this paper.

3. MODELING PROCEDURE

The equivalent circuit model has been determined for each device series by fitting the measured S-parameters vs. temperature. The model extraction has been accomplished by following the subsequent steps: 1) fixing the basic circuit topology which includes package parasitics; 2) applying a decomposition approach for the optimization of the circuit variables at room temperature; 3) adding knowledge of the behavior of the chip model elements to provide physical consistency and additional contraints for the optimization at lower temperatures.

The basic structure obtained for the given transistor series is sketched in Fig.2; the associated element values vary according to each HEMT set and the decreasing temperature.

The circuit elements which mainly affect the microwave performance of the device are the transconductance $g_m$, the gate-to-source capacitance $C_{gs}$ and the output resistance $R_{ds}$. Their value trends are in accordance with the physics-based behavior.

The complete noise performance of the device has been reproduced by associating to its equivalent circuit a simple noise model which employs two uncorrelated equivalent noise temperatures (namely, $T_{ch}$ and $T_d$) associated with $R_{ch}$ and $R_{ds}$, desides the thermal noise due to the remaining lossy elements of the circuit model. More practically, since $T_{ch}$ has been found to approach the ambient temperature for HEMTs, this model reduces to the single frequency-independent factor $T_d$ whose value can be derived by fitting either the optimum noise figure $F_0$ or the $F_{50}$ noise figure. Common values of this fitting factor lie within the 1000-5000 K range and decrease with either decreasing temperature and decreasing noise figure values.

At each ambient temperature we determined the relevant $T_d$ value and properly set the other resistor temperatures. By model analysis, we then computed the complete noise performance in
terms of the four parameters. We had previously tested the effectiveness of this procedure by comparing the simulated noise parameters with the measured ones deriving from a complete characterization at room temperature (20 °C). The results of this comparison showed that we can get noise parameter values close within a 10% difference which supports the reliability of this method [3].

4. RESULTS

Sample plots showing the measured scattering parameters and the \( F_{50} \) noise figure vs. frequency and temperature are reported in Figs. 3-5 for the TC2623 series by reporting the statistically averaged behavior. It is clearly seen that the performance of the devices in terms of scattering parameters is not much changed, which is to be expected due to the reduced temperature lag (70 °C). Substantial alterations of these devices can be observed for temperature ranging down to -100 °C. The other parameters show only subtle changes which, however, affect the device modeling.

However, slightly pronounced effects are observed for \(|S_{21}|\) (i.e. the device gain) which exhibits a clear trend for increasing upon cooling down the device, and \(|S_{22}|\) which decreases with temperature. The behavior of these parameters is directly related to the device channel properties which are affected by temperature changes. As a consequence on the model element values, we note an increase of the transconductance \(g_m\), a decrease of the output resistance \(R_{ds}\) and an increase of the output capacitances \(C_{gs}\) and \(C_{gd}\). In addition, the expected increase of the cutoff frequency \(f_T = g_m / 2 \pi C_{gs}\) as the temperature lowers is due to either the increase of \(g_m\) and a decrease of the values of \(C_{gs}\).

As far as the noise figure \(F_{50}\) is concerned, the measured data show that it decreases by lowering the temperature and the slope of the relevant curves vanishes. As an example, the performance of \(F_{50}\) at -40 °C is quite flat with values below 1 dB over the entire measured frequency range (6 to 16 GHz). Such a behavior is almost ideal for an active device to be employed in a low-noise wideband amplifier since it would allow for obtaining a very low noise figure without any tuning network at the device input port.

On the basis of the measured values of \(F_{50}\) we derived the noise fitting factor \(T_d\) and employed the circuit models of the two HEMT sets for deriving the complete noise performance at decreasing temperatures.

For the sake of comparison with the model performance at different temperatures, the complete noise parameters relevant to all devices of the CFB001-03 series at room temperature is shown in Fig.6a-d. By model analysis we have then derived the noise parameters upon lowering the noise temperatures of the resistive elements and varying accordingly the values of the circuit elements. By analyzing the latter HEMT type, we note that the noise performance exhibits a marked change in the behavior of \(II'_{\text{ol}}\) with respect to that observed at 20 °C. The behavior of \(II'_{\text{ol}}\) is shown in Fig.7 and the substantial variation occurring with the temperature is represented by its trend for becoming higher than \(|S_{11}|\) in the middle of the frequency range (about 6 to 14 GHz). The reason of this variation has been investigated by model analysis to the aim of separating the contributions due to the noise temperatures of the resistive elements from those originated by the different value sets of the model elements. It has been found that the main cause of this performance of \(II'_{\text{ol}}\) at decreasing temperatures is the change in the circuit element values since the noise temperatures play a minor role over the observed value ranges [5].

As far as the phase of \(\Gamma_0\) is concerned, we observed that its performance is not much affected by the temperature, except for slight variations at the higher frequencies.

The behavior of \(F_0\) is linear vs. frequency, even at low temperatures, but its slope and its values decrease markedly as it occurs to \(F_{50}\). The slope reduces to 0.04 dB/GHz at -50 °C which is 50% of its value at 20 °C, and the value of \(F_0\) at 18 GHz is 0.7 dB.

Finally, we noted that the curves of the equivalent noise resistance \(R_n\), shown in Fig.8, broaden with frequency by lowering the temperature. Also, it decreases proportionally thus reaching values less than 1 Ω in the middle frequency range.

While the performance of \(F_0\) is directly affected by the decrease of the model noise temperatures (mainly \(T_d\)), the performance of \(F_{50}\) is determined by either the lower noise temperatures and the "combined" influence of \(R_n\) and \(\Gamma_0\) which are heavily affected by the
change of the model element values with the temperature. As an overall result, the noise performance of the device markedly improves upon decreasing the ambient temperature.

The model prediction will be compared with the results of the complete noise characterization vs. temperature which is currently in progress.

5. CONCLUDING REMARKS

In this work, the microwave performance of different series of HEMT has been investigated as a function of frequency (2-18 GHz) and decreasing temperature (from 20°C to -50°C, 10 °C step) for assessing the most typical aspects of such devices from the noise viewpoint.

The devices have been tested at each temperature in terms of scattering parameters and noise figure $F_{50}$ in input matched conditions. In addition, at room temperature we performed a complete characterization of the devices by determining all the noise, gain and scattering parameters with an original noise figure measuring system only.

A model extraction has then been accomplished in terms of a noisy equivalent electrical network for computing the four noise parameters vs. temperature by model analysis. The most interesting aspects of the observed variations occurred with temperature have been presented and evaluated. Knowledge of the changes in the device noise parameters is of basic importance in accomplishing the accurate design of low-noise amplifiers for applications in unfriendly environmental conditions where a high-level performance of the receiver has to be guaranteed.

References


Figure 1: Block diagram of the noise figure measuring system employed for the complete characterization of low-noise active devices
Figure 2: Equivalent circuit model extracted for the HEMT series (CFB001-03, by Celeritek, and TC2623 by Thomson) characterized vs. frequency and temperature.

Figure 3: Measured $|S_{11}|$ and $|S_{21}|$ parameters (average device performance) as a function of frequency and temperature for the TC2623 series.

Figure 4: Measured $|S_{12}|$ and $|S_{21}|$ parameters (average device performance) as a function of frequency and temperature for the TC2623 series.

Figure 5: Measured performance of the noise figure $F_{50}$ (typical device of the TC2623 series) as a function of frequency and temperature $T$. 

Figure 6: Circuit diagram of the equivalent circuit model.
Figure 6: Measured noise parameters of the FIB/001-03 HEMT series at room temperature: a) minimum noise figure $F_0$ (dB); b) magnitude of the optimum noise reflection coefficient $\Gamma_0$; c) angle of the optimum noise reflection coefficient $\Delta \theta$; d) equivalent noise resistance $R_n$ (Ω).

Figure 7: Computed performance of $|\Gamma_0|$ vs. frequency at decreasing temperatures (CFB001-03 series).

Figure 8: Performance of the equivalent noise resistance $R_n$ modeled vs. frequency at decreasing temperatures (model of the CFB001-03 series).