The Impact of Ge Grading on the Bias and Temperature Characteristics of SiGe HBT Precision Voltage

References

M. Latham, J. Cressler, A. Joseph, R. Jaeger

To cite this version:


HAL Id: jpa-00254235
https://hal.archives-ouvertes.fr/jpa-00254235

Submitted on 1 Jan 1996

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L’archive ouverte pluridisciplinaire HAL, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d’enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.
The Impact of Ge Grading on the Bias and Temperature Characteristics of SiGe HBT Precision Voltage References

M.S. Latham, J.D. Cressler, A.J. Joseph and R.C. Jaeger

Abstract: We analyze the effects that the Ge profile shape has upon the bias and temperature characteristics of SiGe HBT's. The widely used bandgap reference (BGR) design equation and a more general analytical expression we developed incorporating Ge grading are used to compare silicon devices to their SiGe counterparts. Measurement and simulation show that although the Ge-ramp effect is negligible in the -55 to 85 °C range, it can become important as the temperature drops, perhaps affecting the operation of SiGe circuits in the 77 K regime.

1. INTRODUCTION

The past few years have seen significant progress in SiGe HBT technology. The SiGe HBT was first demonstrated in 1987 [1], and since then devices with fT and fMAX greater than 50 GHz have been used to realize such circuits as a 1.0 GS/sec 12-bit A/D converter [2], as well as mixers and voltage-controlled oscillators operating in the 5-25 GHz range [3,4]. With performance figures such as these, there is great potential for the use of SiGe in low-cost, high-speed circuits for RF, microwave, and analog applications. The ac and dc characteristics of SiGe circuits at room temperature are significantly better than those of silicon [2], and both its gain and frequency response improve with cooling, thus making SiGe HBT's ideally suited for cryogenic applications [5].

To test the applicability of SiGe HBT technology to precision analog circuitry, however, its stability over temperature and bias must be proven. It has been shown that Ge grading across the base can have a negative impact upon collector current behavior, and hence current gain, in SiGe HBT's operating at low temperature [6]. Since its introduction by Wildar in 1971 [7], the bandgap reference (BGR) circuit has been widely used as a voltage reference source in A/D and D/A converters, voltage regulators, and other precision analog circuits due to its good long-term stability and its ability to operate at low supply voltages. In this paper we experimentally examine
the effects of Ge grading on the bias and temperature characteristics of SiGe HBT's for BGR applications.

2. THE GE-RAMP EFFECT

We must first examine the effects of the Ge profile shape on the operation of the HBT in order to analyze the performance of precision SiGe circuits. The collector current at any bias of a graded-base SiGe HBT is exponentially dependent upon the amount of Ge at the edge of the emitter-base space-charge region. If we imagine increasing the collector current, the base-emitter voltage also increases and the space-charge region contracts, thereby reducing the amount of Ge seen by the device at that boundary (refer to Fig. 1). The dependence of the collector current on the Ge profile is given in equation (1):

$$J_{C,\text{SiGe}} = \frac{qD_{nb}}{N_{ab}W_b} \left[ \frac{e^{\Delta E_{\text{Ge}}(0)/kT} \Delta E_{\text{Ge}}(\text{grade}) / kT}{1 - e^{-\Delta E_{\text{Ge}}(\text{grade})/kT}} \right]$$

where $E_{\text{Ge}}(0)$ is the bandgap narrowing due to Ge at the emitter-base depletion edge at bias $V_{BE}$, $\Delta E_{\text{Ge}}(\text{grade})$ is the grading across the quasi-neutral base, $\gamma$ is the position-averaged density-of-states reduction factor $(N_{C}N_{V})_{\text{SiGe}} / (N_{C}N_{V})_{\text{Si}}$, and $\bar{n}$ is the ratio of electron diffusivities in the base $(\bar{D}_{ns})_{\text{SiGe}} / (D_{ns})_{\text{Si}}$. The result is a degradation in $J_C$ (hence $\beta$) as we increase the base-emitter (BE) bias. In addition, because $J_C$ depends exponentially on the Ge-induced bandgap reduction at the BE junction divided by $kT$, this effect becomes more pronounced at cryogenic temperatures [6].

Fig. 1. The emitter-base space-charge region and the Ge ramp effect.
To determine the effects of the Ge-induced ramp effect on the BGR circuit, we must recast the collector current equation into the familiar BGR design equation (2) [8]:

$$V_{BE} = E_{ge} - \frac{T}{T_R} \left( E_{ge} - V_{BERR} - \frac{kT_R}{q} \ln \frac{I_C}{I_{CR}} + m \frac{kT_R}{q} \ln \frac{T}{T_R} \right) \quad (2)$$

In equation (2), $E_{ge}$ is the average base bandgap voltage in the presence of doping and $T_R$, $I_{CR}$, and $V_{BERR}$ are the reference values of the respective parameters (typically referenced to 300 K). First, the process-dependent parameters ($B$) and the Ge profile terms ($z$) can be lumped together as in equation (3),

$$I_C = B T^n e^{-E_s/kT} e^{qV_{BE}/kT} z \quad (3)$$

and rewritten in terms of the base-emitter voltage (equation (4)). In practice, we can “measure”

$$V_{BE} = E_s + \frac{kT}{q} \ln \frac{I_C}{z BT^n} \quad (4)$$

the base-emitter voltage at a reference temperature and collector current and solve for the lumped process parameters ($B$). Inserting the lumped parameters back into the original $V_{BE}$ equation and simplifying yields the desired result (equation (5)).

$$V_{BE} = (E_{ge} - \Delta E_{E,Ge}(0)) - \frac{T}{T_R} \left[ \frac{kT_R}{q} \ln \frac{I_C}{I_{CR}} + m \frac{kT_R}{q} \ln \frac{T}{T_R} \right] + \frac{kT_R}{q} \ln \frac{1 - \exp(-\Delta E_{E,Ge}(grade) / kT)}{1 - \exp(-\Delta E_{E,Ge}(grade) / kT)} \quad (5)$$

The effects of Ge on the base-emitter voltage of the transistor can be gleaned directly from this more generalized result. Observe that the effective bandgap at the emitter-base junction is simply the silicon result with bandgap narrowing due to doping, $E_{ge}$, minus the bandgap reduction due to the amount of Ge at the junction, $\Delta E_{E,Ge}(0)$. In addition, the shape of $V_{BE}$ on temperature is changed due to the addition of Ge, as is apparent in the last two terms of the equation. The ratio $T/T_R$ enhances this effect. For temperatures near the reference temperature, the last two terms in the equation have little effect on $V_{BE}(I_C, T)$, though as temperature decreases these effects can become more pronounced. This result will later be used to compare the measurements of Si and SiGe devices.

3. RESULTS AND DISCUSSION

The SiGe HBT’s measured were fabricated using a self-aligned, deep- and shallow- trench isolated, polysilicon emitter technology (Fig. 2) [9]. The graded SiGe base was deposited using the UHV/CVD technique. Devices were wirebonded in 68-pin LCC packages and cooled in a
cryostat system to ensure accurate thermometry. The base-emitter voltage at $I_C = 10, 50,$ and $100$ $\mu A$ for bandgap core transistors ($A = 8$) from two different SiGe HBT profiles (Fig. 3) and one Si BJT control were measured to six decimal place accuracy over a temperature range of -55 to 85 °C. Gummel plots were measured at each temperature to ensure proper device operation. A representative Gummel plot for a 1.0x2.5x8 $\mu m^2$ device at 218 K and 358 K is shown in Fig. 4, and the collector currents across temperature of a typical SiGe HBT are shown in Fig. 5, with the three bias points indicated. The $V_{BE}$ data was collected by an HP 4155 using a time-sampled measurement and is shown in Fig. 6 for the three profiles at 10 $\mu A$ and 100 $\mu A$.

Because proper design of BGR circuits requires extremely accurate knowledge of $V_{BE} (I_C, T)$, we might naively expect them to be sensitive to the above-described Ge-ramp effect. One of our goals was to see if SPICE can accurately model $V_{BE} (I_C, T)$ in SiGe HBT's. To test this, we fit the BGR design equation (1) to the data to determine the values of $E_g$, and $m$. In this...
analysis of equation (1), $E_{ge}$ is the average base bandgap in the presence of doping and Ge-induced bandgap narrowing, and $T_R$, $I_{CR}$, and $V_{BER}$ are the reference values of the respective parameters (300 K, 50 μA, $V_{BE}$ ($I_{CR}, T_R$)). Of interest is the inferred value of the EG ($E_{ge}$) SPICE parameter that controls the slope and intercept of $V_{BE}$ ($T$) (see Fig. 6) and the XTI ($m$, saturation current temperature exponent) parameter that controls the deviation of $V_{BE}$ ($T$) from linearity (Fig. 7). The values of $E_{ge}$ determined from this experiment appear in Fig. 6, where the value of this parameter for both the Ge profiles is lower than that of the Si BJT, as expected. The deviation from linearity is determined by drawing a line between the $V_{BE}$'s at the temperature extremes and then subtracting this line from the actual $V_{BE}$ data. The values of $m$ and the deviation from linearity that result are also shown in Fig. 7.

![Fig. 6. Base-emitter voltages over temperature for Si and SiGe devices with emitter areas of 1.0x2.5x8 μm².](image)

![Fig. 7. Deviation from linearity for Si and SiGe $V_{BE}$ data of Fig. 6.](image)

The matching BGR core transistor whose area is 1.0x2.5 μm² was also measured in this investigation, although the results are not shown. Since the area of this device is 8 times smaller than the other core transistor, the $V_{BE}$ as in Fig. 6 is naturally higher. However, the shape of $V_{BE}$ over $T$ was identical for the range of temperatures measured, a result which bodes well for the operation of BGR circuits between -55 to 85 °C. However, it is unclear what happens to the shape of this curve when the temperature is lowered to 77 K. It is evident that over the -55 to 85 °C temperature range there is no significant difference between the Si and SiGe devices in the shape of the $V_{BE}$ curve, though the effective bandgap is naturally reduced by the addition of Ge to the base.

We performed SCORPIO simulations [10] using calibrated profiles as well as a hypothetical Ge profile of larger grading to further test these conclusions down to 77 K. Fig. 8 corroborates our measurements in that there is little difference in the shape of VBE on T for the three different profiles in the -55 to 85 °C temperature range. However, once the simulation range was extended down to 77 K, it can be seen in Fig. 9 that the difference between the three profiles becomes significant. Therefore, the operation of SiGe precision analog circuits below the -55 to 85 °C temperature range, especially near 77 K, is expected to be affected by these Ge ramp effects.
4. CONCLUSION

This analysis of the temperature and bias dependence of the base-emitter voltage of SiGe HBT’s is key to assessing the operation of this technology in precision analog circuits. It does not appear that the Ge grading in the base of these HBT’s will have a detrimental effect on the operation of BGR circuits over the temperature range of -55 to 85 °C, though these effects do become more pronounced with cooling. More investigation into the operation of BGR circuits using SiGe HBT’s is needed, as well as experimental results in the 77 K range, and these are presently underway.

Acknowledgments - The authors would like to thank B. Gilbert, P. Brokaw, D. Buss, and T. Tewksbury of Analog Devices and C. Ellis of Auburn University for their contributions to this work, and IBM Microelectronics for wafer fabrication. We are also grateful to the SiGe research group at Auburn: W. Ansley, D. Richey, S. Jayanarayanan, S. Mathew, J. Roldan, and G. Bradford. This work was supported by a National Science Foundation Graduate Research Fellowship and the Office of Naval Research under grant # N00014-94-0690.

References