MICROENGINEERING - THE NEW APPLICATION OF CHEMICAL VAPOR DEPOSITION

P. Grabiec, J. Lysko

To cite this version:

P. Grabiec, J. Lysko. MICROENGINEERING - THE NEW APPLICATION OF CHEMICAL VAPOR DEPOSITION. Journal de Physique IV Colloque, 1991, 02 (C2), pp.C2-897-C2-901. <10.1051/jp4:19912107>. <jpa-00249785>

HAL Id: jpa-00249785
https://hal.archives-ouvertes.fr/jpa-00249785
Submitted on 1 Jan 1991

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L’archive ouverte pluridisciplinaire HAL, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d’enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.
MICROENGINEERING - THE NEW APPLICATION OF CHEMICAL VAPOR DEPOSITION

P.B. GRABIEC and J.M. LYSKO

Inst. of Electron Technology, al. Lotnikow 32/46, 02-668 Warsaw, Poland

Abstract - Techniques of Chemical Vapour Deposition, developed primarily for VLSI Circuits fabrication, enriched by the experience of CVD methods utilized in the field of hard wear resistant coatings and other mechanical related application, are the versatile tools enabling creation of complex, 3-dimensional microstructures required for development of micromechanical devices - sensors and actuators. In this paper the application of CVD methods for fabrication of micromechanic and other transducers technologically microelectronic compatible is discussed. An example of capacitive pressure sensor which can be integrated with CMOS devices is presented.

1. Introduction.
Up to now transducers have been mainly fabricated by the use of conventional fine mechanics. This is a limiting factor for the further system integration with respect to miniaturization, weight, power consumption, performance and price.
Micromechanics is a discipline which evolved out of the advanced integrated circuits manufacturing. While the standard IC technology solely exploits the electrical properties of silicon, dielectrics and metals, micromechanics introduces a mechanical element into the device performance. In addition, other physical and chemical properties of materials can be utilized to develop sensor structures. All the well known advantages of the silicon technology, such as precision, reliability, repeatability, miniaturization, high productivity and cost effectiveness apply also here.

2. Micromechanical materials and processing.
The basic material applied in silicon micromechanics is of course silicon itself. Mechanical properties of monocrystalline silicon are surprisingly good as compared to that of typical construction material - stainless steel (see table below). Also, materials widely used in VLSI IC processing such as SiO₂, doped SiO₂, Si₃N₄, Al, W, Mo can be utilized as well. Due to their advantageous mechanical properties, some hard wearing resistant materials as TiC, SiC or diamond are also considered.
The creation of micromechanical devices consist of various steps, the most important of which is a sequence of thin layer deposition, fine lithography patterning and etching. The key technological issue is the proper choice of the thin layer material and etching method, to ensure required etching selectivity, with regard to different components of the device structure, necessary to create such complex 3-D structures. An important advantage of Chemical Vapour Deposition as compared with other methods is the variety of materials which could be deposited using this technique and the ready control of the properties of the deposited material by means of doping during deposition. In this way, depositing materials etchable and etch resistant in appropriate sequence, and using standard photolithography patterning methods bridges, membranes, springs, cantilevers and many other complex three dimensional structures can be created. An important advantage of many CVD techniques, especially plasma assisted, is low process temperature resulting in thermal induced stress reduction. Below, is a list of materials which can be deposited by means of CVD methods.

<table>
<thead>
<tr>
<th>Material</th>
<th>Yield Strength dyne/cm$^2$ * 10$^{10}$</th>
<th>Knoop Hardness kg/mm$^2$</th>
<th>Young’s modulus dyne/cm$^2$ * 10$^{12}$</th>
<th>Density g/cm$^3$</th>
</tr>
</thead>
<tbody>
<tr>
<td>diamond</td>
<td>53</td>
<td>7000</td>
<td>10.35</td>
<td>3.5</td>
</tr>
<tr>
<td>SiC</td>
<td>21</td>
<td>2480</td>
<td>7.0</td>
<td>3.2</td>
</tr>
<tr>
<td>Si$_3$N$_4$</td>
<td>14</td>
<td>3486</td>
<td>3.85</td>
<td>3.1</td>
</tr>
<tr>
<td>SiO$_2$</td>
<td>8.4</td>
<td>820</td>
<td>0.73</td>
<td>2.5</td>
</tr>
<tr>
<td>Si</td>
<td>7.0</td>
<td>850</td>
<td>1.9</td>
<td>2.3</td>
</tr>
<tr>
<td>W</td>
<td>4.0</td>
<td>485</td>
<td>4.1</td>
<td>19.3</td>
</tr>
<tr>
<td>St.St.</td>
<td>2.1</td>
<td>660</td>
<td>2.0</td>
<td>7.9</td>
</tr>
<tr>
<td>Al</td>
<td>0.17</td>
<td>130</td>
<td>0.70</td>
<td>2.7</td>
</tr>
</tbody>
</table>

The table above summarizes the mechanical properties of some micromechanics materials.
In spite of the variety of the deposition processes available, those developed for integrated circuits technology are utilized mainly in micromechanics. This is due to the fact that for these materials suitable etching methods have been mastered, ensuring the necessary precision, selectivity and, if necessary, anisotropy. A typical selection is: epitaxial silicon layers doped with phosphorus or with boron, LPCVD or FECVD deposited silicon nitride and polysilicon layers and boron and/or phosphorous doped SiO$_2$ glass deposited using a variety of CVD methods. The epitaxial silicon layers are utilized to produce thin membranes by electrochemical etching which stops at the epitaxial pn-junction. A similar effect could be achieved by chemical etching in ethylenediamine (NH$_2$(CH$_2$)$_2$NH$_2$) - water - pyrocatechol (C$_6$H$_4$(OH)$_2$) - pyrazine (C$_4$H$_7$N$_2$) mixture which show significant reduction of heavily B-doped silicon etching rate. Both Si$_3$N$_4$ and SiO$_2$ are used as dielectric or passivation layers or as the construction elements of a device structure. The 3-dimensional structuring of these materials could be achieved due to the good selectivity of etching in a typical nitride etchant - H$_3$PO$_4$ as well as in HF based SiO$_2$ etchants. In the case of both SiO$_2$ and poly-Si layers doping with boron or phosphorous could be used to control the etching rate [4]. It should be noted, that to develop the design and technology of micromechanical device a good knowledge of available material and processing features and performance is necessary, so that some engineering tricks could be used to achieve the desirable goal. An example would be a poor penetration of the reactants, resulting in poor conformity of atmospheric pressure deposited SiO$_2$ or PSG layers. This phenomenon can be utilized to clog a cavity, created by selective etching of PSG placed beneath silicon nitride or polysilicon layer.
3. A CVD based capacitive silicon pressure sensor [5]

Silicon capacitive pressure sensors have been well known for at least 10 years [6,7]. However, the constructions are usually based on difficult two-side wafer etching, silicon to glass plate fusion sealing etc. Besides, integration of such process with IC processing is difficult. Use of CVD methods allows the design of a pressure sensor avoiding such problems.

Fig. 1 presents cross-section of a capacitive pressure sensor. An immovable capacitor electrode (2) and the conductive path (3) are formed in the p-type silicon substrate (1) by the phosphorous doping. These elements are contacted with the bonding pad (4) through the contact hole (5) in the phosphosilicate glass PSG (6). A movable capacitor electrode (7) and the conductive path (8) are formed by an aluminum layer (doped polysilicon was applied in some versions as well). The electrode (7) is deposited on the thin silicon nitride diaphragm (9) over the diffusion region. The electrode (7) is connected with the second bonding pad. The diaphragm (9) consist of the two LPCVD silicon nitride layers (10) (11). Its edges are stiffened by the thick polysilicon frame (12). The cavity (13) of the sensor is closed between the silicon substrate (1) the diaphragm (9) and, in the penetration region (14), by the PSG layer (6). A penetration region is a hole in the polysilicon layer (12) and silicon nitride layers (10), (11) - it is used to remove sacrificial PSG layer from the cavity (13) during the fabrication sequence. The shape and the dimensions of the cavity are almost the same as for the sacrificial PSG layer. The penetration region (14) is located at the same side of the substrate (1) as the diaphragm. To provide some kind of mechanical protection, the whole capacitor with diaphragm is located in a 1.5μm deep trench in the substrate.

The whole sensor fabrication is based on one-sided wafer processing as in common integrated circuits technologies. The formation of sensor cavity is based on the coincidence of a two phenomena, the first, good selectivity of PSG and silicon nitride etching in diluted HF, and the second, APCVD poor deposition kinetics inside cavities. For typical cell dimensions (electrode area 50×50μm, separation 0.5μm) the maximum capacitance change is 41 fF to 590 fF.
Fig.1. Cross-section of a capacitive pressure sensor.

4. Grabiec P.B., Pietruszko S.M., ibid, p. C5-585
5. Lysko J.M., Stolarski E., Jachowicz R.S., Capacitive Silicon Pressure Sensor Based on the One-Side Wafer Processing, will be presented on TRANSDUCERS'91 Conf., San Francisco 1991