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CHEMICAL VAPOR DEPOSITION OF COPPER FOR MICROELECTRONIC DEVICES BASED ON SILICON

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Abstract: In this paper we present a study of copper CVD deposition on different types of substrates used for microelectronic devices. The influence of substrate temperature, pressure, flux and contamination effect of the gas phase has been determined to obtain the deposition conditions of the metallic films on substrate as clean Si and CrN. Copper acetylacetonate has been used as precursor. The CVD reactor is coupled to an ultra high vacuum chamber with Auger electrons spectroscopy which allows in situ surface characterizations. The chemical composition of the film has been measured for different deposition conditions. These surface techniques have been used with X-ray diffraction to characterize the deposited layers. In the case of silicon substrate the deposited film can either be a pure metal or a metal rich silicide near Cu₄Si at low or high substrate temperatures respectively. The formation of the copper silicide is compared to the case of Si/Cu junctions where the Cu layer is deposited by electron gun evaporation. Selectivity phenomena have also been observed: Cu growth rate is higher on metallic CrN substrate than on PPQ insulator.

1) Introduction:

Copper is the material that could replace Aluminum for interconnexion in ULSI technology. Numerous papers has been published on Cu CVD processes during these last few years, most of them are in the superconductor field and few on the metallization for microelectronic (ref.1-8). Open questions still remain that need to be solved for applications in device fabrication. The selectivity and the conformal deposition (ref.9-14) could lead to a drop of device fabrication cost. CVD reactions are very complex and the steps of deposition mechanism are not well known. Systematic studies of the influence of the process conditions on film quality and on the growth rate versus the chemical properties of the substrate can be helpful for a better understanding of the CVD deposition mechanisms. The characteristics of the initial surface play an important role in the formation of the first layers, and must be taken into account to have a better understanding of the selectivity. The originality of our approach comes from the capability of performing a chemical characterization before and after in situ deposition. In this study we have used the copper acetylacetonate which is known to decompose at quite low temperature (300°C) and provide a metallic copper film. We will focus on the results obtained on Si(111) where, depending on the process, different compositions can be reached and we will present a case where the selectivity is observed.

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2) Experimental set up:

This work has been performed in the experimental set up schematically represented in figure 1. The two stainless steel chambers have been designed so that a grown film in the reactor can be transferred to the analysis chamber for surface characterization. The one inch wafer is loaded on a copper heater station, the thermal contact is obtained by spring clips. In this chamber the temperature of the sample can be monitored between the ambient and 500°C by a controller allowing different rampings and annealing times. The reproducibility of the sample temperature checked by an infrared pyrometer is better than 5°C. The temperature of the wall (T_W) and of the container (T_C) are measured by a thermocouple but not regulated. In the reactor, the base pressure in the 10^{-5} Pa range is obtained by a turbo pump associated to a stable chemical roughing pump. The pressure in the reactor is measured by an ion gauge from 10^{-7} to 0.1 Pa range and by a thermocouple gauge in the 0.1 to 10 Pa range: to get the desired pressure in the reactor the pumping speed is limited by adjusting the gate valve between the reactor and the turbo pump while the temperature T_C and T_W are kept constant. It is obvious that changing the pressure in the reactor modify both the flux and the molecule resident time in the hot region.

A gas ramp allows a controlled flux of oxygen in the reactor. From the reactor the wafer can be transported on a magnetic rod to the manipulator in the analysis chamber. This UHV chamber is equipped with an Auger spectrometer and thermal annealing capabilities. Post thermal processing up to 650°C can be done under UHV conditions. The chemical surface composition can be deduced from the energy distribution of Auger electrons emitted by the 2 keV electron irradiated surface. We must keep in mind that Auger spectroscopy is very sensitive to the surface. The probed depth is less than 3 nm for electrons around 100 eV due to the limited free path of electrons in matter.

![Figure 1: Experimental Set up.](Image)
3) Experimental process:

All the substrates investigated are based on silicon (111) or (100). Before processing the one inch wafers are pretreated to obtain a clean and defect free surface. The silicon substrate is loaded after HF buffer etch dip and without being rinsed. This procedure is known to strip the silicon oxide and leaves a clean silicon surface. This surface is hydrogen saturated and passivated (ref.15). This is confirmed by the Auger spectra of figure n^22 corresponding to the as introduced surface (2a) and to a 600°C 10min. in situ annealing (2b). It shows the typical silicon LVV line at 92eV and no oxygen signature around 510eV. The peak around 272eV is due to contamination by hydrocarbons which can be mostly desorbed by a short annealing(15min.) at 350°C. We can assume that this cleaning procedure (HF dip and annealing) leads in our conditions to a silicon surface with less than a monolayer of carbon and no oxygen.

The Polyphenylquinoxaline (PPQ) samples are obtained by spin coating on silicon (100) covered by an adhesion layer of CrN. Openings can be done through this insulator layer by local thermal decomposition induced by a 2.5nm copper film. Copper deposited on PPQ through a microscope grid allows the design of a pattern: By annealing at 450°C during 1h in oxygen the 1μm thick insulator film is only decomposed underneath the copper film (ref 16).

The results presented in this paper have been obtained by using the copper acetylacetone as the precursor. The source of precursor is a pyrex container connected to the reactor which can be heated by a quartz lamp from ambient to 200°C.
4) Experimental results and discussion:

We have tried different processing conditions and we will report those which gave the most interesting results. The investigated parameters were the substrate temperature, the wall temperature, the flux and contamination of the gas phase and the pressure. As a first result we notice that the leak level of the reactor is an important parameter and can influence the reproducibility of our results. Before beginning a process the pressure in the vessel is checked and must reach the low $10^{-5}$ Pa to be sure that no contamination by leaks could change the gas phase in the reactor. For clean silicon surface we observed that the growth rate in the absence of oxygen, is so low that such CVD process cannot be used for practical applications. To obtain measurable growth rate a controlled amount of oxygen is needed in the gas phase.

The results presented below have been obtained on silicon (111) substrates using different processing conditions.

Our last results with Si(100) substrate are similar to those with Si (111). The corresponding Auger spectra are given on figure no3 where the carbon peak to peak height has been normalized to the same value for each spectrum; a direct comparison can be done in these conditions. On this figure the label (a,b,...) correspond to the process (a,b,c,...) described in table 1. All the substrate were cleaned before loading as described before. When the walls were intentionally over heated at 150°C, the grown film is dominated by carbon. On the (3a) Auger spectrum only the 272eV peak is important no silicon is detected and a small contributions of copper and oxygen are detected at 58eV and 510eV respectively. This carbon film thickness is over the depth escape of electrons which means that the silicon is covered by at least more than 5nm of carbon.

On sample (b) the typical copper Auger line is well pronounced around 58eV on spectrum 3b. As previously discussed the film thickness is over 5nm since the silicon Auger line is not detectable. Carbon and oxygen are also well pronounced and remained

<table>
<thead>
<tr>
<th>RUN</th>
<th>SUBSTRATE</th>
<th>Tw (°C)</th>
<th>Tc (°C)</th>
<th>Ts (°C)</th>
<th>Pressure (Torr)</th>
<th>$O_2$ Flow (sccm)</th>
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<tbody>
<tr>
<td>(a)</td>
<td>Si(111) P</td>
<td>150</td>
<td>120</td>
<td>325</td>
<td>1,00E-01</td>
<td>1,6</td>
</tr>
<tr>
<td>(b)</td>
<td>Si(111) P</td>
<td>150</td>
<td>120</td>
<td>350</td>
<td>1,00E-01</td>
<td>1</td>
</tr>
<tr>
<td>(c)</td>
<td>Si(111) P</td>
<td>100</td>
<td>120</td>
<td>390</td>
<td>1,00E-01</td>
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<td>(d)</td>
<td>Si(111) P</td>
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<td>(e)</td>
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<td>(f)</td>
<td>PPQ + NOCr</td>
<td>100</td>
<td>120</td>
<td>350</td>
<td>1,00E-01</td>
<td>1</td>
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</table>

Table 1: Process conditions
even after annealing at 350°C. Annealings at higher temperature have not been investigated since copper is expected to induce complex reactions with silicon and oxygen leading to the formation of silicide. This contamination of the film by carbon is not due to the hydrocarbons produced by the reaction and redorsorbed on the copper surface during cooling down in the reactor ambient. Hydrocarbons are expected to desorb at low temperature. Preliminary sputtering experiments on CVD copper film have shown that the carbon is mostly accumulated at the surface of the film. At the present time we have not determined the copper profile within the whole layer.

At higher substrate temperature (T=390°C) (run c), the composition of the deposited film is quite different. Surface analysis gives the (3c) Auger spectrum where a silicon and a copper peaks are both detected at 92eV and 58eV respectively. This spectrum can easily be compared to the one obtained in the case of copper silicide (ref.17-18). It is well known that with evaporated layer copper react with clean silicon at temperature over 200°C to form metal rich silicide (Cu$_3$Si, Cu$_4$Si). The peaks at 21.8° and 25.35 on the diffraction patterns (figure no4 c) do not correspond to pure copper. They can be assign to a rich silicide compound. Both Auger spectroscopy and X-ray diffraction suggest the reaction of copper with silicon atoms. One can notice that carbon and oxygen are still observed in the film.

To obtain pure metallic films on silicon it is not possible to increase the temperature of deposition process since it induces the reaction of copper with the silicon underneath. Spectrum (3d) corresponds to a film grown at lower temperature (300°C), but with a higher oxygen rate in the gas phase. It shows a typical Auger metallic copper peak at 58eV. To
fully realize the differences between the previous cases this zone has been enlarged in figure n°5. On the (5b) spectrum the peak shape let think that the copper is in an oxidized state (ref.19). One can see that the M23VV Auger copper line at 58eV on the (5d) spectrum is split. The observation of this splitting is related to the metallic state of copper. The oxygen peak intensity is the lower of this series and confirm the metallic behavior of the film. X-ray diffraction gives an other proof of this point: the diffraction pattern (figure n°4 d) is similar to the one obtained on evaporated pure copper film. We emphasize the fact that in the case of a CVD copper film the reaction to form a silicide occurs at higher temperature that in the case of evaporated copper. This is an interesting point that needs to be clarified. A greater stability of the CVD copper film is important for devices fabrication. Copper is known to have a strong reaction with silicon and to diffuse very easily in it.

Concerning the selectivity of Cu CVD, the SEM picture (fig N° 6) has been obtained on a PPQ sample prepared as described above. The process conditions correspond to the f run of table 1. The sample temperature 350°C is compatible with the thermal stability of PPQ. This picture shows that the uncovered copper zone appears in dark while the bright one corresponds to the CVD copper film. It is clear from such picture that copper has been deposited selectively. By X-ray-fluorescence microanalysis, the amount of copper is five time less in the dark area than in the bright area. The copper has been deposited selectively on the metallic surface. It does not seem that the predeposited 2.5nm of copper influences the selectivity behavior. Other experiments where the PPQ has been removed by scribbing, lead in the same process conditions to a macroscopic copper film deposited only on the CrN layer. The mechanism that can explain the selectivity is certainly complex and further experiments need to be done to determine the sensitivity of the differential growth rate to the process parameters. It can be expected that the change of the process conditions can lead to a better selectivity.

Figure N°6

SEM picture of selective CVD copper film. Dark area correspond to PPQ while bright area is the copper film.
6) Conclusion:

From the study of the influence of the process conditions, we have determined CVD processes allowing a control of the chemical composition of the film deposited on silicon. Metallic copper films are formed at a temperature (300°C) compatible with processing silicon devices. The formation of CVD copper silicide occurs at higher temperature than in the case of evaporated copper. Further experiments need to be done to understand the increase in the stability of the Cu/Si interface. Oxygen and carbon could induce this peculiar behavior of copper in CVD films. An interesting result is the selectivity obtained on a real microelectronic interconnection problem where one needs to deposit a copper film in openings through PPQ. The growth rate differences could be explained by a change in the interaction of species with the surface.

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REFERENCES

/6/ Beach-DB Legoues-FK Hu-CK Chem. Mater. 2 (1990) 217
/8/ Houle-F-A Jones-C-R Baum-T Pico-C Kovac-C-A Appl.Phys.Lett. 46 1985 204
/19/ Benndorf-C Egert-B Keller-G Tieme-F Surf.Sci. 74 (1978) 216