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Misfit accommodation and dislocations in heteroepitaxial semiconductor layers: II-VI compounds on GaAs

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Résumé. — Nous proposons un mécanisme de germination et de développement des dislocations permettant de compenser l’écart de paramètres d’une couche epitaxiée sur un substrat, par exemple dans notre cas, un composé II-VI sur GaAs. On porte une attention particulière aux dislocations résiduelles dans la couche dont on cherche à minimiser la densité.

Abstract. — We suggest a model for the nucleation and expansion of dislocations which accommodate the parameter misfit of an epitaxial layer on a substrate, applied, in this work, to a II-VI compound on GaAs. We examine in particular the dislocations threading through the layer, which must be kept as low as possible in density.

1. Introduction.

A large number of investigations have been performed on the properties of III-V semiconductor layers in epitaxy on a substrate. A system which has received a great deal of attention in GaAs on Si [1, 2] expecting to take advantage of the performance of silicon single crystals, in spite of the large parameter misfit ($\Delta a/a = 4\%$).

The II-VI semiconductors are attractive because they can be used in opto-electronic devices, for wavelengths which do not correspond to any III-V compounds. For instance, (CdHg)Te permits infra-red detection, for wavelengths up to 10 $\mu$m by adjusting the mercury concentration [3].

Attempts have been made to prepare good quality (CdHg)Te epitaxial layers by MOCVD. In order to spread the lattice mismatch between the layers and the GaAs substrates, intermediate layers are grown, viz. ZnTe on GaAs ($\Delta a/a = 8\%$) followed by alternative CdTe and HgTe growth aiming at achieving a Cd/Hg ratio by interdiffusion. The two compounds have almost the same parameter (Tab. I) which in 6% larger than the one of ZnTe [4].

We have examined the microstructure of such epitaxial layers, mainly using transmission electron microscopy (TEM). A prominent feature is the presence of large dislocation densities from the interfaces to the surface of the layers. In this paper, we present a model which allows us to describe the formation of dislocations which accommodate the lattice mismatch and to explain the presence of excess dislocations in the epitaxial layers. These considerations may
Table I. — Principal parameters of semiconductor compounds involved in the present study.

<table>
<thead>
<tr>
<th></th>
<th>GaAs</th>
<th>ZnTe</th>
<th>CdTe</th>
<th>HgTe</th>
<th>References</th>
</tr>
</thead>
<tbody>
<tr>
<td>( a ) (nm) lattice parameters</td>
<td>0.565</td>
<td>0.6104</td>
<td>0.6481</td>
<td>0.6461</td>
<td>[12]</td>
</tr>
<tr>
<td>( \Delta a / a = 2(a_1 - a_2) / (a_1 + a_2) )</td>
<td>( \leftarrow 7.7 % \rightarrow )</td>
<td>( \leftarrow 6 % \rightarrow )</td>
<td>( \leftarrow 0.3 % \rightarrow )</td>
<td>( \leftarrow 13.7 % \rightarrow )</td>
<td></td>
</tr>
<tr>
<td>elastic constants ( 10^{10} ) Pa</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>[13]</td>
</tr>
<tr>
<td>( C_{11} )</td>
<td>11.92</td>
<td>6.99</td>
<td>5.25</td>
<td>5.37</td>
<td></td>
</tr>
<tr>
<td>( C_{12} )</td>
<td>5.94</td>
<td>3.99</td>
<td>3.61</td>
<td>3.74</td>
<td></td>
</tr>
<tr>
<td>( C_{44} )</td>
<td>5.38</td>
<td>3.06</td>
<td>1.95</td>
<td>2.01</td>
<td></td>
</tr>
<tr>
<td>thermal expansion ( 10^{-6} ) K(^{-1} )</td>
<td>6</td>
<td>8.4</td>
<td>4.9</td>
<td>5.1</td>
<td>[12], [13]</td>
</tr>
<tr>
<td>slip system dissociation</td>
<td>( 1/2 \langle 1\bar{1}0 \rangle ) ( {111 } )</td>
<td>( 1/2 \langle 1\bar{1}0 \rangle \rightarrow 1/6 \langle 2\bar{1}1 \rangle + 1/6 \langle 1\bar{2}\bar{1} \rangle )</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>stacking fault energy ( \text{mJ.m}^{-2} )</td>
<td>20</td>
<td>14</td>
<td>9</td>
<td></td>
<td>[12], [14]</td>
</tr>
</tbody>
</table>

apply to a number of situations where heteroepitaxial semiconductor layers are grown on a perfect substrate with large lattice mismatch.

2. Experimental conditions.

We have gathered in table I the main parameters of the materials under investigation. The epitaxial layers are prepared by MOCVD at temperatures of 350-400 °C [5]. The difference in thermal expansion can create some stress during cooling to room temperature, the layers being grown stress free once the lattice mismatch is relaxed. One calculates from elasticity theory that cooling implies a shear stress of 2.7 MPa, which is opposite to misfit stress. Such stress value is too low to induce dislocation motion and multiplication in the II-VI semiconductor layer.

For transmission electron microscopy (TEM), two kinds of foils were prepared. Plan view observations were performed with the foil parallel to the \{100\} ZnTe/GaAs interface. The materials are mechanically polished, using a « dimpler » on the GaAs sides, until the interface is reached. Further thinning is obtained by chemical etching until electron transparency. Ion thinning for plane views is avoided because of the important damage induced by this technique in II-VI semiconductors as formerly observed in CdTe [6].

Cross-section observations have also been performed. The surfaces of two ZnTe layers are glued together, with matching crystallographic orientations. Foils are then cut perpendicular to...
the glued interfaces and parallel to \{110\} planes. A dimple is obtained by mechanical polishing, before submitting the foils to ion thinning under low beam intensity to prevent the formation of damage. The variety of materials to be etched does not allow us to use chemical thinning.

TEM images were obtained either in a JEOL 100C machine or in a JEOL 2000FX operated respectively at 100 kV and 200 kV, the second permitting resolution of the double atomic columns along \langle110\rangle directions.

3. Different ways of misfit accommodation.

3.1 Elastic deformation of epitaxial layers. — The parameter of ZnTe is, almost, 8% larger than the one of GaAs (Tab. I). ZnTe epitaxial layers are therefore submitted to large compressive stresses; their value is of the order of one tenth of the elastic modulus for an homogeneously strained layer, assuming linear elasticity. We take the axis 1 and 2 in the interface; we write \( \varepsilon_1 = \varepsilon_2 = -0.08 \). The deformation along the axis 3 perpendicular to the interface can be written: \( \varepsilon_3 = -2 \left( \frac{C_{12}}{C_{11}} \right) \varepsilon_1 = 0.09 \). For the purpose of describing dislocation motion in the epitaxial layers, we will assume that it is submitted to a tensile stress \( \sigma_3 \) parallel to the axis 3, the other stresses being isostatic \( \sigma_1 = \sigma_2 = -\sigma_3 \), without influence on the motion of dislocation.

3.2 Dislocations in ZnTe/GaAs interfaces. — Dislocations in the interface are the most efficient way to relax the strain accumulated in the layer. If the lattice parameters are \( a_1 \) and \( a_2 \) for GaAs and ZnTe, respectively, the average distance \( \delta_0 \) between misfit dislocations corresponds to the shortest distance permitting the correspondence between atomic planes of epilayer and substrate. One finds: \( \delta_0 = d_2 \frac{a_1}{\Delta a} \) where \( \Delta a/a_1 \) is the misfit and \( d_2 \) is the repeat distance in the interface. For every distance \( \delta_0 \), there is a missing plane in ZnTe compared to GaAs, i.e. a dislocation along a line U ending the extra-plane with a Burgers vectors \( b \), such that its edge component in the interface plane \( b'' \) is equal to the lattice repeat distance \( d_2 \) in the earlier formula. Extra half planes and dislocations can be seen by a careful examination of figure 1 obtained by high resolution TEM. Since the observation was made along \langle110\rangle, the average distance between dislocations, which is expected (Tab. II), is given by

\[
\delta_0 = \frac{a_2}{\sqrt{2}} \frac{a_1}{\Delta a} = 5.4 \text{ nm}
\]

The experimental value is in good agreement.

The full relaxation of the ZnTe layer requires an other set of dislocations, which relieves the misfit in the perpendicular direction.

The dislocations observed in figure 1 belong to those which can be predicted from a crystallographic approach; their characteristics are given in table II for a (001) interface plane. Type \( \langle A \rangle \) dislocations have \( b \) parallel to the interface (Lomer dislocations), corresponding to the dominant observations (Fig. 1). These Lomer dislocations are sessile and therefore cannot be introduced directly by dislocation multiplication and slip. They are formed by reaction of mobile \( \langle B \rangle \) dislocations; this is suggested by TEM observations of threading dislocations (Fig. 2) which represent the ending of the interface dislocations, as sketched in figure 3 for the beginning of their development. A and B are the emergences with an extra half plane in GaAs and ZnTe below the dislocation line (Fig. 3). Strain relief is achieved in the upper part above the dislocation line (Figs. 3 and 4b).
Fig. 1. — High resolution TEM cross section of a ZnTe/GaAs interface. The mean spacing between Lomer misfit dislocations (shown by arrows) is about 5.2 nm.

Table II. — Types of dislocations which are able to relieve misfit strains: efficient components \( b'' \) of perfect \( b \) and partial \( b' \) Burgers vectors. They are given together with the corresponding average distance \( \delta_0 \) between misfit dislocations and their Schmid factor under the tensile stress \( \sigma_3 \) for slip in one of the four \( \{111\} \) planes.

<table>
<thead>
<tr>
<th>type</th>
<th>( b'' )</th>
<th>( \delta_0 ) (nm)</th>
<th>( s )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( b = \frac{1}{2} [\overline{1}10] )</td>
<td>( \langle A \rangle )</td>
<td>( b )</td>
<td>5.4</td>
</tr>
<tr>
<td>( b' = \frac{1}{6} [\overline{1}21] )</td>
<td>( \langle B \rangle )</td>
<td>( 0.28 , b' )</td>
<td>0.9</td>
</tr>
<tr>
<td>( b = \frac{1}{2} [0\overline{1}1] )</td>
<td>( \langle B \rangle )</td>
<td>( 0.5 , b )</td>
<td>2.7</td>
</tr>
<tr>
<td>( b' = \frac{1}{6} [\overline{1}12] )</td>
<td>( \langle B \rangle )</td>
<td>( 0.57 , b' )</td>
<td>1.8</td>
</tr>
</tbody>
</table>
Fig. 2. — Threading dislocations in a CdTe(2 μm)/ZnTe(0.1 μm)/GaAs{001} TEM cross section micrograph. The GaAs substrate on the left contains no defect.

Fig. 3. — Schematic representation of a dislocation threading from A and B to the interface, gliding in a \{111\} plane. \( b \) and \( \nu \) represent the Burgers vector and the glide direction of dislocations respectively. \( b'' \) is the edge component of the Burgers vector in the interface which relieves the compressive strain between the emergence points A and B of the dislocation.
Fig. 4. — Example of packing of vacancies nucleating the dislocation represented in figure 3, during deposition of ZnTe on GaAs. a) First layer on top of the substrate. b) Following layer. $b'$, $a$ Burgers vector component, represents $b$ or $b''$ for a $<A>$ or $<B>$ dislocation in formation.

A three dimensional dislocation network can relieve the misfit in epitaxial layers; however, it will be detrimental to electronic properties of the semiconductor.

3.3 ROLE OF POINT DEFECTS. — ZnTe having a lattice parameter greater than GaAs, vacancies can help to relax the epitaxial layer. Their formation is favoured in stressed materials, as well as by the chemical vapor deposition process which deposit atoms from a gaseous phase. Vacancy supersaturation is expected in the growing layer.

In a numerical simulation, Djafari et al. [7] have shown that when CdTe is grown on GaAs, about 10% of the sites remain vacant. In order to achieve total relief of the misfit, the vacancy concentration must be twice $\Delta a/a$, assuming vacancy and atomic volumes identical; for CdTe on GaAs, this would give 28% (Tab. I) well above the value of 10% found by simulation [7].

For ZnTe, the vacancy concentration would be 16%. There is no evidence that such large amounts of vacancies are present in growing epitaxial layers. However, these defects may partly relieve the misfit at the early stage of growth and play as essential role in the nucleation of dislocations.

Indeed, let us consider an isolated vacancy in the first layer (Fig. 4a). A configuration such as the one of figure 4b is very likely for the second layer, because it is the most efficient to relieve the misfit. After several planes, a dislocation of Burgers vector $b$ (Fig. 4b) is created. In this example the missing plane would create a type $<A>$ dislocation (Tab. II) which cannot expand under the stress $\sigma_3$ and does not bring further misfit relief.

Vacancy clustering can also create type $<B>$ dislocations (Fig. 3) and partial dislocations (Tab. II). They are glissile and can move in order to decrease the total energy of the system. The observed $<A>$ sessile dislocations (Fig. 1) are certainly due to a combination of $<B>$ dislocations gliding in two symmetric $\{\bar{1}1\bar{1}\}$ planes inclined $55^\circ$ at the interface.


The first atomic layers of a growing film are strained to match the substrate, with a small fraction of relief due to point defects, which are, most probably, clustered because of their large concentration (see Sect. 3). For a critical thickness, dislocations appear and contribute to strain relief. There are many calculations of the critical thickness in the literature, based on various criteria [9].

An estimate is obtained by writing that the energy $W_b$ of the homogeneously strained film is equal to that of the dislocation network ($W_d$) at the critical thickness $h = h_c$. 
For the unit surface, classical results are:

\[ W_h = 2 \mu \frac{1 + \nu}{1 - \nu} h \left( \frac{\Delta a}{a} \right)^2 \]
\[ W_d = \frac{2}{\delta} \left( \frac{\mu b^2}{4\pi} \ln \frac{R}{b} + W_c \right) \]

where \( \mu \) is the elastic shear modulus, \( \nu \) is the Poisson's ratio, \( R \) is the smallest value between the film thickness \( h \) and half the average distance \( \delta \) between misfit dislocations for thick films, and \( W_c \approx 0.25 \mu b^2 \) is the dislocation core energy. A straightforward calculation gives for ZnTe on GaAs with type \( \langle A \rangle \) dislocations:

\[ h_c \approx 0.13 \frac{b}{\Delta a/a} = 1.7 b. \]

For \( b = a \sqrt{2} = 0.42 \) nm we find \( h_c = 0.7 \) nm. This corresponds to a few atomic layers.

This result agrees with the one deduced from equation (23) in Ball and Van der Merwe [9] and with an extrapolation of the experimental data of Cibert et al. [10] for CdTe on (CdZn)Te. For \( 1\% < \Delta a/a < 6\% \) the experimental \( h_c \) varies as \( (\Delta a/a)^{-1.15} \) and our calculations give \( (\Delta a/a)^{-1} \). In spite of the assumption of linear elasticity for large deformations, the energetic models are adequate for the prediction of critical thicknesses.

The presence of dislocations implies that they can move for the thickness \( h_c \), being more likely to glide under the misfit stress \( \tau_c = s\sigma_3 \) (s = schmid factor, Tab. II). At the critical thickness, the work done on a moving dislocation segment nearly perpendicular to the interface, is of the order of \( \tau_c bh_c \) for unit length displacement. The interface dislocations energy is increased by \( W_d \).

By writing \( \tau_c bh_c = W_d \), one can deduce again:

\[ h_c \approx 0.13 \frac{b}{\Delta a/a} \]

The critical thickness values deduced from the two approaches are identical. This means that the conditions are gathered to relax the misfit by moving dislocation segments reaching the surface from the interface, point defect clusters (see Sect. 3.3) being good candidates for these dislocation nuclei.


In the previous sections, we have examined the driving forces and the critical conditions for the presence of dislocations in epitaxial layers. We now describe a few of the steps required to organize the dislocation microstructure formed while the layer is growing.

As soon as a dislocation length \( \ell \) is formed, the stress \( \sigma \) in the layer is reduced. We can write:

\[ \sigma = \sigma_0 (1 - \ell / \ell_0) \]

where \( \sigma_0 \) is the stress before the formation of dislocation, and \( \ell_0 \) the ideal length of dislocations per surface unit to accommodate the parameter mismatch i.e. \( \ell_0 = \frac{2}{\delta_0} \). The control of the regular dislocation spacing is a direct consequence of the previous equation. If locally the formation of dislocations has been faster than elsewhere, the stress becomes smaller, thus slowing the dislocation motion and giving time to the surrounding ones to reach
the same density. This explains why the misfit dislocation arrays in the interface are very regular (Fig. 1). We have suggested that interface dislocations are created by extension of segments of the type shown in figure 3. This implies an array of points where dislocations cuts the surface. This is displayed in figure 5, where the distances between two points range from 20 nm to 50 nm, at an early stage of the growth, for a 50 nm thick layer. By taking that two points correspond to a dislocation segment, one can calculate the average length of an interface dislocation in ZnTe on GaAs. For a dislocation density $2 \times 10^{11} \text{cm}^{-2}$ corresponding to a 20 nm distance, the total length of interface dislocations is about $4 \times 10^6 \text{cm/cm}^2$ which is distributed over segments of average length of 0.4 μm. The high resolution TEM image of figure 1 requires straight dislocations through the foil, a condition easily encountered with segments 0.4 μm long or more. In this case (Fig. 5) the nucleus density would be equal to the half dislocation density: $10^{11} \text{cm}^{-2}$.

Fig. 5. — TEM plane view of a 50 nm thick layer of ZnTe on GaAs with diffraction vector $\langle 220 \rangle$ in ZnTe and GaAs. Endings of moiré lines localize the emergences of dislocations (as A or B in Fig. 3). Keep in mind that fluctuations in the direction of moiré lines is much larger than the rotation of the layer crystal lattice.

If we assume that a volume of vacancies equal to a unit cell is necessary to create a dislocation nucleus, we can calculate the vacancy concentration in the layer of thickness $h_c$ and 1 cm$^2$ area. With $10^{11}$ cm$^2$ nucleus, the vacancy concentration can be calculated as $4 \times 10^{-4}$. This value is much larger that the one corresponding to thermodynamic equilibrium. But such supersaturation is likely to exist, as shown by theoretical calculation by Djafari [7] who gave a concentration of 0.1.

This numerical evaluation means that the high density of dislocations observed in the layers
can be formed directly from the nucleus associated to vacancy clusters. Multiplication of dislocations by conventional sources is not necessary to form the microstructure (Fig. 6).

The final target is to make CdTe or (CdHg)Te layer. The ZnTe layer is an intermediate which is grown to spread the misfit (Tab. I) over some distance, that is to distribute the internal stresses in a controlled manner: We expect that the dislocations created in ZnTe (Fig. 5) are used at the interface with CdTe to accommodate the 6 \% misfit (Tab. I). Indeed the dislocation density falls from $2 \times 10^{11} \text{ cm}^{-2}$ (Fig. 5) to $5 \times 10^9 \text{ cm}^{-2}$ in a 0.5 \(\mu\text{m}\) thick CdTe layer (Fig. 6). The trapping of threading dislocations at interfaces is a common observation in III-V compounds, where superlattices are currently introduced for that purpose [1, 2].

**Fig. 6.** — Dislocations in a layer of CdTe at a level of 0.5 \(\mu\text{m}\) above the interface CdTe/ZnTe. The dislocation density is about $5 \times 10^9 \text{ cm}^{-2}$ Some stacking faults, common in CdTe, are observed.

In spite of the low mismatch between CdTe and HgTe (Tab. I), dislocations can be bent at the interface between the two layers (Fig. 7). The (CdHg)Te layer is the result of the interdiffusion of CdTe/HgTe multi-layers, which create good internal stress conditions for dislocation annihilation. The densities at the surface are of the order of $10^4-10^5 \text{ cm}^{-2}$ However, other mechanisms for the annihilation of dislocations are operative, since densities lower than the one of figure 6 are observed in thick CdTe layers: if is five times lower at 2 \(\mu\text{m}\) from the ZnTe/CdTe interface than at 0.5 \(\mu\text{m}\) (Fig. 6). This suggests a recovery mechanism typical of II-VI semiconductors where dislocation mobilities are high at temperatures between 350-400 °C. This behaviour relates to the one observed in III-V semiconductors after additional annealing [1, 2].
6. Conclusions and summary.

We have focused our attention in this paper on one class of defects in CdTe/ZnTe grown on GaAs by OMCVD, i.e. dislocations which are present in high densities. Other types of defects will be described in further publications [11, 15].

We have suggested that the origin of dislocations can be associated with point defect clusters which are very likely to be abundant in view of the non-equilibrium growth conditions. The high mobility of dislocations allow these loops to expand as soon as the critical thickness is reached. These features are typical of II-VI semiconductors. The recovery processes are particularly efficient in these compounds where diffusivity is high [11].

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References