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Defect creation and development at the polysilicon-silicon interface during a cold technology simulation

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Abstract. A simulation of a cold technology (≤ 950 °C) has been analysed by X-ray topography at different stages of its elaboration. It is shown first that an elastic deformation occurs straight below the edges of the film pattern. Then, point defects are generated at the polysilicon-silicon interface upon oxidation. Finally the crystalline quality is perturbed in the whole bulk wafer upon further chemical and heat treatments.

1. Introduction.

The deposition of a thin film of polycrystalline silicon on the wafer back surface is a new method [1, 2] used to getter metals from silicon devices. This technique is compatible with most integrated circuit processing schemes and has the advantage that it does not induce deformation in the wafer. Accordingly, it is tempting to check whether this new gettering process improves the performances of solid state devices. As part of this ambitious project, we studied the creation and the development of defects in a silicon wafer at different stages of a process of cold technology (≤ 950 °C) performed by the LETI-CEA (Grenoble). The final goal is to correlate the defect evolution and density to lifetime measurements and optimize the physicochemical and heat treatments during device fabrication. In this paper, we only report our observation of defects by X-ray topography at different sequences of the simulation of the process, leaving to a more complete article the detailed presentation of the relationship between the structural and electrical properties of the manufactured devices.

2. Experimental.

The starting material is commercially available wafers with ~ 1 μm polycrystalline silicon deposited at the backside (Monsanto). A thin silicon oxide layer (~ 100 Å) called « pedestal » is first deposited on the wafer surfaces. The geometry of the sample is sketched in part I of figure 1. Then, about 700 Å of silicon nitride is deposited on both sides and a photolithographic process is used to fabricate Si₃N₄ + SiO₂.

Fig. 1. — Sketches of the different stages of the process simulation (see text). The thicknesses of the deposited materials are not at the right scale.

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« pedestal » patterns at the front side. After that, a wet oxidation is carried out and results in the formation of $\sim 4\,000\,\text{Å}$ of oxide on the non-protected parts of the surface. The obtained structure is represented in part II of figure 1. In the next step, $\text{Si}_3\text{N}_4 + \text{SiO}_2 \prec $ pedestal » film patterns are removed and a $\sim 300\,\text{Å}$ grid oxide is grown. This corresponds to the drawing III in figure 1. Then, polycrystalline silicon is deposited, annealed and partly oxidized. The final structure is sketched in IV.

At each stage I, II, III, IV, the silicon crystal quality is observed by X-ray topography. Figure 2 represents the principle of the recording. A fine, parallel X-ray beam probes a section of the wafer under a Bragg incidence. The diffracted image is recorded on a photographic plate. If a crystallographic defect $A$ is located along the path of the incident beam, the local variation of the Bragg condition induces a modification of the image contrast in $A'$. Hence, the topography of the crystal defects can be explored section after section by moving the crystal (and the plate) with respect to the incident beam. Furthermore, if a region of the sample is too strongly perturbed and yields too intense a scattering, the slot $S$ can be used to reduce the probed part of the section. This capability is illustrated in figures 3b and c. In that case, the film pattern at the front surface introduces a strong perturbation of the scattered beam (Fig. 3b). To obtain a better resolution in the topography of the Si crystal section, one can intercept the beam scattered from the front surface by acting on the slot. The obtained topogram is presented in figures 3c. Finally, if the X-ray incident beam sweeps continuously at the wafer surface, the images of the sections are superimposed on the photographic plate. As a result, one gets a projection of all the defects contained in the wafer. This is illustrated in the topographs of figures 3a.

3. Results.

The evolution of the defects in the wafers after the various treatments described in figure 1 is shown in the X-ray topographs presented in figure 3. Step I exhibits no defects within the instrumental resolution ($\sim 5\,\mu\text{m}$) as seen on both projection (Ia) and section (Ib). The good quality of the crystal is confirmed on the section by the enhancement of the diffracted intensity at the surfaces and by the occurrence of Pendellösung fringes.

On stage II, the deformation of the wafer by the $\text{Si}_3\text{N}_4 + \text{SiO}_2$ film pattern is visible in projection IIa. This deformation is located at the edge of the pattern [3]. Only the part of the stress for which the diffraction criterion $g, b$ is different from zero, are revealed in figure 3Ia. If the 220 reflection had been used instead of the 220 ones, another set of contrasts perpendicular to the preceding ones would have appeared. Except for these localized deformations, no crystalline defect is observed in the projection. Two section topographs are also presented (IIb, IIc). Figure 3Iib exhibits strong black contrasts associated to the edge of the pattern. As explained in section 2, one can improve the resolution of the section topogram straight below the pattern edges by adjusting the position of the exit slot. The resulting photograph is presented in figure 3IIc. Just below the pattern edges, a new fringe due to a homogeneous elastic strain is seen [4]. Outside this perturbed region, the silicon wafer keeps its crystalline perfection.

In sequence III, numerous « point defects » occur in the sample. They are clearly seen in the projection (Fig. 3IIIa). The location of these defects is determined by recording section topograms (IIIb, c). The plates show that they are created at the poly Si-Si interface and that they stay in the vicinity of the back-side. Moreover, strong black contrasts are still visible at the front-side below the edge patterns (IIIb). They conceal the elastic deformation at this spot. However, the local strain can be visualized by the technique described above (see Fig. 3IIIc). Nevertheless, the presence of Pendellösung fringes show that the bulk wafer keeps its crystalline quality.

In step IV, a degradation of long range order in the crystal is observed as indicated by the disappearance of the X-ray interference fringes in figures 3IVb, c.

4. Conclusion.

Several conclusions can be drawn from the above observations:

— An elastic strain is induced straight below the edges of the film pattern. The other parts of the wafer are not perturbed.

— No defects are created at the back-surface as long as a nitride layer protects polysilicon from oxidation. If the nitride film is removed, point defects develop at the poly Si-Si interface.

— Further physicochemical and heat treatments perturb the bulk silicon lattice.

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Fig. 3. — X-ray topograms corresponding to the states I, II, III, IV of the processing — 220 reflection — $\lambda$ K$\alpha_1$ Mo.
a: projection topograms; b, c: section topograms (F: front-side, B: back-side).

References