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INVESTIGATION OF THE INFLUENCE OF DX CENTERS ON HEMT OPERATION AT ROOM TEMPERATURE

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RESUHE - Une étude expérimentale et théorique des Centres DX dans les couches en AlGaAs est présentée. Il est montré qu'à cause des centres DX, à température ambiante, la transconductance hyperfréquence des HEMTs peut être considérablement plus élevée que celle obtenue en régime continu et approche les valeurs qui pourraient être obtenues si les centres DX n'existaient pas. En conséquence, pour des applications à température ambiante, des structures particulières destinées à éliminer les centres DX ne sont pas réellement nécessaires.

ABSTRACT - A study of DX centers in GaAlAs layers is presented, both experimental and theoretical. It is shown that due to DX centers, at room temperature, the microwave transconductance of HEMTs can be considerably higher than that obtained from the DC regime, and approaches the values which could be expected if DX centers did not exist. As a result, for room temperature applications, special structures designed in order to eliminate DX centers are not really necessary.

It is now well known that, due to deep levels associated with DX centers in doped AlxGa1-x As [1], the electrical properties of HEMTs may be strongly modified leading to low temperature collapse [2], persistent photoconductivity [3]. Solutions to this have been proposed: a reduction of Al mole fraction x below 0.2; the use of a n+ GaAs-AlAs superlattice [4]; or even a planar (delta)-doped layer [5]. It is clear that these solutions are necessary for low temperature operation, but are they really useful for room temperature operation? Here, we study the effect of DX centers on DC and microwave properties of HEMTs at room temperature. We use a HEMT simulator which is able to take DX centers into account [6] and we will support our findings by a comparison with a few experimental data.

Our theoretical study is carried out using a method which is somewhat similar to [6]. Since we are interested primarily in the influence of DX centers upon the drain current and device performance, we have to evaluate the importance of electron transfer between the GaAs and GaAlAs layers, requiring a correct evaluation of the gate current through the GaAlAs layer. To do this, we start with some 2 DEG density nG at some point of
the source-drain channel, from which the exact position of the Fermi level \( E_F(n_s) \) with respect to the conduction band is known. This \( E_F(n_s) \) was established from exact results computed with a self-consistent solution of Schrödinger's equation [6]. Using this as the boundary condition for the solution of drift-diffusion and Poisson equations, the Fermi level through the AlGaAs layer is calculated and several assumptions for the effect of DX centers on the electron concentration can be compared. Then, at a given gate voltage, the gate current density can be computed once the correct boundary condition is met at the gate plane. This method is somewhat similar to the one presented by Ponse et al [7]. Thus, we get the \( n_s \) and the total charge in the AlGaAs layer as a function of gate voltage. Finally, the drain current and gate-source capacitance are computed using a short channel approximation [8] by solving the following equations:

\[
I_{DS} = Z \left( V_{GaAs} + Q_{free} \right) \left( V_{GaAlAs} \right)
\]

\[
C_{gs} = Z L_g / dV_{gs}
\]

where \( Q_s = q n_s \), \( Q_{free} \) is the free charge in the GaAlAs layer, and \( Q_{DX} \) is the charge trapped on DX centers, \( V_{GaAs} \) \( V_{GaAlAs} \) is the peak drift velocity in GaAs (GaAlAs), \( Z \) and \( L_g \) are respectively the gate length and the gate width.

The key-point here is in the way in which deep DX centers can be taken into account. We used some recent results reported by Theis [9] and made the following assumptions: i) every impurity atom gives rise to a deep donor level (presumably due to a DX center). ii) The deep donor level approximately follows the L-valley minimum as this latter changes with \( x \) and is taken to be 140 meV below the L-valley. iii) Shallow donor levels are not taken into account because at high doping concentrations used in HEMTs, the shallow impurity band is resonant with the \( \Gamma \)-valley. As a consequence, each impurity atom gives one electron which either can occupy a conduction band state or can be captured by a(positively) ionized empty deep donor level and the concentrations of these two types of occupied states can be determined since the quasi Fermi level is known.

In order to assess the role of DX centers, the main characteristics \( (g_m, C_{gs}) \) of a typical HEMT structure have been computed both when DX centers, as described above, are taken into account or when they are ignored. This is illustrated in figure 1. DX centers have a strong influence on the transconductance at positive gate voltages. In DC regime a good part (depending on the Fermi level values) of electrons are trapped by DX centers and this limits the upper values of the static drain current and consequently of \( g_m \). When DX centers are ignored, the \( g_m \) can be substantially enhanced. Correspondingly, one finds that \( C_{gs} \) is also very sensitive to the effect of DX centers. Static regime can be characterized by times much longer than the emission or capture times on DX centers. Preliminary experiments performed in our laboratory showed that, at 300K, the capture time is of a few \( \mu s \), although emission time is about 100\( \mu s \) (see figure 2). Consequently, at 500MHz and above, it can be assumed in our model that the charge trapped in DX centers depend on the static bias voltage \( V_{gs0} \) and not on the microwave voltage \( \Delta V_{gs} \). Taking into account these assumptions, the \( Q_s, Q_{free}, Q_{DX} \) values can be determined for \( V_{gs} = V_{gs0} \) and then assuming no variation of the charge trapped in DX centers for \( V_{gs} = V_{gs0} + \Delta V_{gs} \) (typical value
used for \( \Delta V_{gs} \) is 0.1 volt). By using such a method, the microwave \( g_m \) including DX centers can be computed and it is found (figure 1) to be closer to the static \( g_m \) when DX centers are neglected. Similar results are found for the gate capacitance.

In the rest of the paper we check this idea against experiments. We used two different methods in order to measure the static and microwave \( g_m \) of HEMTs with a structure very similar to the one shown in figure 1. In the first method, a pulsed bias voltage is applied to the gate and the corresponding variation of the drain current is recorded (see figure 2 for an example). One then notes that the drain current observed just after the pulse has been applied, can be much higher than the drain current at longer times. This leads, for large positive value of the gate voltage, to microwave \( g_m \) 30% higher than the static \( g_m \). As a counter experiment we have checked that this behaviour is never observed in normal GaAs MESFETs where the effect of DX centers is negligible. A second method was also used to confirm these results: on one side the static \( g_m \) is measured in the DC regime as a function of \( V_{gs} \) and on the other side, the microwave \( g_m \) is obtained from S parameters measurements [10]. The results obtained in this way are presented in figure 3. The fact that at \( V_{gs} > 0 \) the microwave \( g_m \) is substantially higher than the static \( g_m \) is clearly seen. Moreover the results shown here are in quite good agreement with theoretical predictions of figure 1.

In conclusion, we have shown that, although the influence of the deep level due to DX centers in AlGaAs layers may be strong as concerns static characteristics, the influence on microwave performance is clearly much weaker. Consequently, at room temperature, special structures designed in order to eliminate DX centers formation in HEMTs appear not really necessary.

**Figure 1:** This figure compares the computed results obtained (1) when DX centers are ignored; (2) when they are taken into account (see text) in DC regime; (3) when they are taken into account in the transient regime which characterizes DX centers behaviour at microwave frequency.

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![Simulated structure](image-url)
Figure 2: Drain current time response to a pulsed gate voltage. Shorter capture time by DX centers is observed at the beginning of the pulse and longer emission time is observed after extinction of the pulse.

Figure 3: Experimental transconductance measured on a structure very similar to the one shown in fig.1. Static and microwave (500MHz) data are shown.

REFERENCES