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► **To cite this version:**

Laura Monge-bartolome, Laurent Cerutti, Eric Tournié. Impact of the ridge etching-depth on GaSb-based laser diodes. Electronics Letters, 2021, 10.1049/ell2.12392 . hal-03472735

HAL Id: hal-03472735

<https://hal.science/hal-03472735>

Submitted on 9 Dec 2021

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Impact of the ridge etching-depth on GaSb-based laser diodes

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We have fabricated from the same epitaxial wafer series of GaSb-based Fabry-Pérot laser diodes emitting near $2.3 \mu\text{m}$ with different ridge etching-depths. The analysis of the device performances allows quantifying the detrimental impact of deep ridge etching. The threshold current density is increased, whereas the external differential quantum efficiency is reduced, due to a reduced internal quantum efficiency and higher optical losses.

Introduction: GaSb-based semiconductor laser diodes (LDs) have progressively emerged as the best suited technology to fabricate devices emitting in the whole $2\text{--}3 \mu\text{m}$ wavelength range, which is of great interest because several molecules exhibit fingerprint absorption lines [1]. Indeed, depending on the objective, threshold current densities below $100 \text{ A}\cdot\text{cm}^{-2}$, single mode emission or output powers in the Watt range can all be achieved with the GaSb technology [2, 3]. However, despite the recent progress, there remain parameters to clarify. In particular, the ridge etching-depth has been shown to influence the performance of interband cascade lasers [4, 5]. In this letter we report on the study of the impact of the ridge etching depth on laser diodes.

Epitaxy: The laser structure was grown by solid-source molecular-beam epitaxy on a 2-inch *n*-type (001)-GaSb substrate. The design of the heterostructure is typical for emission near $2.3 \mu\text{m}$ [2, 3]. Two $\text{Ga}_{0.68}\text{In}_{0.32}\text{As}_{0.09}\text{Sb}_{0.91}/\text{Al}_{0.25}\text{Ga}_{0.75}\text{As}_{0.02}\text{Sb}_{0.98}$ quantum wells are embedded between 380-nm -thick $\text{Al}_{0.25}\text{Ga}_{0.75}\text{As}_{0.02}\text{Sb}_{0.98}$ undoped waveguide layers, whereas the cladding layers are $1.3 \mu\text{m}$ -thick $\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.07}\text{Sb}_{0.93}$ layers.

Device fabrication: The LDs were fabricated using standard UV photolithography and inductively coupled plasma reactive-ion etching (ICP-RIE) to define the ridges. The ridges were defined either by shallow etching, that is, by etching through the top-cladding layer down to the upper waveguide layer, or by deep etching, that is, by etching through the whole active zone down to the bottom waveguide layer. Electrical insulation and protection were obtained using Si_3N_4 deposited by plasma enhanced chemical vapour deposition (PECVD). Ti-Au and Pd/AuGeNi were used as contact metals for the *p*- and *n*-type electrodes, respectively. Annealing of the metals was performed to ensure low resistance of the *n*-type contact. Note that $10\text{-}\mu\text{m}$ wide ridge LDs with different cavity lengths were cleaved.

Laser characterization: All sets of LDs were tested under continuous wave (cw) operation at room temperature. The output power of the LDs was measured by a calibrated power-meter. We show in Figure 1 the output power-current-voltage ($P\text{--}I\text{--}V$) curves taken from a set of deep etched ridge LDs (Figure 1a) and shallow etched ridge LDs (Figure 1b) with four different cavity lengths between 1 mm and 3 mm. The cavity lengths have been measured after cleavage for better accuracy, which explains the slightly different lengths for the two series of LDs. The $I\text{--}V$ curves (left axis) show a turn-on voltage around 0.7 V , close to the active zone bandgap, for both series. The serial resistance evolves from 2.5Ω for the longest diodes to 5Ω for the shortest ones. The $P\text{--}I$ curves (right axis) show that the threshold current evolves from 48 mA for 1.2 mm long diodes to 115 mA for 2.7 mm long ones for deep-ridge LDs, whereas it evolves from 32 mA for 1 mm long diodes to 63 mA for 2.45 mm long ones for shallow-ridge LDs. While the threshold voltage is similar for all deep-etched LDs, it varies slightly for shallow etched LDs due to processing variability.

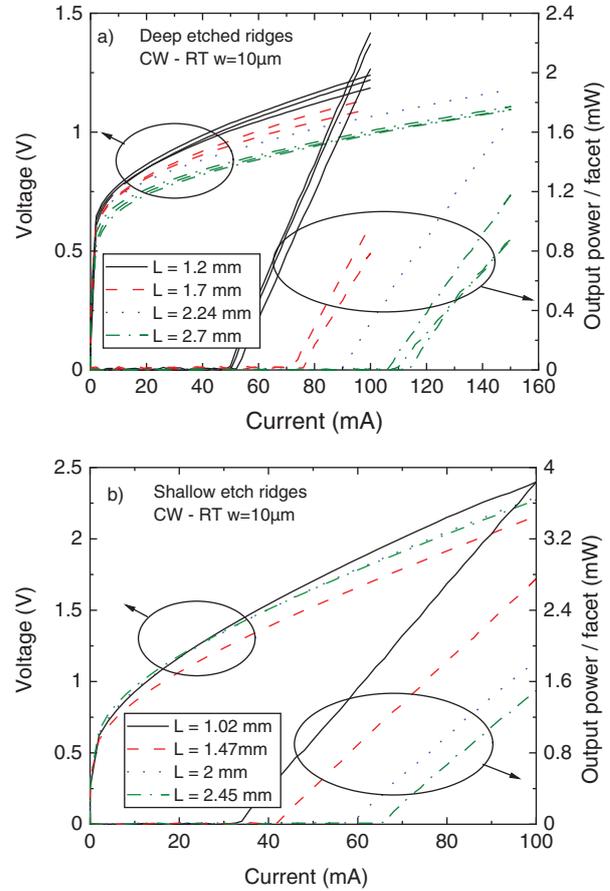


Fig. 1 $P\text{--}I\text{--}V$ curves taken under cw operation at room temperature from (a) deep-etched and (b) shallow-etched laser diodes

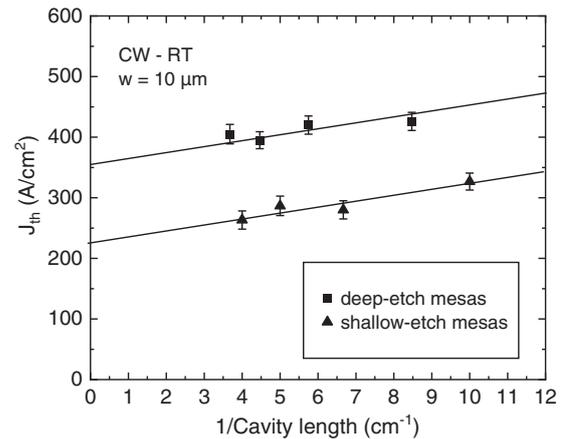


Fig. 2 Evolution of the threshold current density as a function of the reciprocal cavity length for deep-etched (\blacksquare) and shallow-etched (\blacktriangle) laser diodes

To get a clear view on the influence of the etch depth, we have plotted in Figure 2 the evolution of the threshold current density as a function of the reciprocal cavity length for the two LD series. This evolution can theoretically be fitted by [6]:

$$J_{th} = J_{th}(\infty) - \frac{C}{L} \ln(R^{-1}) \quad (1)$$

where R is the facet reflectivity, $J_{th}(\infty)$ is the extrapolated current density threshold for an infinite cavity length, L is the cavity length and C is a constant depending on the material system. Two conclusions can be drawn from Figure 2. The two series of datapoints do obey Equation (1) and sit on parallel straight lines, which reveals that both sets of LDs have similar facet reflectivity. On the other hand, the threshold current density

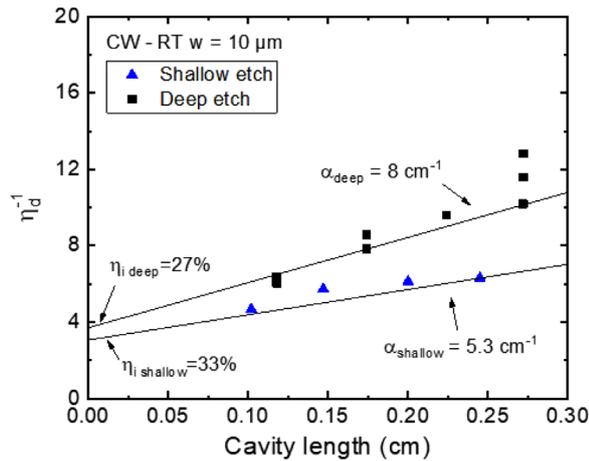


Fig. 3 Evolution of the reciprocal differential quantum efficiency as a function of cavity length for deep-etched (■) and shallow-etched (▲) laser diodes

of deep-etch LDs is much higher than that of shallow-etch LDs, the difference being $\sim 130 \text{ A.cm}^{-2}$, that is, 40–60% of the original threshold.

Next, the external differential quantum efficiencies η_d above threshold have been extracted from the P – I curves and their inverse values are plotted against the cavity length in Figure 3 for both types of cleaved LDs. These data can be analysed using Equation (2) where η_d and η_i are the external differential and internal quantum efficiencies, respectively, R is the facet reflectivity, α_i represents the internal losses and L is the cavity length [7]:

$$\frac{1}{\eta_d} = \frac{1}{\eta_i} + \frac{\alpha_i}{\eta_i \ln(R^{-1})} L. \quad (2)$$

Both sets of data give slightly different internal quantum efficiencies (27% vs. 33% for deep- and shallow- ridges, respectively). We ascribe the difference to carrier leakage at the deep-ridge side walls. In addition, the slope of the η_d^{-1} versus L fit is different for the two data sets. Assuming a power reflectivity of $R = 0.3$ for the facets, one extracts from Figure 3 internal losses of $\alpha_i = 5.3 \text{ cm}^{-1}$ for shallow-ridge LDs, a typical value for LDs made in this materials system [2, 3]. In contrast, the slope gives a value of $\alpha_i = 8 \text{ cm}^{-1}$ for the deep-ridge LDs. These differences arise from non-radiative recombination and optical losses through scattering at the side walls of deep-ridge LDs.

Conclusion: We have fabricated and studied different series of GaSb-based quantum-well LDs with deep and shallow etched ridges. Our results show that deep ridge etching increases the internal losses and lowers the internal quantum efficiencies, which results in higher threshold current densities and lower external differential quantum efficiencies.

Acknowledgements: This work was supported by the French initiative on Investments for the Future (ANR-11-EQPX-0016) and the H2020 Program of the European Union (REDFINCH, GA 780240).

Data availability statement: The data that support the findings of this study are available from the corresponding author upon reasonable request.

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Received: 9 September 2021 Accepted: 24 November 2021

doi: 10.1049/el2.12392

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