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Influence of Nanostructuration on the Vibrational, Electronic and Optical Properties of CrSi$_2$ Thin Films

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Abstract

We report a detailed experimental investigation of the influence of the formation of nanocrystallites on the vibrational, electronic and optical properties of CrSi$_2$ thin films. Both amorphous and nanostructured thin films were investigated by means of electrical resistivity, Hall effect measurements as well as Raman and infrared spectroscopies. We show that both types of films exhibit a semiconducting-like behavior, with the notable difference that the high defect concentrations in amorphous films act as hole donors, modifying the electronic band structure and optical constants. The effect of the film thickness on electrical properties is well captured by the Fuchs-Sondheimer model indicating a decrease in the charge carrier mean free path, likely due to the formation of amorphous/nano-crystallite interfaces that contribute to strongly scatter the charge carriers. Raman spectroscopy performed on nano-crystallized thin films evidences the presence of a Raman-active mode at 229 cm$^{-1}$ and confirms DFT calculations predicting a mode at 248 cm$^{-1}$, the observation of which had remained elusive so far in polycrystalline CrSi$_2$. Measurements of the refractive index and dielectric constants of amorphous thin films show a very high refractive index in the mid-IR range. Our results illustrate how the controlled growth of nano-crystallites can be used to tailor the electronic, vibrational and optical properties of amorphous thin films.

1. Introduction
Transition metal silicides have been considered over the last decades as important technological materials, particularly in microelectronics \(^1\)\(^-\)\(^3\). Among their numerous advantages, their high melting point, high thermal stability, and compatibility with silicon-based microelectronic devices explain the strong interest for these compounds. Moreover, the low cost and large abundance of silicon enables to consider large-scale development using its well-mastered processing technologies. While most of these compounds are metallic, some of them contain transition metals from group VI to VIII elements and exhibit a semiconducting or semimetallic behavior possibly accompanied by exotic magnetic or topological properties such as FeSi\(_2\), MnSi\(_{1.7}\), Ru\(_2\)Si\(_3\) and CoSi \(^4\)\(^-\)\(^7\).

Among these semiconducting silicides, chromium disilicide, CrSi\(_2\), is a \(p\)-type narrow-bandgap material that remains chemically and thermally stable up to at least 1000 K under air \(^8\), \(^9\). Due to its indirect bandgap measured to be about 0.35 eV \(^10\), \(^11\), low temperature coefficient of resistance \(^12\)\(^-\)\(^14\) and promising thermoelectric power factor \((PF = \alpha^2/\rho)\) resulting from low electrical resistivity, \(\rho\), and high Seebeck coefficient, \(\alpha\), \(^15\), \(^16\), this compound has been considered for various applications ranging from IR detection \(^10\), \(^17\), \(^18\), thin-film resistor in integrated circuits \(^12\)\(^-\)\(^14\), contact material in nanosystems \(^19\), Schottky junctions \(^20\), \(^21\) or for thermoelectric power generation at high temperatures \(^15\), \(^16\).

Due to the high number of potential applications, a wealth of studies of the electrical properties of CrSi\(_2\) in bulk polycrystalline and single-crystalline forms and in thin films have been reported. However, widely differing results were obtained, with \(\rho\) values ranging from 0.21 m\(\Omega\).cm \(^22\) to 18.5 m\(\Omega\).cm \(^23\) at room temperature. This wide range of values can be explained by differences in deposition methods, heat treatments, film thickness and Cr:Si stoichiometry. Studies of thin films obtained by co-sputtering of Cr and Si \(^24\)\(^-\)\(^27\), evaporation \(^13\), \(^28\), \(^29\) or pulsed laser deposition
at room temperature, have all reported the formation of an amorphous material. Partial or full crystallization of thin films were observed by annealing between 420 and 870 K, resulting in an increase in the \( \rho \) values. One possibility would be that high annealing temperature and duration may induce the formation of cracks due to thermal expansion and crystallographic orientation mismatches between the thin film and the substrate. Nevertheless, the increase in the electrical resistivity with crystallization is mostly explained by charge carrier scattering at the amorphous phase – nanocrystallites interfaces and by the presence of acceptor-like extended or localized states near the valence band in the amorphous material disappearing by crystallization. Thus, the electrical properties of thin films are highly sensitive to the process route, heat treatments applied and to the nature of the substrate.

Gong et al. studied the influence of annealing on the electrical properties of sputtered CrSi\(_2\) thin films deposited on Si(100) wafer with SiO\(_2\) sublayer with thicknesses spanning the 40 – 90 nm range. An increase in \( \rho \) was observed after annealing 1 h at 573 K under nitrogen atmosphere, a behavior attributed to the full crystallization of the thin film with the formation of wide boundaries surrounding columnar grains. Recently, Burkov et al. reported the study of CrSi\(_2\) thin films with a thickness of 100 nm deposited by sputtering on oxidized Si wafer. Partial crystallization from 500 to about 900 K was shown to lead to the formation of non-percolating nano-crystallites within the amorphous matrix that behave as scattering centers. Concomitantly, enhanced \( \alpha \) values were observed in this heterogeneous material, tentatively ascribed to an energy-filtering mechanism of holes.

Despite the abundant literature data available for CrSi\(_2\), the physical properties of the amorphous phase have been only scarcely investigated. To the best of our knowledge, the vibrational properties were only measured by Koudelka et al., while optical
properties have not been reported yet. Amorphous materials are characterized by the absence of
long-range atomic order with a high number of dangling bonds acting as dopants. Moreover,
disorder drastically alters the phonon properties, strongly scatters charge carriers, thereby
reducing their mobility, and triggers the formation of long tails of localized states in the band gap
of amorphous semiconductors. These effects can significantly impact the vibrational, optical and
electrical properties of the materials. The control of interface charge carriers and phonons
scattering mechanisms by structural defects or further nanostructuration is of great importance
for some applications such as thermoelectricity\textsuperscript{35,36}. It is therefore of prime interest to determine
the influence of microstructure variations on the transport properties, a necessary step towards
further development of nanostructured CrSi\textsubscript{2} for applications.

Here, we report a detailed experimental study of the influence of nano-crystallization on the
vibrational, electronic and optical properties of CrSi\textsubscript{2} amorphous thin films elaborated by
sputtering. Vibrational properties were studied by Raman scattering which evidenced, for the
first time, the existence of a new vibrational mode of crystalline CrSi\textsubscript{2}, which was previously
predicted by DFT calculations\textsuperscript{37}. The electrical properties of both amorphous and nanostructured
thin films were measured for film thicknesses ranging between 12 and 480 nm. We show that the
effect of film thickness on the electrical properties is well captured by the Fuchs-Sondheimer
model. The modification of the electronic structure and optical properties due to
nanocrystallization are highlighted for the first time by IR reflectance and transmittance
measurements.

2. Methods
2.1. Film elaboration. CrSi$_2$ thin films were deposited by sputtering with thickness ranging from 12 up to 480 nm on either glass substrates, CaF$_2$ substrates, or $p$-type Si(100) wafers (BT electronics, B doping), depending on the characterization technique used. Prior to deposition, the substrates were cleaned with acetone and ethanol in an ultrasonic bath during 1 min and dried under N$_2$. In the case of Si wafer, the native oxide layer was etched in a hydrofluoric acid solution (10 vol.%, 30 s) and a SiO$_2$ sublayer with a thickness of 30 nm was pre-deposited for electrical measurements. Scanning electron microscopy (SEM) observations evidenced a continuous SiO$_2$ sublayer, preventing a direct contact between the CrSi$_2$ deposited film with the Si wafer. A Plassys MP4505 RF magnetron sputtering (13.56 MHz) equipment was used for thin film deposition. The distance between the CrSi$_2$ target (Neyco 99.95 % purity, 3 in. diameter) and the substrate was about 5 cm. The base pressure in the chamber was $2 \times 10^{-6}$ mbar before deposition. Argon gas (99.999 % purity) was used to create the plasma with a controlled pressure of 5.1 Pa. The sputtering power was set to 120 W. The target and the substrate were water cooled during sputtering. The film thickness was controlled by measuring the height of a step made by lift-off process using atomic force microscopy (AFM). Thermal treatments of thin films were performed during 1 h at 723 K in dynamic vacuum ($10^{-4}$ mbar) with a heating rate of 100 K h$^{-1}$ and natural cooling. The temperature was controlled with a K-type thermocouple next to the sample.

2.2. Microstructural characterization. High Resolution Transmission Electron Microscopy (HRTEM) cross-section observations were performed using a JEOL 2200FS-200 kV equipment at the MEA platform, University of Montpellier. Prior to these observations, the CrSi$_2$ film deposited on a Si(100) substrate was prepared in cross-section by focused ion beam (FIB) on a FEI Strata DB 235 at IENM - Lille. Before etching the sample with the gallium ion beam, a
platinum layer, and a carbon layer were first deposited to protect sample surface. Atomic force microscopy (AFM) was performed in tapping mode with a Dimension 3100 AFM microscope from Brücker Instruments equipped with a nanoscope 3A quadrex. The tip used was a silicon pointprobe-plus from Nanosensors. Images of sizes 2x2 µm² and 5x5 µm² were recorded. Data were treated with the software Gwyddion 38 to determine the mean square roughness, \( R_{ms} \).

2.3. Electrical measurements. Low-temperature in-plane electrical resistivity and Hall Effect measurements were performed from 80 up to 230 K for samples deposited on a Si/SiO\(_2\) substrate in a Van der Pauw configuration with an Ecopia HMS 5000 equipment under air. Good electrical contacts were achieved using indium. A magnetic field of 0.5 T was used for Hall effect measurements. At high temperatures (300 – 700 K), in-plane electrical resistivity of thin films deposited on glass substrate was measured with a ZEM-3 (Ulvac-Riko) apparatus under a low helium pressure using a conventional four-probe method. Electrical contacts were ensured by deposited thin gold tracks on the thin films. The thin films were maintained in a vertical position using a dedicated sample holder (Ulvac-Riko).

2.4. IR and Raman measurements. Reflectance and transmittance infrared (IR) measurements were carried out with a 161 nm thick film deposited on a transparent CaF\(_2\) substrate. The measurements were performed on the same film before and after annealing. IR spectra were recorded using a Bruker IFS 66 V Fourier transform spectrometer covering the 800–7500 cm\(^{-1}\) spectral range. A black body source, a KBr beam splitter, and a Mercury-Cadmium-Telluride (MCT) detector were used. Room-temperature transmittance and reflectance measurements were performed using the reflection of a Transmittance Bruker accessory (A510 Q/T) with an angle of incidence (\( \theta \)) of 11° placed in the sample compartment of the FTIR spectrometer. Micro-Raman spectroscopy experiments were conducted at room temperature on a 480 nm thick CrSi\(_2\) film.
grown on Si(100) substrate after annealing at 873 K for 20 min under Ar. A LabRam Aramis spectrometer from Horiba Jobin-Yvon was implemented using a He-Ne laser (λ= 633 nm). The beam power was set to 7.3 mW to avoid sample thermal heating.

3. Results and discussion

3.1. Thin film microstructure and effect of thermal treatment

The TEM cross-section images of a 45 nm thin film (deposited on Si(100) wafer) before and after annealing performed at 723 K for 1 h are shown in Figure 1. A typical columnar structure is observed for the as-deposited thin film with the presence of the native silicon oxide sublayer (~5 nm) at the substrate/film interface (Figure 1a). The electron diffraction pattern mainly consists in a large halo characteristic of the amorphous nature of the thin film (Figure 1b). The halo is located between inter-reticular distances of 1.80 and 2.37 Å. The intense and regular spots observed are characteristic of the single-crystalline Si(100) substrate.

The annealing step does not alter the columnar structure which is maintained (Figure 1c). A modification of the microstructure of Si is observed at the Si/CrSi$_2$ interface by TEM. This is due to Cr diffusion into the Si substrate up to about 10 nm as estimated by Electron Dispersive X-ray Spectroscopy (EDXS) analysis. Nevertheless, the diffraction pattern shows additional spots, evidencing the crystallization of the hexagonal CrSi$_2$ phase (Figure 1d). Their low intensity is attributed to the nanometer size of the crystallites, ranging from 3 to 10 nm. A weak diffuse halo is still observed. The annealed thin film is therefore made of CrSi$_2$ nano-crystallites embedded in an amorphous matrix.
**Figure 1.** TEM cross-sectional images of a 45 nm thin film deposited on a Si substrate: a) and b) as deposited, c) and d) after thermal treatment at 723 K for 1 h, and electronic diffraction patterns in the insets. The spots due to the Si(100) substrate are colored in red on the diffraction pattern of Figure d).

Because both the phonon and electron transport mechanisms can be modified by the surface roughness of the thin films, the root-mean-square roughness $R_{ms}$ of the surface of the thin film was determined from the 2x2 $\mu$m$^2$ AFM images for the as-deposited and annealed films with thicknesses varying from 12 up to 164 nm (Figure S1 in Supporting Information). The evolution of $R_{ms}$ as a function of the film thickness is reported in Figure 2. The $R_{ms}$ values of the as-deposited amorphous thin films are low, varying from 0.5 up to 0.8 nm, and only weakly depend on the film thickness. After annealing, the $R_{ms}$ values of the nanostructured thin films only slightly increase, with values ranging between 0.7 and 1.2 nm.
Figure 2. Roughness $R_{ms}$ of amorphous (as-deposited) and nanostructured (annealed) CrSi$_2$ thin films. ○ Amorphous films ■ Nanostructured films.

3.2. Vibrational properties of CrSi$_2$ thin films

At the Brillouin zone center, the optical phonon modes of crystalline CrSi$_2$ can be classified according to the irreducible representations of the D$_6$ point group $\Gamma = A_1 + 2A_2 + 4E_2 + 4E_1 + 3B_2 + 2B_1$. The $E_1$ representation is both infrared and Raman active. The $A_1$ and $E_2$ representations are Raman active while the $A_2$ representation is only infrared active. The $B_1$ and $B_2$ representations are silent. The frequencies of the silent modes have never been measured so far but they have been predicted by DFT calculations$^{37}$ at 204 and 300 cm$^{-1}$ ($B_1$ modes), and at 238, 284 and 393 cm$^{-1}$ ($B_2$ modes). The two infrared $A_2$ modes have been reported in the literature to be around 295 and 375 cm$^{-1}$, in agreement with calculations.

Raman spectra of as-deposited and annealed thin films of 480 nm thickness grown on Si(100) are shown in Figure 3 and are compared with that obtained for powders$^{37}$. For the as-deposited thin film, no lines are observed in agreement with its amorphous state, as previously reported by
Koudelka et al.\textsuperscript{27} After annealing, the spectrum is composed of 6 Raman lines centered at 229, 300, 350, 394, 408 and 453 cm\textsuperscript{-1}. The contributions between 300 and 410 cm\textsuperscript{-1} have been reported in the literature on CrSi\textsubscript{2} thin films\textsuperscript{11, 39}, powders\textsuperscript{37} and single crystal\textsuperscript{40} and their frequency positions are clearly identified (see Ref.\textsuperscript{37} for the assignment of these Raman lines). The Raman line at 229 cm\textsuperscript{-1} is unambiguously identified for the first time on a CrSi\textsubscript{2} film to be the E\textsubscript{1} mode predicted by DFT calculations\textsuperscript{37}. Although this line has already been observed in single crystal\textsuperscript{40} and powders\textsuperscript{37}, its significantly weaker intensity prevented any definitive conclusion to be drawn about its frequency position. Interestingly, an additional tiny Raman line can also be identified at 248 cm\textsuperscript{-1} in Fig. 3. This line had been previously observed only in powders\textsuperscript{37}, but with a very low intensity. The fact that this line is experimentally observed on different samples and confirmed by first-principles calculations makes its assignment and position firmly established. The broad line observed at 453 cm\textsuperscript{-1} has never been reported in literature, should it be on thin films, powders or single-crystalline CrSi\textsubscript{2}. DFT calculations assign the 440 – 450 cm\textsuperscript{-1} range as either E\textsubscript{2} modes or longitudinal optical (LO) E\textsubscript{1} modes\textsuperscript{37}. At this stage, these two possible modes cannot be distinguished, which will require additional polarization-like experiments.
3.3. Electronic properties of CrSi$_2$ thin films

3.3.1. Evolution of the electrical resistivity with temperature

The electrical resistivity, $\rho$, was measured from 300 to 685 K for a CrSi$_2$ thin film grown on glass substrate with a thickness of 480 nm. As shown in Figure 4, $\rho$ does not significantly vary from room temperature up to 520 K, with a mean value of 0.46 m$\Omega$.cm. This behavior, characterized by a low temperature coefficient of resistance ($\text{TCR}= 1/\rho \times \partial \rho / \partial T$), is consistent with an amorphous material $^{41}$. It should be noted that contrary to crystalline materials, a positive or negative TCR is not related to either a metallic or semiconducting state $^{41}$. Above 520 K, $\rho$ significantly increases up to 0.94 m$\Omega$.cm at 685 K. This behavior, similar to that reported previously for amorphous CrSi$_2$ thin films with a transition at temperatures ranging from 450 to
600 K, was attributed to the partial crystallization of the material. Upon cooling, \( \rho \) remains practically temperature-independent with a mean value of 0.94 m\( \Omega \).cm.

The electrical resistivity of bulk polycrystalline CrSi\(_2\), reported by Dasgupta et al. \(^{42}\), increases from 0.9 m\( \Omega \).cm at 300 K to a maximum value of 1.8 m\( \Omega \).cm at 550 K, before decreasing at higher temperatures. These two distinct regimes correspond to the degenerate and intrinsic regime, respectively, of a semiconductor \(^{43}\). Our results also show a positive TCR for CrSi\(_2\) nanostructured films, about \( 5 \times 10^{-4} \) K\(^{-1} \) around 300 K, in contrast to as-deposited amorphous films, but with values significantly lower than those reported in bulk CrSi\(_2\) \((\sim 4 \times 10^{-3} \) K\(^{-1} \) at 300 K)\(^{42}\). These results suggest that the nanostructured films are already above the percolation threshold after annealing at 700 K. This is in agreement with the work of Gladun et al. \(^{31}\), where a positive TCR for Cr\(_x\)Si\(_{1-x}\) films \((x = 0.115 \) to 0.30) was measured after annealing at a temperature that was varying with \( x \) (at 573 K for \( x = 0.3 \) with TCR\( \sim 3 \times 10^{-4} \) K\(^{-1} \)). They notably showed that this effect was related to the percolation of CrSi\(_2\) crystallites formed within the amorphous matrix during annealing for \( x > 0.11 \).
Figure 4. Electrical resistivity $\rho$ as a function of temperature for a CrSi$_2$ thin film with a thickness of 480 nm.

To better understand the origin of the increase in $\rho$ after partial crystallization, the effective charge carrier density, $p_H$, and mobility, $\mu_H$, were measured by Hall effect between 80 and 230 K. For the amorphous films, both $p_H = 1.0 \times 10^{23} \pm 0.5 \times 10^{23}$ cm$^{-3}$ and $\mu_H = 0.12 \pm 0.06$ cm$^2$ V$^{-1}$ s$^{-1}$ are thickness- and temperature-independent. This $p_H$ value is in good agreement with that reported by Lenzlinger et al$^{33}$ for sputtered amorphous films on oxidized silicon wafer and is significantly higher than the values reported for bulk CrSi$_2$ ($p_H = 4 \times 10^{20}$ cm$^{-3}$)$^{16,44,45}$. Meanwhile, $\mu_H$ decreases by two orders of magnitude compared to bulk CrSi$_2$ for which $\mu_H = 12$ cm$^2$ V$^{-1}$ s$^{-1}$.$^{44}$ This low mobility can be attributed to the structural disorder inherent to amorphous compounds that strongly scatters the charge carriers. After partial crystallization, $p_H$ decreases to $6 \times 10^{21} \pm 3 \times 10^{21}$ cm$^{-3}$, regardless of the thickness, and remains constant in the temperature range covered. This effect originates from the decrease in the defect concentration induced by crystallization and atomic rearrangement. In contrast, the observed increase in $\mu_H$ is no longer thickness-independent. The mobility values increase from
\( \mu_H = 0.90 \pm 0.09 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1} \) to \( \mu_H = 1.40 \pm 0.16 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1} \) for 12 and 164 nm thick films, respectively. These values remain substantially lower than those measured in the bulk counterpart. It should be noted that \( \mu_H \) values in \( \text{CrSi}_2 \) are highly related to the defect concentration as shown by Zhu et al.\(^{46} \) who obtained \( \mu_H = 1085 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1} \) for epitaxial films due to a lower defect concentration compared to the present sputtered amorphous and nanostructured films.

### 3.3.2. Influence of film thickness on the electrical resistivity

Figure 5 shows the evolution of the electrical resistivity as a function of temperature for amorphous and nanostructured thin films deposited on \( \text{Si}(100) \) wafers with \( \text{SiO}_2 \) sublayer with a thickness varying from 12 to 164 nm. For amorphous films, \( \rho \) increases with decreasing the thickness over the entire temperature range. This behavior is characteristic of charge carrier scattering at the film surface, as previously observed for example for \( \text{Cu} \)\(^{47} \), \( \text{W} \)\(^{48} \), \( \text{Mo} \)\(^{49} \) or \( \text{CrSi}_2 \) amorphous thin films\(^{12} \). In contrast, the thickness effect is less significant for nanostructured thin films. This different behavior cannot be explained by the surface roughness as similar \( R_{ms} \) values were measured for both films (see Figure 2). For this reason, we believe that charge carrier scattering mechanisms at the film surface are equivalent in both films. Nevertheless, the formation of nano-crystallites in the annealed thin film likely leads to an additional scattering mechanism at the amorphous matrix / nano-crystallite interface.
To explain this behavior, we considered the well-known Fuchs-Sondheimer (F-S) model, which is based on the Boltzmann transport equation\(^50\). This model is generally used to describe surface scattering of electrons in metallic thin films, and can be extended to semiconducting thin films by neglecting surface states within the flat-band approximation\(^51\). The F-S model, which assumes a single parabolic conduction band, a thickness-independent structural disorder, plane parallel surfaces and isotropic scattering processes\(^52\), yields the following equation:

\[
\frac{\rho_\infty}{\rho} = 1 - \frac{3(1 - p)}{2K} \int_1^\infty \left( \frac{1}{t^3} - \frac{1}{t^5} \right) \frac{1 - e^{-kt}}{1 - pe^{-kt}} \, dt
\]

where \(\rho_\infty\) is the electrical resistivity of an infinitely thick film with the same microstructure (including contributions from phonons, grain boundaries, impurities or point-defect scattering mechanisms), \(K = d/\lambda_e\) where \(d\) is the film thickness and \(\lambda_e\) is the charge carrier mean free path in a bulk material with the same defect and impurity concentrations, and \(p\) \((0 \leq p \leq 1)\) is the specularity coefficient describing the fraction of electrons that are specularly scattered from the surface. These scattered electrons have a conserved momentum and hence, do not contribute
to the electrical resistivity. In contrast, in the case of totally diffused scattering ($p = 0$), all the electrons are scattered with a random direction resulting in increased electrical resistivity.

In the present study, we tried to apply this approach to model the low-temperature experimental data measured on amorphous and nanostructured thin films. These data were fitted by a least-squares method using the Gauss-Newton algorithm to evaluate the values of $\rho_\infty$ and $\lambda_e$. Within this model, however, the value of $p$ is usually unknown and strongly depends on the deposition method and substrate. In the case of copper thin films, for instance, Marom et al. have shown that the scattering of electrons on smooth surfaces (that is, below the de Broglie wavelength) is mostly specular, synonymous with $p$ values close to 1. In the present case, the $R_{ms}$ values of the CrSi$_2$ thin films are low, varying from 0.5 to 0.8 nm and from 0.7 to 1.2 nm for amorphous and nanostructured thin films, respectively, that is, below the de Broglie wavelength of CrSi$_2$ estimated to be 5 nm. Based on this empirical criterion, we may therefore expect a mostly specular diffusion scattering in both cases, giving rise to high $p$ values. Both $p$, and $\lambda_e$ parameters cannot be independently determined by the F-S model. Due to the lack of direct experimental probes to easily determine $p$, a totally diffused scattering with $p = 0$ is usually assumed in the literature. For these reasons, we have implemented this model with several values of $p$ varying from 0 to 1. As illustrative examples of the quality of the fits obtained, similar across the entire temperature range and for each $p$ value, Figures 6a and 6b show the results obtained at 80 and 205 K assuming $p = 0$.

In the case of amorphous thin films, surface scattering occurs for thicknesses below about 45 nm (Figure 6a and 6b), thereby increasing the electrical resistivity of thinner films. This value is in good agreement with the thickness limit of 40 nm reported by Novikov et al. The estimated values of $\rho_\infty$ at each temperature are reported in Figure 6c and are similar for $p$ value ranging
from 0 to 1 (Figure S2). The value of $\rho_{\infty} = 0.56$ m$\Omega$.cm does not depend on temperature, which is consistent with the amorphous nature of the film, as previously discussed. In contrast, $\lambda_e$ strongly depends on $p$, as shown in Figure 6d, resulting in charge carrier mean free paths that increase with increasing $p$, from $\lambda_e \approx 15$ nm for $p = 0$ up to $\lambda_e \approx 40$ nm for $p = 0.5$. These values are similar across the entire temperature range covered. A wide range of electron mean free paths in amorphous metallic films were reported in literature, ranging from 3 nm in Co$_{94}$Zr$_6$ films$^{58}$ (at 300 K, with $p = 0$) to 18 – 30 nm$^{60}$ in Y films (at 4.2 K, determined using another model) through 10 – 13 nm$^{61}$ in Pt films (at 300 K, with $p$ varying from 0.13 to 0.15). Based on these reported values, the large mean free paths obtained herein for higher $p$ values, up to $\lambda_e = 240$ nm for $p = 0.9$, are unrealistic. For this reason, we have restricted our analysis to values of $p$ near 0. These considerations thus suggest that diffused scattering of charge carriers prevails in amorphous films despite their low roughness, with $\lambda_e \geq 15$ nm.

For nanostructured thin films, the values of $\rho_{\infty}$ are higher compared to amorphous thin films and slightly increase with increasing temperature (Figure 6c) from 0.78 m$\Omega$.cm at 80 K to 0.89 m$\Omega$.cm at 255 K (without any dependence on $p$). Figure 6d shows that the charge carrier mean free path is reduced for each value of $p$, compared to the amorphous thin films, with, for instance, $\lambda_e$ decreasing from 15 nm to 3 nm if $p = 0$. We note that these values are in good agreement with the value of $\lambda_e = 2$ nm inferred by Trofimov et al. assuming $p = 0^{12}$ for nano-crystallized CrSi$_2$ thin films with thicknesses of up to 20 nm and crystallite sizes varying from 25 to 100 nm. The reduction in the charge carrier mean free path can be explained by the formation of nano-crystallites with sizes comparable to $\lambda_e$ that induce additional charge carrier scattering at the nano-crystallites / amorphous phase interfaces.
The fact that a decrease in the charge carrier density is partially responsible for the increase in $\rho_\infty$, and that the charge carriers mean free path is significantly reduced by scattering at the nanocrystallites / amorphous phase interfaces, explains why $\rho$ is less influenced by the film thickness.

Figure 6. Experimental data (points) and fitted model (lines) of the electrical resistivity $\rho$ as a function of the film thickness for amorphous and nanostructured thin films for $p = 0$ at a) 80 K and b) 205 K. c) $\rho_\infty$ values and d) $\lambda_e$ values as a function of temperature for amorphous and nanostructured thin films with $p = 0$, $p = 0.3$ and $p = 0.5$. In the case of $\rho_\infty$ the points are nearly identical for each $p$ value and cannot be clearly distinguished. ○ Amorphous films ■ Nanostructured films.
3.3.3. Electronic structure

The optical electronic bandgaps, $E_{g, \text{opt}}$, of the thin films have been determined by IR reflectance and transmittance measurements for thin film with a thickness of 161 nm deposited on a CaF$_2$ substrate. The direct and indirect optical bandgap values have been determined using Tauc plots, a representation particularly well adapted for amorphous and nano-crystalline materials \(^{62}\):

$$(ahv)^n = \beta(hv - E_{g, \text{opt}})$$

where $\alpha$ is the absorption coefficient, $\beta$ is the band tailing parameter, $hv$ is the incoming photon energy, and $n$ is a constant related to the transition process ($n = 1/2$ and $n = 2$ for indirect and direct transitions, respectively). Numerous models are used for the determination of $\alpha$ from experimental reflectance and transmittance measurements depending on the experimental conditions. After comparison of some models, we used the method of Galkin et al. \(^{63}\), the details of which are given in the Supporting Information Part 3. This model is adapted in the case of an absorbing thin film and considering multiple reflections in the thickness of the film and the substrate. The inferred values of $\alpha$ with the corresponding Tauc plots are shown in Figures 7a and 7b. A steep increase in $\alpha$ can be observed for photon energies above 0.23 and 0.43 eV for amorphous and nanostructured CrSi$_2$ thin films, respectively (Figure 7a). It clearly indicates a semiconducting nature of both materials, while a metallic behavior of amorphous CrSi$_2$ films was suggested based solely on electrical resistivity measurements \(^{22, 29, 34, 64}\). The non-zero absorption at low energies can be attributed to extrinsic absorption from free carriers. Tauc plots (Figure 7b) show that amorphous and nanostructured CrSi$_2$ thin films have indirect band gaps of $E_{g, \text{ind}}^{\text{opt}} = 0.15 \pm 0.01$ eV and $E_{g, \text{ind}}^{\text{opt}} = 0.20 \pm 0.01$ eV, respectively, and direct band gaps of $E_{g, \text{dir}}^{\text{opt}} = 0.59 \pm 0.01$ eV and $E_{g, \text{dir}}^{\text{opt}} = 0.63 \pm 0.01$ eV. The errors have been estimated from the
linear regression dependence on the selected data range. Thus, amorphous CrSi$_2$ thin films show a very low band gap value explaining the low electrical resistivity and nearly-metallic behavior. An increase in the band gap can be observed upon partial crystallization, although the value is not equivalent to that of crystallized CrSi$_2$ ($\sim0.35$ eV$^{10}$). This modified electronic structure is in agreement with the increase in the electrical resistivity and decrease in the charge carrier concentration measured experimentally, which can be attributed to the reduction of defect level and nano-crystallization during the annealing process. Such changes of the electronic structure of CrSi$_2$ thin films due to nano-crystallization are consistent with previous optical conductivity measurements$^{22}$.

In contrast to crystalline materials that exhibit well-defined band gap edges, amorphous materials can be characterized by band tails due to structural defects$^{65}$. They are quantified by the Urbach energy, $E_u$, inferred from the exponential part of the absorption coefficient near the gap using the relation $\alpha = \alpha_0 \exp \left( \frac{h\nu}{E_u} \right)$. The value of $E_u$ corresponds to the width of the tails of localized states near the band edge and are obtained from the plot of the variations in $\ln (\alpha)$ with $h\nu$ near the bandgap (see Supporting Information Part 4). Herein, a dramatic decrease of $E_u$ by nano-crystallization is observed with values of $E_u$ of 0.13 eV and 0.06 eV for as-deposited and annealed films, respectively, due to a decrease in the defect level. Thus, these band tails, nearly identical to the band gap values, can explain the quasi-metallic behavior of the amorphous thin films.
Figure 7. Optical properties of amorphous (as-deposited) and nanostructured (annealed) CrSi$_2$ thin films. a) Absorption coefficient and b) corresponding Tauc plot for both indirect and direct band gaps. □ Amorphous film, □ Nanostructured film. The black dashed lines show the linear fit of the Tauc plot.

3.4. Optical properties of amorphous and nanostructured CrSi$_2$ thin films

The refractive index, $n$, and extinction coefficient, $k$, shown in Figure 8a, were determined from the IR absorbance coefficients (see Figure 7a). The observed variations are in line with the electronic structure modifications induced by the nano-crystallization.

The behavior of $n$ agrees with those reported for well-crystallized or epitaxial films and single crystals$^{10,63,66}$. A broad maximum is observed around 0.16 – 0.25 eV and 0.43 – 0.53 eV for as-deposited and annealed films, respectively. These energy windows are consistent with those reported in the literature for well-crystallized CrSi$_2$ thin films, extending from 0.3 – 0.55 eV$^{10}$ to 0.47 – 0.56 eV$^{63}$. The energy shift of the maximum in $n$ upon annealing is directly related to electronic structure modifications due to the change of inter-band transition as the band gap increases. Moreover, a large reduction of $n$ values is observed after nano-crystallization, as expected from an increase in the band gap. Nevertheless, the $n$ values of annealed CrSi$_2$ thin
films are still higher than those reported previously \(^{10, 63, 66}\) because of the partial crystallization of the material after the thermal treatment. As for \(k\), both amorphous and nanostructured samples show a similar behavior with a shift of about 0.25 eV. A large increase in \(k\) with decreasing the energy is observed, due to the absorption by free charge carriers \(^{10}\). The slope of this part is higher for the amorphous thin film due to the high defect concentration. A minimum value of \(k \approx 0.8\) is observed around 0.3 eV and 0.5 eV for amorphous and nanostructured thin films, respectively.

The real (\(\varepsilon_1\)) and imaginary (\(\varepsilon_2\)) parts of the dielectric function, were calculated from the \(n\) and \(k\) values using the following relations \(^{67}\):

\[
\varepsilon_1 = n^2 - k^2
\]
\[
\varepsilon_2 = 2nk
\]

The \(\varepsilon_1\) and \(\varepsilon_2\) values, reported in Figure 8b, are analogous with \(n\) and \(k\). Broad maxima of \(\varepsilon_1\) are observed at 0.16 – 0.25 eV and 0.43 – 0.53 eV for amorphous and nanostructured thin films, respectively, due to inter-band transitions. The \(\varepsilon_1\) values are nevertheless larger in the case of the amorphous thin films. As for \(\varepsilon_2\), a large increase is observed at low energies, as previously described in the case of \(k\).

Similarly to our results on nanostructured film, a maximum in \(\varepsilon_1\) at about 0.5 eV is reported for epitaxial and polycrystalline CrSi\(_2\) films \(^{63, 66}\), but with a smaller \(\varepsilon_1\) value (near 50) due to the residual amorphous phase in our sample.
Figure 8. a) Refractive index $n$ and extinction coefficient $k$ as a function of the photon energy $h\nu$. b) Real and imaginary parts of the dielectric function $\varepsilon_1$ and $\varepsilon_2$ as a function of the photon energy. □ Amorphous (as-deposited) and □ Nanostructured (annealed) thin films.

4. Conclusion

Contrasting the electrical, vibrational and optical properties of as-deposited, amorphous and annealed, nanostructured CrSi$_2$ thin films have revealed interesting insights into the influence of nano-crystallites, successfully grown in the amorphous matrix through annealing for 1 h at 723 K under vacuum. Raman spectroscopy has evidenced a line at 229 cm$^{-1}$, predicted to be a $E_1$ mode by DFT calculations, that had remained elusive so far in experiments on thin films. In agreement with first-principles calculations, these measurements further enabled the identification of a weak Raman line at 248 cm$^{-1}$, which had been previously observed solely on polycrystalline CrSi$_2$, but with a significantly lower intensity. The presence of high defect concentrations in amorphous films, acting as acceptor dopants, significantly increases the hole concentration to values usually encountered in metals. The amorphous nature of these films explains the strong lowering of the band gap width evidenced by optical spectra ($E_{g,ind}^{opt} = 0.15 \pm 0.01$) due to band tails that cover
nearly entirely the band gap ($E_u = 0.13$ eV). Upon annealing, the nano-crystallization induces a decrease in the defect concentration, leading to a concomitant increase in the band gap and reduction in the band tail width, in agreement with the measured lower carrier concentration. Thus, the increase in the electrical resistivity in partially-crystallized thin films can be explained by both the reduction of defect concentration by annealing, which modifies the electronic structure, and the formation of nano-crystallites that promotes an additional interface scattering mechanism reducing the charge carrier mean free path. Using the Fuchs-Sondheimer model, we showed that a charge carrier mean free path of $\lambda_e \geq 15$ nm in amorphous films is responsible for the variations in electrical properties as a function of the thickness by surface scattering that dominates for thicknesses below 45 nm. The variations in the electronic structure also manifest in the optical and dielectric constants. The decrease in the band gap in amorphous thin films produces a shift of the $n, k, \varepsilon_1$ and $\varepsilon_2$ constants towards low energy and a modification of their intensities. A large index of refraction $n$ in the mid-IR range was obtained for amorphous CrSi$_2$ thin films, which can be of practical relevance to form waveguides for instance. Our study provides a robust basis to understand the vibrational, electronic and optical properties of both amorphous and nanostructured CrSi$_2$ thin films. In particular, our results demonstrate the possibility to tailor the electrical, optical and vibrational properties by controlling the crystallization rate during annealing and the thickness of the thin films for a given application.

ASSOCIATED CONTENT

Supporting Information

Atomic Force Microscopy (AFM) observations, details of the Fuchs-Sondheimer model, absorption coefficient calculation and Urbach energy determination.
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