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The Geometry of Parallelism
Classical, Probabilistic, and Quantum Effects

Extended version

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Abstract
We introduce a Geometry of Interaction model for higher-order quantum computation, and prove its adequacy for a fully fledged quantum programming language in which entanglement, duplication, and recursion are all available. This model is an instance of a new framework which captures not only quantum but also classical and probabilistic computation. Its main feature is the ability to model commutative effects in a parallel setting. Our model comes with a multi-token machine, a proof net system, and a PCF-style language. Being based on a multi-token machine equipped with a memory, it has a concrete nature which makes it well suited for building low-level operational descriptions of higher-order languages.

Categories and Subject Descriptors F.3.2 [Semantics of Programming Languages]: Algebraic approaches to semantics

Keywords Geometry of Interaction, memory structure, quantum, probabilistic, PCF

1. Introduction
In classical computation, information is deterministic, discrete and freely duplicable. Already from the early days [1], however, determinism has been relaxed by allowing state evolution to be probabilistic. The classical model has then been further challenged by quantum computation [2], a computation paradigm which is based on the laws of quantum mechanics.

Probabilistic and quantum computation are both justified by the very efficient algorithms they give rise to: think about Miller-Rabin primality test [3, 4], Shor’s factorization [5] but also the more recent algorithms for quantum chemistry [6] or for solving linear systems of equations [7]. Finding out a way to conveniently express those algorithms without any reference to the underlying hardware, is then of paramount importance.

This has stimulated research on programming languages for probabilistic [8, 9] and quantum computation (see [10] for a survey), and recently on higher-order functional languages [11–13]. The latter has been epitomized by variations and extensions of the λ-calculus. In order to allow compositional reasoning, it is important to give a denotational semantics to those languages; maybe surprisingly, a large body of works in this direction is closely connected to denotational and interactive models of Linear Logic [14], in the style of Game Semantics [15, 16] and the Geometry of Interaction [17].

The case of quantum computation is emblematic. The first adequate denotational model for a quantum programming language à la PCF, only two years old [13], marries a categorical construction for the exponentials of linear logic [18, 19] to a suitable extension of the standard model of quantum computation: the category of completely positive maps [20]. The development of an interactive semantics has proved to be highly nontrivial, with results which are impressive but not yet completely satisfactory. In particular, the underlying language either does not properly reflect entanglement [21–23], a key feature of quantum computation, or its expressive power is too weak, lacking recursion and duplication [24, 25]. The main reason for this difficulty lies in the inherent non-locality of entanglement [2].

In this paper we show that Girard’s Geometry of Interaction (GoI) indeed offers the right tools to deal with a fully fledged quantum programming language in which duplication and full recursion are available, when we equip GoI with an external quantum memory, a standard technique for operational models of quantum λ-calculi [11].

We go further: the approach we develop is not specific to quantum computation, and our quantum model is introduced as an instance of a new framework which models choice effects in a parametric way, via a memory structure. The memory structure comes with three operations: (1) allocation of fresh addresses in the memory, (2) low-level actions on the memory, and (3) choice based on the value of the memory at a given address. The notion of memory structure is flexible, the only requirement being commutativity of the operations. In Sec. 3.3 we show that different kinds of choice effects can be systematically treated: classical, probabilistic and quantum memory are all instances of this general notion. Therefore, the memory makes the model suitable to interpret classical, probabilistic and quantum functional programs. In particular, in the case of quantum memory, a low-level action is an application of unitary gate to the memory, while the choice performs a quantum measure.
The GoI model we give has a very concrete nature, as it consists of a class of token machines [26, 27]. Their distinctive feature is to be parallel and multi-token [25, 28] rather than single-token as in classic token machines [26]. Being multi-token means that different computational threads can interact with each other and synchronize (think of this as a multi-player game, where players are able to collaborate and exchange information). The presence of multiple tokens allows to appropriately reflect non-locality in a quantum setting, but also to generally deal with parallelism and choice effects in a satisfactorily way. We discuss why this is the case when we concretely present the machine (Sec. 5).

Finally, to deal with the combination of parallelism, probabilistic side-effects and non-termination, we develop a general notion of PARS, probabilistic abstract rewrite system. The results we establish on PARS are the key ingredient in the Adequacy proofs, but are also of independent interest. The issues at sake are non-trivial and we discuss them in the dedicated Sec. 1.2.

Contributions. We present a Geometry of Interaction (GoI) model for higher-order quantum computation, which is adequate for a quantum programming language in which entanglement, duplication, and recursion are all available. Our model comes with a multi-token machine, a proof net system, and a PCF-style language. More specifically, this paper’s contributions can be summarized as follows:

- we equip GoI with the ability to capture choice effects using a parametric notion of memory structure (Sec. 3);
- we show that the notion of memory structure is able to capture classical, probabilistic and quantum effects (Sec. 3.3);
- we introduce a construction which is parametric on the memory, and produces a class of multi-token machines (Sec. 5), proof net systems (Sec. 4) and PCF-style languages (Sec. 6). We prove that (regardless of the specific memory) the multi-token machine is an adequate model of nets reduction (Th. 28), and the nets an adequate model of PCF term rewriting (Th. 29);
- we develop a general notion of parallel abstract rewrite system, which allows us to deal with the combination of parallelism and probabilistic choice in an infinitary setting (Sec. 2).

Being based on a multi-token machine associated to a memory, our model has a concrete nature which makes it well suited to build low-level operational descriptions of higher-order programming languages. In the remainder of this section, we give an informal overview of various aspects of our framework, and motivate with some examples the significance of our contribution.

This report is an extended version of [29].

1.1 Geometry of Interaction and Quantum Computation

Geometry of Interaction is interesting as semantics for programming languages [27, 30, 31] because it is a high-level semantics which at the same time is close to low-level implementation and has a clear operational flavor. Computation is interpreted as a flow of information circulating around a network, which essentially is a representation of the underlying program. Computational steps are broken into low-level actions of one or more tokens which are the agents carrying the information around. A long standing open question is whether fully fledged higher-order quantum computation can be modeled operationally via the Geometry of Interaction.

1.1.1 Quantum Computation

As comprehensive references can be found in the literature [2], we only cover the very few concepts that will be needed in this paper. Quantum computation deals with quantum bits rather than bits. The state of a quantum system can be represented with a density matrix to account for its probabilistic nature. However for our purpose we shall use in this paper the usual, more operational, non-probabilistic representation. Single quantum bits (or qubits) will thus be represented by a ray in a two-dimensional complex Hilbert space, that is, an equivalence class of non-zero vectors up to (complex) scalar multiplication. Information is attached to a qubit by choosing an orthonormal basis \((|0\rangle, |1\rangle)\): a qubit is a superposition of two classical bits (modulo scalar multiplication). If the state of several bits is represented with the product of the states of single bits, the state of a multi-qubit system is represented with the tensor product of single-qubit states. In particular, the state of an \(n\)-qubit system is a superposition of the state of an \(n\)-bit system. We consider superpositions to be normalized.

Two kinds of operations can be performed on qubits. First, one can perform reversible, unitary gates: they are unitary maps in the corresponding Hilbert space. A more exotic operation is the measurement, which is the only way to retrieve a classical bit out of a quantum bit. This operation is probabilistic: the probabilities depend on the state of the system. Moreover, it modifies the state of the memory. Concretely, if the original memory state is \(\alpha|0\rangle + \beta|1\rangle\) (with \(\alpha, \beta\) normalized), measuring the first qubit would answer \(x\) with probability \(|\alpha|^2\), and the memory is turned into \(|x\rangle \otimes \phi_x\). Note how the measurement not only modifies the measured qubit, but also collapses the global state of the memory.

The effects of measurements are counterintuitive especially in entangled system: consider the 2-qubit system \(\frac{\alpha}{\sqrt{2}} (|0\rangle + |1\rangle)\). This system is entangled, meaning that it cannot be written as \(|\psi\rangle = \phi_1 \otimes \phi_2\) with \(|\phi_1\rangle\) and \(|\phi_2\rangle\) single-qubit states. One can get such a system from the state \(|00\rangle\) by applying first an Hadamard gate \(H\) on the second qubit, sending \(|0\rangle\) to \(\frac{1}{\sqrt{2}} (|0\rangle + |1\rangle)\) and \(|1\rangle\) to \(\frac{1}{\sqrt{2}} (|0\rangle - |1\rangle)\), therefore getting the state \(\frac{\alpha}{\sqrt{2}} (|00\rangle + |01\rangle)\), and then a CNOT (controlled-not) gate, sending \(|x\rangle\) to \(|x \oplus y\rangle\ \otimes \ y\rangle\). Measuring the first qubit will collapse the entire system to \(|00\rangle\) or \(|11\rangle\), with equal probability \(\frac{1}{2}\).

**Remark 1.** Notwithstanding the global collapse induced by the measurement, the operations on physically disjoint quantum states are commutative. Let \(A\) and \(B\) be two quantum states. Let \(U\) act on \(A\) and \(V\) act on \(B\) (whether they are unitaries, measurements, or combinations thereof). Consider now \(A \otimes B\): applying \(U\) on \(A\) then \(V\) on \(B\) is equivalent to first applying \(V\) on \(B\) and then \(U\) on \(A\). In other words, the order of actions on physically separated quantum systems is irrelevant. We use this property in Sec. 3.3.3.

1.1.2 Previous Attempts and Missing Features

A first proposal of Geometry of Interaction for quantum computation is [21]. Based on a purely categorical construction [32], it features duplication but not general entanglement: entangled qubits cannot be separately acted upon. As the authors recognize, a limit of their approach is that their GoI is single-token, and they already suggest that using several tokens could be the solution.

**Example 2.** As an example, if \(S = \frac{\alpha}{\sqrt{2}} (|00\rangle + |11\rangle)\), the term

\[
\text{let } x \otimes y = S \in (Ux) \otimes (Vy)
\]

(1)

cannot be represented in [21], because it is not possible to send entangled qubits to separate parts of the program.

A more recent proposal [25], which introduces an operational semantics based on multi-tokens, can handle general entanglement. However, it does neither handle duplication nor recursion. More than that, the approach relies on termination to establish its results, which therefore do not extend to an infinitary setting: it is not enough to “simply add” duplication and fix points.

**Example 3.** In [25] it is not possible to simulate the program that tosses a coin (by performing a measurement), returns a fresh qubit on head and repeats on tail. In mock-up ML, this program becomes

\[
\text{letrec } f x = (\text{if } x \text{ then new else } f\ (H\new)) \in (f\ (H\new))
\]
where \( \text{new} \) creates a fresh qubit in state \( |0\rangle \) and where the \( \hat{x} \) test performs a measurement on the qubit \( x \). Note how the measure of \( H_{\text{new}} \) amounts to tossing a fair coin: \( H_{\text{new}} \) produces \( \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle) \). Measuring gives \( |0\rangle \) and \( |1\rangle \) with probability \( \frac{1}{2} \).

Example 3 will be our leading example all along the paper. Furthermore, we shall come back to both examples in Sec. 7.1.1.

1.2 Parallel Choices: Confluence and Termination

When dealing with both probabilistic choice and infinitary reduction, parallelism makes the study of confluence and convergence highly non-trivial. The issue of confluence arises as soon as choices and duplication are both available, and non-termination adds to the challenges. Indeed, it is easy to see how tossing a coin and duplicating the result does not yield the same probabilistic result as tossing twice the coin. To play with this, let us take for example the following term of the probabilistic \( \lambda \)-calculus [33]:

\[ M = (\lambda x. x \, \text{xor} \, x)((\text{tt} \oplus \text{ff}) \oplus \Omega) \]

where \( \text{tt} \) and \( \text{ff} \) are boolean constants, \( \Omega \) is a divergent term, \( \oplus \) is the choice operator (here, tossing a fair coin), and \( \text{xor} \) is the boolean operator computing the exclusive or. Depending on which of the two redexes we fire first, \( M \) will evaluate to either the distribution \( \{\text{ff}^{\frac{1}{2}}, \text{tt}^{\frac{1}{2}}\} \) or to the distribution \( \{\text{tt}^{\frac{1}{2}}, \text{ff}^{\frac{1}{2}}\} \).

In ordinary, deterministic PCF, any program of boolean type may or may not terminate, depending on the reduction strategy, but its normal form, if it exists, is unique. This is not the case for our probabilistic term \( M \): depending on the choice of the redex, it evaluates to two distributions which are simply not comparable.

In the case of probabilistic \( \lambda \)-calculi, the way-out to this is to fix a reduction strategy; the issue however is not only in the syntax, it appears—at a more fundamental level—also in the model. This is the case for [33], where the model itself does not support parallel probabilistic choice. Similarly, in the development of a Game Semantics or Geometry of Interaction model for probabilistic \( \lambda \)-calculi, the standard approach has been to use a polarized setting, so to impose a strict form of sequentiality [34, 35]. If instead we choose to have parallelism in the model, confluence is not granted and even the definition of convergence is non-trivial.

In this paper we propose a probabilistic model that is

- **infinitary and parallel but confluent**, to achieve this, in Sec. 2 we develop some results which are general to any probabilistic abstract rewrite system, and which to our knowledge are novel. More specifically, we provide sufficient conditions for an infinitary probabilistic system to be confluent and to satisfy a property which is a probabilistic analogous of the familiar “weak normalization implies strong normalization”. We then show that the parametric models which we introduce (both in the semantics the standard features from the “notions of computation” which is the specificity of the language, allows for an easier comparison between different languages.

- Simplify the Design of a Language with Its Operational Model: it is enough to focus on the memory structure which encapsulates the desired effects. Once such a memory structure is given, the construction provides an adequate Geometry of Interaction model for a PCF-like language equipped with that memory. Memory structures are defined in Sec. 3, while the operational core is based on Linear Logic: a linearly-typed PCF-like language, Geometry of Interaction, and its syntactical counterpart, proof nets. Proof nets are a graph-based formal system that provides a powerful tool to analyze the execution of terms as a rewriting process which is mostly parallel, local, and asynchronous.

More in detail, our framework consists of:

1. a notion of memory structure, whose operations are suitable to capture a range of choice effects;
2. an operational core, which is articulated in the three base rewrite systems (a proof net system, a GoI multi-token machine, and a PCF-style language);
3. a construction which is parametric on the memory, and lifts each base rewrite system into a more expressive operational system. We respectively call these systems: program nets, MSIAM machines and PCF\textsubscript{\textit{MSIAM}} abstract machines.

Finally, the three forms of systems are all related by adequacy results. As long as the memory operations satisfy commutativity, the construction produces an adequate GoI model for the corresponding PCF language. More precisely, we prove—again parametrically on the memory—that the MSIAM is an adequate model of program net reduction (Th. 28), and program nets are expressive enough to adequately represent the behavior of the PCF language (Th. 29).

1.4 Related Work

The low-level layer of our framework can be seen as a generalization and a variation of systems which are in the literature. The nets and multi-token machine we use are a variation of [28], the linearly typed PCF language is the one in [13] (minus lists and coproducts). What we add in this paper are the right tools to deal with challenges like probabilistic parallel reduction and entanglement. Neither quantum nor probabilistic choice can be treated in [28], because of the issues we clarified in Sec. 1.2. The specificity of our proposal is really its ability to deal with choice together with parallelism.

We already discussed previous attempts to give a GoI model of quantum computation, and their limits, in Sec. 1.1.2 above. Let us quickly go through other models of quantum computation. Our parametric memory is presented equationally: equational presentations of quantum memory are common in the literature [36, 37]. Other models of quantum memories are instead based on Hilbert spaces and completely positive maps, as in [13, 20]. In both of these approaches, the model captures with precision the structure and behavior of the memory. Instead, in our setting, we only consider the interaction between the memory and the underlying computation by a set of equations on the state of the memory at a given address, the allocation of fresh addresses, and the low-level actions.

Finally, taking a more general perspective, our proposal is by no means the first one to study effects in an interactive setting. Dynamic semantics such as GoI and Game Semantics are gaining interest and attention as semantics for programming languages because of their operational flavor. [35, 38, 39] all deal with effects in GoI. A common point to all these works is to be single-token. While our approach at the moment only deals with choice effects, we indeed deal with parallelism, a challenging feature which was still missing.

2. PARS: Probabilistic Abstract Reduction Systems

Parallelism allows critical pairs; as we observed in Sec. 1.2, firing different redexes will produce different distributions and can lead to
possibly very different results. Our parallel model however enjoys a
property similar to the diamond property of abstract rewrite systems.
Such a property entails a number of important consequences for
confluence and normalization, and these results in fact are general to
any probabilistic abstract reduction system. In particular, we define
what we mean by strong and weak normalization in a probabilistic
setting, and we prove that a suitable adaptation of the diamond
property guarantees confluence and a form of uniqueness of normal
forms, not unlike what happens in the deterministic case. Th. 11 is
the main result of the section.

In a probabilistic context, spelling out the diamond property
requires some care. We will introduce a strongly controlled notion
of reduction on distributions (⇒). The need for this control has
the same roots as in the deterministic case: please recall that strong
normalization follows from weak normalization by the diamond
property (b ↦ a ⇒ c ⇔ b = c ∨ ∃d(b → d ↦ c) but not from
subcommutativity (b ↦ a ⇒ c ⇔ ∃d(b → d ↦ c) ⇒ a) which appears very similar, but “leaves space” for an infinite branch.

2.1 Distributions and PARS

We start by setting the basic definitions. Given a set A, we note
DST(A) for the set of distribution properties on A: any µ ∈
DST(A) is a function from A to [0, 1] such that ∑µ(a) ≤ 1.
A distribution µ is proper if ∑µ(a) = 1. The distribution
assigning 0 to each element of a set A is indicated with 0. We
indicate with SUPP(µ) the support of a distribution µ, i.e.
the subset of A whose image under µ is not 0. On DST(A), we define
the relation ⊆ point-wise: µ ⊆ ρ if µ(a) ≤ ρ(a) for each a ∈ A.

A probabilistic abstract reduction system (PARS) is a pair A =
(A, →) consisting of a set A and a relation ⊳ ⊆ A × DST(A)
(rewrite relation, or reduction relation) such that for each (a, µ) ∈ →,
SUPP(µ) is finite. We write a → µ for (a, µ) ∈ →. An element a ∈ A is
terminal or in normal form (w.r.t. →) if there is no µ with a → µ, which we write a ↩.

We can partition any distribution µ into a distribution µc on
terminal elements, and a distribution µv on elements for which there
exists a reduction, as follows:

\[ µ(a) = \begin{cases} µ(a) & \text{if } a ↩, \\ 0 & \text{otherwise;} \end{cases} \]

The degree of termination of µ, written T(µ), is ∑µ(a).

We write → for the reflexive and transitive closure of →, namely
the smallest subset of A × DST(A) closed under the following
rules:

\[ a → µ \quad \text{a} ↳ \{ a^1 \} \quad a ↳ \{ b^1 \} \quad b ↳ ρ \quad b ∉ SUPP(µ) \]

We read a ↳ µ as “a reaches µ”.

The Relation ⊳. In order to extend to PARS classical results on
termination for rewriting systems, we define the binary relation
⊳, which lifts the notion of one step reduction to distributions.
We require that all non-terminal elements are indeed reduced. The
relation ⊳ ⊆ DST(A) × DST(A) is defined as

\[ µ = µc + µv \quad \{ a → ρ_a \}_{a ∈ SUPP(µc)} \]

Please note that in the derivation above, we require a → ρ_a for each
a ∈ SUPP(µ). Observe also that µc ↳ µv since SUPP(µv) = ∅.

We write µ ⊳ ρ if µ reduces to ρ in n ≥ 0 steps; we write
µ ⊳ ρ if there is any finite sequence of reductions from µ to ρ.

With a slight abuse of notation, in the rest of the paper we
sometime write {a} for {a^1}, or simply a when clear from the
context. As an example, we write a ↳ µ for {a} ⊳ µ. Moreover,
the distribution \{a_1, \ldots, a_n\} will be often indicated as ∑ pi, \{a_i\} thus facilitating algebraic manipulations.

2.2 Normalization and Confluence

In this subsection, we look at normalization and confluence in
the probabilistic setting, which we introduced in Sec. 2.1. We
need to distinguish between weak and strong normalization. The
former refers to the possibility to reach normal forms following
any reduction order, while the latter (also known as termination,
see [40]) refers to the necessity of reaching normal forms. In both
cases, the concept is inherently quantitative.

Definition 4 (Weak and Strong Normalization). Let p ∈ [0, 1] and
let µ ∈ DST(A) Then:
• µ weakly p-normalizes (or weakly normalizes with probability
at least p) if there exists ρ such that µ ⊳ ρ and T(ρ) ≥ p.
• µ strongly p-normalizes (or strongly normalizes with probability
at least p) if there exists n such that µ ⊳n ρ implies T(ρ) ≥ p,
for all p.

The relation → is said uniform if for each p, and each µ ∈ DST(A),
weak p-normalization implies strong p-normalization.

Following [40], we will also use the term p-termination for
strong p-normalization, and refer to weak p-normalization as simply
p-normalization.

Even the mere notion of convergent computation must be made
quantitative here:

Definition 5 (Convergence). The distribution µ ∈ DST(A) converges
with probability p, written µ ⇓ p, if p = supµ→p T(µ).

Observe that for every µ there is a unique probability p such that
µ ⇓ p. Please also observe how Definition 5 is taken over all p such
that µ ⇓ p, thus being forced to take into account all possible
reduction orders. If → is uniform, however, we can reach the limit
along any reduction order:

Proposition 6. Assume → is uniform. Then for every µ such that
µ ⇓ p and for every sequence of distributions (ρ_n) such that µ = ρ_0
and ρ_n ⇓ ρ_{n+1} for every n, it holds that p = lim_{n→∞} T(ρ_n).

Remark 7. Observe that because of Prop. 6, sup_{µ→p} T(µ) =
sup_{ρ→p} T(ρ).

A PARS is said to be confluent iff ⊳ is a confluent relation in
the usual sense:

Definition 8 (Confluence). The PARS (A, →) is said to be
confluent if whenever τ ⇓ µ and τ ⇓ ξ, there exists ρ such that
µ ⇓ ρ and ξ ⇓ ρ.

As an immediate consequence:

Corollary 10 (Confluence). If (A, →) satisfies the diamond prop-
erty, then (A, →) is confluent.

Finally, then, the diamond property ensures that weak p-
normalization implies strong p-normalization, precisely like for
usual abstract rewrite systems:

Theorem 11 (Normalization and Uniqueness of Normal Forms).
Assume (A, →) satisfies the diamond property. Then:
1. Uniqueness of normal forms. µ ⇓ ρ and µ ⇓ τ for some
k ∈ N implies ρ = τ.
2. Uniformity. If µ is weakly p-normalizing (for some p ∈ [0, 1],
then µ strongly p-normalizes, i.e., → is uniform.
Proof. First note that (2) follows from (1). In order to prove (1), we use an adaptation of the familiar “tiling” argument. It is not exactly the standard proof because reaching some normal forms in a distribution is not the end of a sequence of reductions. Assume \(\mu = \rho_0 \Rightarrow \rho_1 \Rightarrow \cdots \Rightarrow \rho_k\), and \(\mu = \tau_1 \Rightarrow \cdots \Rightarrow \tau_k\). We prove \(\rho_k = \tau_k\) by induction on \(k\). If \(k = 1\), the claim is true by Definition 9 (1). If \(k > 1\) we tile (w.r.t. \(\Rightarrow\)), as depicted below:

We build the sequence \(\sigma_0 = \tau_1 \Rightarrow \cdots \Rightarrow \sigma_{k-1}\) (see the Fig. on the side) where each \(\sigma_{i+1}\) \((i \geq 0)\) is obtained via Definition 9 (2), from \(\rho_i \Rightarrow \rho_{i+1}\) and \(\mu \Rightarrow \sigma_i\), by closing the diamond. By Definition 9 (1) \(\rho_k = \sigma_{k-1}\). Now we observe that \(\tau_1 \Rightarrow \cdots \Rightarrow \tau_k\) and \(\tau_1 \Rightarrow \cdots \Rightarrow \sigma_{k-1}\). Therefore we have (by induction) \(\sigma_{k-1} = \tau_{k-1}\), from which we conclude \(\rho_k = \tau_k\). \(\square\)

3. Memory Structures
In this section we introduce the notion of memory structure. Commutativity of the memory operations is ensured by a set of equations. To deal with the notion of fresh addresses, and avoid unnecessary bureaucracy, it is convenient to rely on nominal sets. The basic definitions are recalled below (for details, see, e.g., [41]).

3.1 Nominal Sets
If \(G\) is a group, then a \(G\)-set \((M, \cdot)\) is a set \(M\) equipped with an action of \(G\) on \(M\), i.e. a binary operation \((\cdot) : G \times M \rightarrow M\) which respects the group operation. Let \(I\) be a countably infinite set; let \(M\) be a set equipped with an action of the group \(\text{Perm}(I)\) of finitary permutations of \(I\). A support for \(m \in M\) is a subset \(A \subseteq I\) such that for all \(\sigma \in \text{Perm}(I)\), \(i \in A\), \(\sigma = i\) implies \(\sigma \cdot m = m\). A nominal set is a \(\text{Perm}(I)\)-set all of whose elements have finite support. In this case, if \(m \in M\), we write \(\text{supp}(m)\) for the smallest support of \(m\). The complementary notion of support is freshness: \(i \in I\) is fresh for \(m \in M\) if \(i \not\in \text{supp}(m)\). We write \(i\) for the transposition which swaps \(i\) and \(j\).

We will make use of the following characterization of support in terms of transpositions: \(A \subseteq I\) supports \(m \in M\) if and only if for every \(i, j \in I - A\) it holds that \((i \cdot j) \cdot m = m\). As a consequence, for all \(i, j \in I\), if they are fresh for \(m \in M\) then \((i \cdot j) \cdot m = m\).

3.2 Memory Structures
A memory structure \(\text{Mem} = (\text{Mem}, I, \cdot, \mathcal{L})\) consists of an infinite, countable set \(I\) whose elements \(i, j, k, \ldots\) we call addresses, a nominal set \((\text{Mem}, \cdot)\) of each of whose elements we call memory states, or more shortly, memories, and a finite set \(\mathcal{L}\) of operations.

We write \(\mathcal{I}^*\) for the set of all tuples made from elements of \(I\). A tuple is denoted with \((i_1, \ldots, i_n)\), or with \(I\). To a memory structure are associated the following maps:

- \(\text{test} : I \times \text{Mem} \rightarrow \text{DST}(\text{Bool} \times \text{Mem})\) (Observe that the set \(\text{Mem}\) might be updated by the operation test: for this reason, it also appears in the codomain – See Remark 15),
- \(\text{update} : \mathcal{I}^* \times \mathcal{L} \times \text{Mem} \rightarrow \text{Mem}\) (partial map),
- \(\text{arity} : \mathcal{L} \rightarrow \mathbb{N}\),

and the following three properties.

(1) The maps test and update respect the group action:
- \(\sigma \cdot \text{test}(i, m) = \text{test}(\sigma(i), \sigma \cdot m)\),
- \(\sigma \cdot \text{update}(i, x, m) = \text{update}(\sigma(i), x, \sigma \cdot m)\),

where the action of \(\text{Perm}(I)\) is extended in the natural way to distributions and pairing with booleans.

(2) The action of a given operation on the memory is only defined for the correct arity. More precisely, \(\text{update}(i_1, \ldots, i_n, x, m)\) is defined if and only if the \(i_k\)'s are pairwise disjoint and \(\text{arity}(x) = n\). (3) Disjoint tests and updates commute: assume that \(i \neq j\), that \(j\) does not meet \(k\), and that \(k\) and \(k'\) are disjoint. First, updates on \(k\) and \(k'\) commute: \(\text{update}(k, x, \text{update}(k', x', m)) = \text{update}(k', x', \text{update}(k, x, m))\). Then, tests on \(i\) commute with tests on \(j\) and tests of \(j\) commute with updates on \(k\). We pictorially represent these equations in Fig. 1. The drawings are meant to be read from top to bottom and represent the successive memories along action. Probabilistic behavior is represented with two exiting arrows, annotated with their respective probability of occurrence, and the boolean resulting from the test operation. Intermediate memories are unnamed and represented with “..”. We write the formal equations in Appendix A.

3.3 Instances of Memory Structures
The structure of memory is flexible and can accommodate several choice effects. Let us give some relevant examples. Typically, here \(I = \mathbb{N}\), but any countable set would do the job.

3.3.1 Deterministic, Integer Registers
The simplest instance of memory structure is the deterministic one, corresponding to the case of classical PCF (this subsumes, in particular, the case studied in [28]). Memories are simply functions \(m\) from \(I\) to \(\mathbb{N}\), of value 0 apart for a finite subset of \(I\). The test on address \(i\) is deterministic, and tests whether \(m(i) = 0\) or not. Operations in \(\mathcal{L}\) may include the unary predecessor and successor, and for example the binary max operator.

Example 12. A typical representation of this deterministic memory is a sequence of integers: indexes correspond to addresses and coefficients to values. A completely free memory is for example the sequence \(m_0 = (0, 0, 0, \ldots)\). If \(S\) corresponds to the successor and \(P\) to the predecessor, here is what happens to the memory for some operations. The memory \(m_1 := \text{update}(0, S, m_0)\) is \((1, 0, 0, 0, \ldots)\), the memory \(m_2 := \text{update}(1, S, m_1)\) is \((1, 1, 0, 0, 0, \ldots)\), and the memory \(m_3 := \text{update}(0, P, m_2)\) is \((0, 1, 0, 0, \ldots)\). Finally, \(\text{test}(1, m_3) = \text{false}, (0, 1, 0, 0, \ldots)\). Note that we do not need to keep track of an infinite sequence: a dynamic, finite list of values would be enough. We’ll come back to this in Sec. 3.3.3.

Remark 13. The equations on memory structures enforce the fact that all fresh addresses (i.e., not on the support of the nominal set) have equal values. Note that however the conditions do not impose any particular “default” value. These equations also state that, in the deterministic case, a test action on \(i\) can only modify the memory at address \(i\). Otherwise, it could for example break the commutativity of update and test (unless \(\mathcal{L}\) contains trivial operations, only).
3.3.2 Probabilistic, Boolean Registers

When the test operator is allowed to have a genuinely probabilistic behavior, the memory model supports the representation of probabilistic boolean registers. In this case, a memory \( m \) is a function from \( I \) to the real interval \([0, 1]\), whose values represent probabilities of observing “true.” The test on address \( i \) could return

\[ m(i) = \{ (\text{true}, m(i \mapsto 1)) \} + (1 - m(i)) \{ (\text{false}, m(i \mapsto 0)) \} \]

Operations in \( L \) may for example include a unary “coin flipping” operation setting the value associated to \( i \) to some fixed probability.

Example 14. If as in Example 12 we represent the memory as a sequence, a memory filled with the value “false” would be \( m_0 = (0, 0, 0, \ldots) \). Assume \( c \) is the unary operation placing a fair coin at the corresponding address; if \( m_1 \) is \( \text{update}(0, c, m_0) \), we have \( m_1 = (\frac{1}{2}, 0, 0, 0, \ldots) \). Then \( \text{test}(0, m_1) \) is the distribution \( \frac{1}{2} \{ (\text{false}, (0, 0, 0, 0, \ldots)) \} + \frac{1}{2} \{ (\text{true}, (1, 0, 0, 0, \ldots)) \} \).

3.3.3 Quantum Registers

A standard model for quantum computation is the QRAM model: quantum data is stored in a memory seen as a list of (quantum) registers, each one holding a qubit which can be acted upon. The model supports three main operations: creation of a new register, measurement of a register, and application of unitary gates on one or more registers, depending on the arity of the gate under scrutiny. This model has been used extensively in the context of quantum lambda-calculi [11, 13, 24], with minor variations. The main choice to be made is whether measurement is destructive (i.e., if one uses garbage collection) or not (i.e., the register is not reclaimed).

A Canonical Presentation of Quantum Memory. To fix things, we shall concentrate on the presentation given in [13]. We briefly recall it. Given a qubit, a memory is a normalized vector in \((C^2)^{\otimes n}\) (equivalent to a ray). A linking function maps the position of each qubit in the list to some pointer name. The creation of a new qubit turns the memory \( \phi \in (C^2)^{\otimes n} \) into \( \phi \otimes \emptyset \in (C^2)^{\otimes (n+1)} \). The measurement is destructive: if \( \phi = \alpha_0 \phi_0 + \alpha_1 \phi_1 \), then measuring \( \phi \) returns \( \sum \phi_{0,i} \otimes \psi_{0,i} \) with probability \( |\alpha_i|^2 \). Finally, the application of a \( k \)-ary unitary gate \( U \) on \( \phi \in (C^2)^{\otimes n} \) simply applies the unitary matrix corresponding to \( U \) on the vector \( \phi \). The language comes with a chosen set \( U \) of such gates.

Quantum Memory as a Nominal Set. The quantum memory can be equivalently presented using a memory structure: in the following we shall refer to it as \( Q \). The idea is to use nominal set to make precise the hand-waved “pointer name”, and formalize the idea of having a finite core of “in use” qubits, together with an infinite pool of fresh qubits. Let \( F_0 \) be the set of (set-)maps from \( I \) (the infinite, countable set of Sec. 3.2) to \( \{ 0, 1 \} \) that have value 0 everywhere except for a finite subset of \( I \). We have a memory structure as follows.

\( I \) is the domain of the set-maps in \( F_0 \). The nominal set \((\text{Mem}, \cdot)\) is defined with \( \text{Mem} = H_0 \), i.e. the Hilbert space built from finite (complex) linear combinations over \( F_0 \), while the group action \( \cdot \) corresponds to permutation of addresses; \( \sigma \cdot m \) is simply the function-composition of \( \sigma \) with the elements of \( F_0 \) in superposition in \( m \). The support of a particular memory \( m \) is finite: it consists of the set of addresses that are not mapped to 0 by some (set-)function in the superposition. The set of operations \( L \) is the chosen set \( U \) of unitary gates. The arity is the arity of the corresponding gate.

Finally, the \( \text{update} \) and \( \text{test} \) operations correspond respectively to the application of an unitary gate, and to a measurement followed by a (classical) boolean test on the result. We omit the formalization of these operations in the nominal set setting: instead we show how this presentation in terms of nominal sets is equivalent to the previous more canonical one.

Equivalence of the Two Presentations. Let \( m \in Q \). We can always consider a finite subset of \( I \), say \( I_0 = \{ i_0, \ldots, i_n \} \) for some integer \( n \) such that all other addresses are fresh. As fresh values are 0 in \( m \), then \( m \) is a superposition of sequences that are equal to 0 on \( I \setminus I_0 \). Then \( m \) can be represented as “\( \phi \otimes [000\ldots] \)” for some (finite) vector \( \phi \). We can omit the last \([000\ldots] \) and only work with the vector \( \phi \); we are back to the canonical presentation of quantum memory. Update and test can then be defined on the nominal set presentation through this equivalence.

Equations. Memory structures come with equations, which are indeed satisfied by quantum memories. Referring to Sec. 3.2 : (1) is simply renaming of qubits, (2) is a property of applying a unitary, and (3) holds because of the equations corresponding to the tensor of two unitaries or the tensor of a unitary and a measurement (see Remark 1).

Remark 15. The quantum case makes clear why \( \text{Mem} \) appears in the codomain of \( t \): in general the measurement of a register collapses the global state of the memory (see Sec. 1.1.1). The modified memory therefore has to be returned together with the result.

3.4 Overview of the Forthcoming Sections

We use memory structures to encapsulate effects in three different settings. In Sec. 4, we enrich proof nets with a memory, in Sec. 5, we enrich token machines with a memory, while in Sec. 6, we equip PCF terms with a memory. The construction is uniform for all the three systems, to which we refer as operational systems, as opposite to the base rewrite systems on top of which we build (see Sec. 1.3).

4. Program Nets and Their Dynamics

In this section, we introduce program nets. The base rewrite system on which they are built is a variation of SMEYLL nets, as introduced in [28]. SMEYLL nets are MELL (Multiplicative Exponential Linear Logic) proof nets extended with fixpoints (\( Y \)-boxes) which model recursion, additive boxes (\( \perp \)-boxes) which capture the if-then-else construct, and a family of sync nodes, introducing explicit synchronization points in the net.

The novelty of this section is the operational system which we introduce in Sec. 4.3, by means of our parametric construction: given a memory structure and SMEYLL nets, we define program nets and their reduction. We prove that program nets are a PARS system which satisfies the diamond property, and therefore confluence and uniqueness of normal forms both hold. Program nets also satisfy cut elimination, i.e., deadlock-freeness of nets rewriting.

4.1 Formulas

The language of formulas is the same as for MELL. In this paper, we restrict our attention to the constant-only fragment, i.e.,

\[ A ::= 1 \mid \perp \mid A \otimes A \mid A \uplus A \mid A \rightarrow A \mid ?A. \]

The constants \( 1, \perp \) are the units. As usual, linear negation \( \downarrow \) is extended into an involution on all formulas: \( A^{\downarrow} \equiv A, \downarrow \equiv \perp, \endnote{1} (A \otimes B)^{\downarrow} \equiv A^{\downarrow} \otimes B^{\downarrow}, (A \uplus B)^{\downarrow} \equiv A^{\downarrow} \uplus B^{\downarrow} \). Linear implication is a defined connective: \( A \rightarrow B \equiv A^{\downarrow} \uplus B \). Positive formulas \( P \) and negative formulas \( N \) are respectively defined as: \( P ::= 1 \mid P \otimes P \), and \( N ::= \perp \mid N \rightarrow N \).
Exponentials and Fixpoints

A node occurs if it is a node of \( R \), which is a pre-net of conclusions \( \Gamma \) and \( n \) conclusions of the same type \( P_1, P_2, \ldots, P_n \), respectively. A sync node with \( n \) premises and conclusions is drawn as \( n \) many black squares connected by a line as in the figure. The total number of 1’s in the \( P_i \)’s is called the arity of the sync node.

The “skeleton” of the program in Example 3 could be encoded\(^2\) as in the LHS of Fig. 5. The recursive function \( f \) is represented with a Y-box, and has type \( ! 1 \rightarrow 1 \rightarrow 1 \rightarrow 1 \). The test is encoded with a \( \perp \)-box: in one case we forget the function \( f \) by using \( \perp \) and simply return a one node, and in the other case we apply a Hadamard gate, which is represented with a (unary) sync node. To the “\( \perp \)” part of the let-rec corresponds the dereliction node \( ?d \), triggering reduction. With the rules presented in the previous paragraph, the net rewrites according to Fig. 5, where the Y-box has

\(^2\) Precisely speaking, the nets shown in Fig. 5 are those obtained by the translation given in Fig. 15 and 16 (in the Appendix), with a bit of simplification for clarity of discussion and due to lack of space.

4.2 SMEYLL Nets

A SMEYLL net is a pre-net (i.e. a well-typed graph) which fulfills a correctness criterion.

**Pre-Nets.** A pre-net is a labeled directed graph \( R \) built over the alphabet of nodes represented in Fig. 2.

**Edges.** Every edge in \( R \) is labeled with a formula; the label of an edge is called its type. We call those edges represented below (resp. above) a node symbol conclusions (resp. premises) of the node. We will often say that a node "has a conclusion (premise) \( A \)" as shortcut for "has a conclusion (premise) of type \( A \)". When we need more precision, we explicitly distinguish an edge and its type and we use variables such as \( e, f \) for the edges. Each edge is a conclusion of exactly one node and is a premise of at most one node. Edges which are not premises of any node are called the conclusions of the net. Nodes. The sort of each node induces constraints on the number and the labels of its premises and conclusions. The constraints are graphically shown in Fig. 2. A sync node \( n \in \mathbb{N} \) has \( n \) premises of types \( P_1, P_2, \ldots, P_n \), respectively and \( n \) conclusions of the same types \( P_1, P_2, \ldots, P_n \), where each \( P_i \) is a positive type. A sync node with \( n \) premises and conclusions is drawn as \( n \) many black squares connected by a line as in the figure. The total number of 1’s in the \( P_i \)’s is called the arity of the sync node.

We call boxes the nodes \( \perp, !, \text{ and } Y \). The leftmost conclusion of a box is said to be principal, while the other ones are auxiliary. The node \( \perp \) has conclusion \( \{ \perp, \Gamma \} \) with \( \Gamma \) possibly empty. To each \( ! \)-box (resp. \( Y \)-box) is associated a content, i.e. a pre-net of conclusions \( \{ A, ?T \} \) (resp. \( \{ A, ?A^+, !T \} \}). To each \( \perp \)-box are associated a left and a right content: each content is a pair \( (b, S) \), where \( b \) is a new node that has no premise and one conclusion \( \perp \), and \( S \) is a pre-net of conclusions \( \Gamma \). We represent a box \( b \) and its content(s) as in Fig. 3. The nodes and edges in the content are said to be inside \( b \). As is standard, we often call a crossing of the box border a door, which we treat as a node. We then speak of premises and conclusion of the principal (resp. auxiliary) door. Observe that in the case of \( \perp \)-box, the principal door has a left and a right premise.

**Depth.** A node occurs at depth 0 or on the surface in the pre-net \( R \) if it is a node of \( R \). It occurs at depth \( n + 1 \) in \( R \) if it occurs at depth \( n \) in a pre-net associated to a box of \( R \).

**Nets.** A net is given by a pre-net \( R \) which satisfies the correctness criterion of [28], together with a total map \( \text{mname}_R : \text{SyncNode}(R) \rightarrow \mathcal{L} \), where \( \mathcal{L} \) is a finite set of names and \( \text{SyncNode}(R) \) is the set of sync nodes appearing in \( R \) (including those inside boxes); the map \( \text{mname}_R \) is simply naming the sync nodes. From now on, we write \( R \) for the triple \( (R, \mathcal{L}, \text{mname}_R) \).

Correctness is defined by means of switching paths. A switching path in the pre-net \( R \) is an undirected path on the graph \( R \) (i.e. \( R \) is regarded as an undirected graph) which uses at most one of the two premises for each \( ?Y \) and \( ?c \) node, and at most one of conclusions for each sync node. A pre-net is correct if none of its switching paths is cyclic, and the content of each of its box is itself correct.

**Reduction Rules.** Fig. 4 describes the rewriting rules on nets. Note that the redex in the top row has two possible reduction rules, \( n_0 \) and \( n_1 \). Note also the \( y \) reduction, which captures the recursive behavior of the \( Y \)-box as a fixpoint (we illustrate this in the example below.) The metavariables \( X, X_1, X_2 \) of Fig. 4 range over \( \{ !, Y \} \) and are used to uniformly specify reduction rules involving exponential boxes (i.e., \( X \)'s can be either \( ! \) or \( Y \)). The reduction of net has two constraints: (1.) surface reduction, i.e. a reduction step applies only when the redex is at depth 0, and (2.) exponential steps are closed, i.e., they only take place when the \( !A \) premise of the cut is the principal conclusion of a box with no auxiliary conclusion. We come back on the former in Sec. 7.1.2.

As expected, the net reduction preserves correctness.

**Example.** The “skeleton” of the program in Example 3 could be encoded\(^2\) as in the LHS of Fig. 5. The recursive function \( f \) is represented with a Y-box, and has type \( ! 1 \rightarrow 1 \rightarrow 1 \rightarrow 1 \). The test is encoded with a \( \perp \)-box: in one case we forget the function \( f \) by using \( \perp \) and simply return a one node, and in the other case we apply a Hadamard gate, which is represented with a (unary) sync node. To the “\( \perp \)” part of the let-rec corresponds the dereliction node \( ?d \), triggering reduction. With the rules presented in the previous paragraph, the net rewrites according to Fig. 5, where the Y-box has
been unwound once. From there, we could reduce the one node with the sync node H, but of course doing so we would not handle the quantum memory. In order to associate to reductions an action on the memory, we need a bit more, namely the notion of a program net which is introduced in Sec. 4.3.

4.3 Program Nets

Let Inputs(R) be the set of all occurrences of 1’s which are conclusions of one nodes at the surface, and of all occurrences of \( \perp \)’s which appear in the conclusions of \( \perp \).

Definition 16 (Program Nets). Given a memory structure \( \text{Mem} = (\text{Mem}, I, L) \), a raw program net on \( \text{Mem} \) is a tuple \( (R, \text{ind}_R, m) \) such that

- \( R \) is a SMALLELL net (with \( \text{mname}_R : \text{SyncNode}(R) \to L \)),
- \( \text{ind}_R : \text{Inputs}(R) \to I \) is an injective partial map that is however total on the occurrences of \( \perp \),
- \( m \in \text{Mem} \).

We require that the arity of each sync node \( s \) matches the arity of \( \text{mname}_R(s) \). Please observe that in the second item in Definition 16, the occurrences of 1’s belonging to Inputs(R) are not necessarily in the domain of indR; if they are, we say that the corresponding one node is active. Program nets are the equivalence class of raw program nets over permutation of the indexes. Formally, let \( \sigma(R, \text{ind}_R, m) = (R', \sigma \cdot \text{ind}_R, \sigma \cdot m) \), for \( \sigma \in \text{Perm}(I) \). The equivalence class \( R = \{ (R, \text{ind}_R, m) \mid \sigma(R, \text{ind}_R, m) \} \) is \( \{ \sigma(R, \text{ind}_R, m) \mid \sigma \in \text{Perm}(I) \} \). We use the symbol \( \sim \) for the equivalence relation on raw program nets. \( N \) indicates the set of program nets.

Reduction Rules. We define a relation \( \sim \subseteq N \times \text{DST}(N) \), making program nets into a PARS. We first define the relation \( \sim \) over raw program nets. Fig. 6 summarizes the reductions in a graphical way; the function indR is represented by the dotted lines.

1. Link. If \( n \) is a one node of conclusion \( x \), with \( \text{ind}_R(x) \) undefined, then \( (R, \text{ind}_R, m) \sim_{\text{link}(n,i)} ((R, \text{ind}_R \cup \{ x \mapsto i \}, m)^1) \) where \( i \in I \) is fresh both in \( \text{ind}_R \) and \( m \).

2. Update. If \( R \sim \{ R' \}, \) and \( s \) is the sync node in the redex, then \( (R, \text{ind}_R, m) \sim_{\text{update}(s)} ((R', \text{ind}_R, \text{update}(I, \perp, m)^1) \)

where \( l \) is the label of \( s \), and \( i \) are the addresses of its premises.

3. Test. If \( R \sim_{\text{ind}_R} R_0 \) and \( R \sim_{\text{ind}_R} R_1 \), and \( i \) is the address of the premise 1 of the cut, then \( (R, \text{ind}_R, m) \sim_{\text{test}(i)} \left( \text{test}(i, m)[\text{false} := (R_0, \text{ind}_R, m), \text{true} := (R_1, \text{ind}_R, m)] \right) \), where \( \text{ind}_R \) (resp. \( \text{ind}_R \)) is the restriction of \( \text{ind}_R \) to Inputs(\( \text{R} \)) (resp. Inputs(\( \text{R} \))).

4. Otherwise, if \( R \sim_{x \in I} R^* \) with \( x \in \{ m, u_0, u_1 \} \), then we have \( (R, \text{ind}_R, m) \sim_{x} \left( \left( R', \text{ind}_R, m \right)^1 \right) \) (observe that none of these rules modify the domain of \( \text{ind}_R \)).

The relation \( \sim \) extends immediately to program nets (by slight abuse of notation we use the same symbol); Lem. 17 guarantees that the relation is well defined. We write \( (R, \text{ind}_R, m) \sim \mu \) for the reduction of the redex \( r \) in the raw program net \( (R, \text{ind}_R, m) \).

Lemma 17 (Reduction Preserves Equivalence). Suppose that \( (R, \text{ind}_R, m) \sim \mu \) and \( (R', \sigma \cdot \text{ind}_R, \sigma \cdot m) \sim \nu \), then \( \mu \sim \nu \).

Proof. Let us check the rule \( \sim_{\text{test}(i)} \). Suppose \( (R', \text{ind}_R', m') = (\sigma(R', \text{ind}_R', m), (R, \text{ind}_R, m) \sim_{\text{test}(0)} \mu = (\{ R_0, \text{ind}_R, m_0 \}^{0}, \{ R_1, \text{ind}_R, m_1 \}^{1}) \), and \( (R', \text{ind}_R', m') \sim_{\text{test}(i)} \nu = (\{ R'_0, \text{ind}_R', m'_0 \}^{0}, \{ R'_1, \text{ind}_R', m'_1 \}^{1}) \) by reducing the same redex \( r \). It suffices to show that \( \nu \sim \mu \). Element-wise, we have to check \( R'_0 = R_0, \text{ind}_R = \sigma \circ \text{ind}_R, m'_0 = \sigma \cdot m_0, \) and \( p_i = p_i \) for \( i \in \{ 0, 1 \} \). The first two follow by definition of \( \sim_{\text{test}(i)} \) and the last two follow from the equation \( \sigma(\text{test}(i, m)) = \text{test}(\sigma(i), \sigma \cdot m) \). The other rules can be similarly checked.

Remark 18. In the definition of the reduction rules:

- Link is independent from the choice of \( i \). If we chose another address \( j \) with the same conditions, then we would have gone to \( (R, \text{ind}_R \cup \{ x \mapsto j \}, m) \). However this does not cause a problem: by using a permutation \( \sigma = \{ i \mapsto j \} \), since \( \sigma \cdot m = m \) we have \( \sigma(R, \text{ind}_R \cup \{ x \mapsto i \}, m) = (R, \text{ind}_R \cup \{ x \mapsto j \}, m) \) and therefore \( (R, \text{ind}_R \cup \{ x \mapsto i \}, m) \) and \( (R, \text{ind}_R \cup \{ x \mapsto j \}, m) \) are as expected the exact same program net.

- In the rules Update and Test, the involved one nodes are required to be active.

The pair \( (N, \sim) \) forms a PARS. Reduction can happen at different places in a net, however the diamond property allows us to deal with this seamlessly.
Proposition 19 (Program Nets are Diamond). The PARS \((N, \rightarrow)\) satisfies the diamond property.

Theorem 21 (Deadlock-Freeness of Net Reduction). Let \(R = ([R, \text{ind}_R, m])\) be a program net such that no \(\bot, \top\) appears in the conclusions of the net \(R\). If \(R\) contains cuts, a reduction step is always possible.

Corollary 22 (Cut Elimination). With the same hypothesis as above, if \(R \neq \top\), (i.e. no further reduction is possible) then \(R\) is cut free.

Example. The net in LHS of Fig. 5 can be embedded into a program net with a quantum memory of empty support: \((0, 0, 0, \ldots) \equiv \text{"[000 ... ]"}. It reduces according to Fig. 5, with the same memory. The next step requires a \(\sim \text{update}\)-rewrite step to attach a fresh address—say, 0—to the one node at surface. The H-sync node then rewrites with a \(\sim \text{update}\)-step, and we get the program net (A) in Fig. 7 with the “update” action applied to the memory: the memory corresponds to \(\otimes (0) \otimes (00 ... ) \otimes [\text{true}]\). From there, a choice reduction is in order: it uses the “test” action of the memory structure, which, according to Sec. 3.3.3 corresponds to the measurement of the qubit at address 0. This yields the probabilistic superposition of the program nets (B1) and (B2). As the net in (B1) is the LHS of Fig. 5, it reduces to (C1) (dashed arrow (a)), similar to (A) modulo the fact that the address 0 was not fresh: the \(\sim \text{update}\)-rewrite step cannot yield 0: here we choose 1. Note that we could have chosen any other non-zero number as the address. The program net (B2) rewrites to (C2) (dashed arrow (b)): the weakening node erases the Y-box, and a fresh variable is allocated. In this case, the address 0 is indeed fresh and can be picked.

5. A Memory-Based Abstract Machine

In this section we introduce a class of memory-based token machines, called the MSIAM (Memory-based Synchronous Interaction Abstract Machine). The base rewrite system on which the MSIAM is built, is a variation\(^1\) of the SIAM multi-token machine from [28], which we recall in Sec. 5.1. The specificity of the SIAM is to allow not only parallel threads, but also interaction among them, i.e. synchronization. Synchronization happens in particular at the sync nodes (unsurprisingly, as these are nodes introduced with this purpose), but also on the additive boxes (the \(\bot\)-box). The transitions at the \(\bot\)-box model choice: as we see below, when the flow of computation reaches the \(\bot\)-box (i.e. the tokens reach the auxiliary doors), it continues on one of the two sub-components, depending on the tokens which are positioned at the principal door.

The original contribution of this section is contained in Sections 5.2 through 5.4, where we use our parametric construction to define the MSIAM \(M_R\) for \(R\) as a PARS consisting of a set of states \(S\), and a transition relation \(\rightarrow \subset S \times DST(S)\), and establish its main properties, in particular Deadlock-Freeness (Th. 26), Invariance (Th. 27) and Adequacy (Th. 28).

5.1 SIAM

Let \(R\) be a net. The SIAM for \(R\) is given by a set of states and a transition relation on states. Most of the definitions are standard.

\[^{1}\text{In this paper we make this transition non-deterministic; otherwise there is no major difference.}\]
**Multitoken rules**

![Diagram](image)

### Figure 9. SIAM: Deterministic Transition Rules.

<table>
<thead>
<tr>
<th>Rule</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>(i)</td>
<td>Multitoken Rules</td>
</tr>
<tr>
<td>(ii)</td>
<td>Exponential Boxes</td>
</tr>
<tr>
<td>(iii)</td>
<td>Exponential Nodes</td>
</tr>
<tr>
<td>(iv)</td>
<td>Multiplicatives</td>
</tr>
</tbody>
</table>

Multitoken Conditions. The rules marked by (i), (ii), (iii), and (iv) in Fig. 9 require the tokens to interact, which is formalized by multi-token conditions. Such conditions allow, in particular, to capture choice and synchronization. Below we give an intuitive presentation; we refer to [28, 42] for the formal details. For convenience we also recall them in Appendix C.

### Synchronization, rule (i).

To cross a sync node $l$, all the positions on the premises of $l$ (for the same box stack $t$) must be filled; intuitively, having the same $t$, means that the positions all belong to the same copy of $l$. Only when all the tokens have reached $l$, they can cross it; they do so simultaneously.

### Choice, rule (ii).

Any token arriving at a $\perp$-box on an auxiliary door must wait for a token on the principal door to have made a choice for either of the two contents, $S_0$ or $S_1$; a token $(e, s, t)$ on the conclusions $Γ$ of the $\perp$-box will move to $S_0$ (resp. $S_1$) only if the principal door of $S_0$ (resp. $S_1$) has a token with the same $t$.

### Fixpoints, rule (iii).

The rules marked by (iii) and (iv) also carry a multi-token condition, but in a more subtle way: a token is enabled to start its journey on a one or $\bot$ node only when its box has been opened; this reflects in the SIAM the constraint of surface reduction of nets.

### 5.2. MSIAM

Similarly to what we have done for nets, we enrich the machine with a memory, and use the SIAM and the operations on the memory to define a PAR.

#### MSIAM States.

Given a memory structure $\text{Mem} = (\text{Mem}, I, L)$ and a raw program net $(R, \text{ind}_R, m_R)$ on Mem, a raw state of the MSIAM $\mathcal{M}_R$ is a tuple $(T, \text{ind}_T, m_T)$ where:

- $T$ is a state of $\mathcal{M}_R$,
- $\text{ind}_T : \text{START} \to I$ is a partial injective map,
- $m_T \in \text{Mem}$.

States are defined as the equivalence class $T = [(T, \text{ind}_T, m_T)]$ of row states over permutations, with the action of $\text{Perm}(I)$ on tuples being the natural one.

#### MSIAM Transitions.

Let $R$ be a program net, and $T$ be a state $[(T, \text{ind}_T, m_T)]$ of $\mathcal{M}_R$. We define the transition $T \rightarrow \mu \in S \times \\text{DST}(S)$. As we did for program nets, we first give the definition on raw states. The definition depends on the SIAM transitions for $T$.

Let us consider the possible cases.

1. **Link.** Assume $T \rightarrow (l \uparrow) U$ (Fig. 9), and let $n$ be the one node, $x$ its conclusion, and $p$ the new token in $U$. We set

   $(T, \text{ind}_T, m) \rightarrow \text{link}(n, i) (U, \text{ind}_T \cup \{\text{orig}(p) \rightarrow i\}, m)$

   where we choose $i = \text{ind}_T(x)$ if the one node is active, and otherwise an address $i$ which is fresh for both $\text{ind}_T$ and $(m)$.

2. **Update.** Assume $T \rightarrow (l \downarrow) U$ (Fig. 9), $i$ is the name associated to the sync node, and $l$ and $\overline{l}$ are the addresses which are associated to its premises (by composing $\text{orig}$ and $\text{ind}$), then

   $(T, \text{ind}_T, m) \rightarrow \text{update}(a) \{U, \text{ind}_T, \text{update}(l, \overline{l}, m)\}$. 

3. **Test.** Assume $T \rightarrow^{\text{test}} T_0$ and $T_0 \rightarrow T_1$ (Fig. 8, non-deterministic transition). If $p \in T$ is the token appearing in the redex (Fig. 8), and $i$ the addresses that $\text{ind}_T$ associates to $\text{orig}(p)$, then

   $(T, \text{ind}_T, m) \rightarrow \text{test}(i, m) \{\text{false} := (T_0, \text{ind}_{T_0}, \text{true} := (T_1, \text{ind}_{T_1})\}.

4. In all the other cases: if $T \rightarrow U$ then $(T, \text{ind}_T, m) \rightarrow \{U, \text{ind}_T, m\}$. 

Let $\mathcal{M} = (R, \text{ind}_R, m_R)$. The initial state of $\mathcal{M}_R$ is $S_0 = [(R, \text{ind}_R, m_R)]$, where $\text{ind}_R$ is only defined on the initial positions: if $p \in \text{INIT}_R$ and $x$ is the occurrence of $\perp$ corresponding to $p$, then $\text{ind}_R(p) = \text{ind}_R(x)$. A state $[(T, \text{ind}_T, m_T)]$ is final if $T$ is final.

In the next sections, we study the properties of the machine, and show that the MSIAM is a computational model for $\mathcal{N}$. 


Example. We informally develop in Fig. 10 an execution of the MSIAM for the LHS net of Fig. 5. In the first panel (A) tokens (a) and (b) are generated. Token (a) reaches the principal door of the Y-box, which corresponds to opening a first copy. Token (b) enters the Y-box and hits the ⊥-box. The test action of the memory triggers a probabilistic distribution of states where the left and the right components of the ⊥-box are opened: the corresponding sequences of operations are Panels (B1) and (B2) for the left and right sides.

In Panel (B1): the left-side of the ⊥-box is opened and its one-node emits the token (c) that eventually reaches the conclusion of the net. In Panel (B2): the right-side of the ⊥-box is opened and tokens (c) and (d) are emitted. Token (d) opens a new copy of the Y-box, while token (c) hits the ⊥-box of this second copy. The test action of the memory again spawns a probabilistic distribution.

We focus on panel (C10) on the case of the opening of the left-side of the ⊥-box: there, a new token (e) is generated. It will exit the second copy of the Y-box, go through the first copy and exit to the conclusion of the net.

5.3 MSIAM Properties, and Deadlock-Freeness
Intuitively, a run of the machine $M_R$ is the result of composing transitions of $M_R$, starting from the initial state $I_R$ (composition being transitive composition). We are not interested in the actual order in which the transitions are performed in the various components of a distribution of states. Instead, we are interested in knowing which distributions of states are reached from the initial state. This notion is captured well by the relation $\rightsquigarrow$ (see Sec. 2.1). We will say that a run of the machine $M_R$ reaches $\mu \in \text{DST}(\mathcal{S}_R)$ if $I_R \rightsquigarrow \mu$. We will also use the expression “a run of $M_R$ reaches a state $T$” if $I \rightsquigarrow \mu$ with $T \in \text{SUPP}(\mu)$.

An analysis similar to the one done for program nets shows the next lemma (Lem. 24) and therefore Prop. 25:

**Lemma 24 (Diamond).** The relation $\rightarrow$ satisfies the diamond property.

**Proposition 25 (Confluence, Uniformity of Normal Forms).** The relation $\rightarrow$ satisfies confluence, uniformity, and uniqueness of normal forms.

By the results we have studied in Sec. 2, we thus conclude that all runs of $M_R$ have the same behavior with respect to the degree of termination, i.e. if $I_R$ p-normalizes following a certain sequence of reductions, it will do so whatever sequence of reductions we pick. We say that the machine $M_R$ p-terminates if $I_R$ p-terminates (see Def. 4).

**Deadlocks.** A terminal state $T \rightsquigarrow M_R$ can be final or not. A non-final terminal state is called a deadlocked state. Because of the inter-dependencies among tokens given by the multi-token conditions, a multi-token machine is likely to have deadlocks. We are however able to guarantee that any MSIAM machine is deadlock-free, whatever is the choice for the memory structure.

**Theorem 26 (Deadlock-Freeness of the MSIAM).** Let $R$ be a program net of conclusion 1: if $I_R \rightsquigarrow \mu$ and $T \in \text{SUPP}(\mu)$ is terminal, then $T$ is a final state.

The proof (see Appendix D.3) relies on the diamond property of the machine (more precisely, uniqueness of the normal forms).

5.4 Invariance and Adequacy
The machine $M_R$ gives a computational semantics to $R$. The semantics is invariant under reduction (Th. 27); the adequacy result (Th. 28) relates convergence of the machine and convergence of the nets. We define the convergence of the machine as the convergence of its initial state:

$M_R \psi_p$ if $I_R \rightsquigarrow \mu$ and $T \in \text{SUPP}(\mu)$. 

**Theorem 27 (Invariance).** Let $R$ be a program net of conclusion 1. Assume $R \rightsquigarrow \sum_i p_i \cdot \{ R_i \}$. Then we have that $M_R \psi_q$ if and only if $M_{R_i} \psi_q$, with $\sum_i (p_i \cdot q_i) = q$.

**Theorem 28 (Adequacy).** Let $R$ be a program net of conclusion 1. Then, $M_R \psi_p$ if and only if $R \psi_p$.

The proofs of invariance and adequacy, are both based on the diamond property of the machine, and on a map—which we call transformation—which allows us to relate the rewriting of program nets with the MSIAM. To obtain the proofs, we need to establish a series of technical results which we give in Appendix D.

6. A PCF-style Language with Memory Structure
We introduce a PCF-style language which is equipped with a memory structure, and is therefore parametric on it. The base type will correspond to elements stored in the memory, and the base operations to the operations of the memory structure.

6.1 Syntax and Typing Judgments
The language PCFLL which we propose is based on Linear Logic, and is parameterized by a choice of a memory structure $\text{Mem}$.

The terms $(M, N, P)$ and types $(A, B)$ are defined as follows:

$$M, N, P ::= x \mid \lambda x. M \mid MN \mid \text{let } f x = M \text{ in } N \mid \text{letrec } f x = M \text{ in } N \mid \text{new } c \mid \text{if } P \text{ then } M \text{ else } N,$$

$$A, B ::= \alpha \mid A \to B \mid A \otimes B \mid !A,$$

where $c$ ranges over the set of memory operations $\mathcal{L}$. A typing context $\Delta$ is a (finite) set of typed variables $\{ x_1 : A_1, \ldots, x_n : A_n \}$, and a typing judgment is written as $\Delta \vdash M : A$. An empty typing context is denoted by $\vdash$. We say that a type is linear if it is not of the form $!A$. We denote by $\downarrow \Delta$ a typing context with only non-linearly typed variables. A typing judgment is valid if it can be derived from the set of typing rules presented in Fig. 11. We require $M$ and $N$ to have empty context in the typing rule of $\text{if } P \text{ then } M \text{ else } N$. The requirement does not reduce expressivity as typing contexts can always be lambda-abstracted. The typing rules make use of a notion of values, defined as follows:

$$U, V ::= x \mid \lambda x. M \mid \langle U, V \rangle \mid c.$$

6.2 Operational Semantics
The operational semantics for PCFLL is similar to the one of [13], and is inherently call-by-value. Indeed, being based on Linear Logic,
the language only allows the duplication of "!"-boxes, that is, normal forms of "!"-type: these are the values. The operational semantics is in the form of a PARS, written \( \rightarrow \).

The PARS is defined using a notion of reduction context \( C[-] \), defined by the grammar

\[
C[-] := [-] \mid C[-]N \mid VC[-] \mid (C[-], N) \mid (V, C[-])
\]

and a notion of abstract machine: the \( \text{PCF}_{\text{AM}} \). A raw \( \text{PCF}_{\text{AM}} \) closure is a tuple \( (M, \text{ind}_M, m) \) where \( M \) is a term, \( \text{ind}_M \) is an injective map from the set of free variables of \( M \) to \( I \), and \( m \in \text{Mem} \). \( \text{PCF}_{\text{AM}} \) closures are defined as equivalence classes of raw \( \text{PCF}_{\text{AM}} \) closures over permutations of addresses.

The rewrite system is defined in Fig. 12. First, the creation of a new base type element \( \rightarrow_{\text{link}} \) is simply memory allocation: \( x \) is fresh (and not bound) in \( C \) and \( i \) is a new address neither in the image of \( \text{ind} \) nor in the support of \( m \). Then, the operation \( c \) reduces through \( \rightarrow_{\text{update}(c)} \) using the update of the memory when \( \text{arity}(c) = n \) and \( \text{ind}(x_i) = i \). Then, the if-then-else reduces through \( \rightarrow_{\text{test}(c)} \) using the test operation where \( \text{ind}(x) = i \).

Let \( M = [\{M, \text{ind}_M, m\}] \) be a \( \text{PCF}_{\text{AM}} \) closure. We define the judgment \( x_1 : A_1, \ldots, x_m : A_m \vdash M \) if none of the \( x_i \)’s belongs to \( \text{Dom}(\text{ind}) \), \( y_1 : \alpha_1, \ldots, y_n : \alpha_n \vdash x_1 : A_1, \ldots, x_m : A_m \vdash M : B \) if the variables in \( \Delta \) do not appear as conclusions. The full definition is found in Appendix E.

6.3 Modeling \( \text{PCF}_{\text{LL}} \) with Nets

We now encode \( \text{PCF}_{\text{LL}} \) typing judgments and typed \( \text{PCF}_{\text{AM}} \) closures into program nets. As the type system is built on top of Linear Logic, the translation \( \langle - \rangle \) is rather straightforward, modulo one subtlety: it is parameterized by a memory structure \( m \) and a partial function \( \text{ind} \) mapping term variables to addresses in \( l \).

The mapping \( \langle - \rangle \) of types to formulas is defined by \( \alpha^T : = 1 \), \( (A \rightarrow B)^T : = (A^T)^\perp \otimes B^T \) and \( (A \otimes B)^T : = A^T \otimes B^T \). Now, assume that \( \{y_1, \ldots, y_n\} \cap \text{Dom}(\text{ind}) = \emptyset \), that \( \Delta \) is a judgment whose variables are all of type \( \alpha \), and that \( [\Delta] = \text{Dom}(\text{ind}) \). The typing judgment \( y_1 : \alpha_1, \ldots, y_n : \alpha_n \vdash x_1 : A_1, \ldots, x_m : A_m \vdash M : B \) is mapped through \( \langle - \rangle \) \( \text{ind}_m \) to a program net \( M_{\text{ind}_m}^\Delta = ([R_{\text{ind}_m}, \ldots, R_{\text{ind}_m}, m]) \) with conclusions \( (A_1)^T, \ldots, (A_m)^T, (B^T) \) and memory state \( m \) (note how the variables in \( \Delta \) do not appear as conclusions). The full definition is found in Appendix E.

6.3.1 Adequacy

As in Sec. 2 and 5.4, given a \( \text{PCF}_{\text{AM}} \) closure \( M \) we write \( M \downarrow_p \) (\( M \) converges to \( p \)) if \( p = \sup_{M_{\text{ind}_m}^\Delta} T(\mu) \). The adequacy theorem then relates convergence of programs and convergence of nets. A sketch of the proof is given in Appendix E.

Theorem 29. Let \( \vdash M : \alpha \), then \( M \downarrow_p \) if and only if \( M^T \downarrow_p \).

7. Results and Discussion

As we anticipated in Sec. 1.3, we have proved—parametrically on the memory—that the MSIAM is an adequate model of program nets reduction (Th. 28), and program nets are expressive enough to adequately represent the behavior of the \( \text{PCF}_{\text{LL}} \) abstract machine (Th. 29). What does this mean? As soon as we choose a concrete instance of memory structure, we have a language and an adequacy result for it. This is in particular the case for all instances of memory which are outlined in Sec. 3.3. To make this explicit, let \( P, Q \) and \( Q \) be respectively a deterministic, probabilistic and quantum memory. We denote by \( \text{PCF}_{\text{LL}}(P) \), \( \text{PCF}_{\text{LL}}(P) \) and \( \text{PCF}_{\text{LL}}(Q) \), respectively, the language which is obtained by choosing that memory. Observe in particular that the choice of \( P \) or \( Q \), respectively specialize our adequacy result into a semantics for a probabilistic \( \text{PCF} \) in the style of [33], and a semantics for a quantum \( \text{PCF} \), in the style of [11, 13].

7.1 The Quantum Lambda Calculus

Let us now focus on the quantum case, and analyze in some depth our result. We have a quantum lambda-calculus, namely \( \text{PCF}_{\text{LL}}(Q) \), together with an adequate multi-token semantics. How does our calculus relate with the ones in the literature?

We first observe that the syntax of \( \text{PCF}_{\text{LL}}(Q) \) is very close to the language of [13] (we only omit lists and coproducts). The operational semantics is also the same, as one can easily see. Indeed, the abstract machine in [13] consists of a triple \( (Q, L, M) \) where \( M \) is a lambda-term and where \( Q \) and \( L \) are as presented in Sec. 3.3.3. As we discussed there, for \( Q \) and \( L \) one can use either the canonical presentation of [13], or the memory structure \( Q \).

7.1.1 Discussion on the Quantum Model

It is now time to go back to the programs in our motivating examples, Examples 2 and 3. Both programs are valid terms in \( \text{PCF}_{\text{LL}}(Q) \); we have already informally developed Example 3 within our model.

We claimed in the Introduction that Example 2 cannot be represented in the Gd model described in [21]: the reason is that the model does not support entangled qubits in the type \( \alpha \otimes \alpha \) (using our notation), a tensor product is always separable. To handle entangled states, [21] uses non-splittable, crafted types: this is why the simple term in Example 2 is forbidden. In the MSIAM, entangled states pose no problem, as the memory is disconnected from the types.

The term of Example 3, valid in \( \text{PCF}_{\text{LL}}(Q) \), is mapped through \( \langle - \rangle \) to the net of Fig. 5: Th. 29 and 28 state that the corresponding MSIAM presented in Fig. 10 is adequate. Note that Example 3 was presented in the context of quantum computation. It is however possible (and the behavior is going to be the same as the one already described) to use the probabilistic memory sketched in Sec. 3.3.2. In this case, the H-sync node would be changed for the coin-sync node.

7.1.2 Qubits, Duplication and Erasing

It is worth to pinpoint the technical ingredients which allow for the coexistence of quantum bits with duplication and erasing. In
the language, the reason is that, similarly to [13], PCF AM allows only lambda-abstractions (or tuples thereof) to be duplicated. In the case of the nets (and therefore of the M5IAM), the key ingredient is surface reduction (Sec. 4.2): the allocation of a quantum bit is captured by the link rule which associates one node to the memory. Since one node linked to the memory cannot lie inside a box, it will never be copied nor erased. Indeed, the ways in which the language and the model deal with quantum bits, actually match.

7.1.3 More Possibilities for Quantum Memory Structure

In the presentation of $Q$ we have given in Sec. 3.3.3, fresh qubits were arbitrarily created in state $|\emptyset\rangle$. While we could have chosen to do this in the case of the nets (and therefore of the M5IAM), the key ingredient is surface reduction (Sec. 4.2): the allocation of a quantum bit is captured by the link rule which associates a node to the memory. Since one node linked to the memory cannot lie inside a box, it will never be copied nor erased. Indeed, the ways in which the language and the model deal with quantum bits, actually match.

Unlike the case of the integer memory, the mathematical properties of quantum states can make the test action modify the state of the fresh addresses. Let us see how this happens.

Indeed, one can not only build a memory structure $Q$ with $H_0$ and $H_1$ but also with a superposition of the elements in $H_0$ and $H_1$. For example, one could choose $Q = \{\alpha x_0 + \beta x_1 \mid \forall x \in H, |\alpha|^2 + |\beta|^2 = 1\}$. This makes a memory structure satisfying all the equations. In this system, a valid memory can have all of its fresh variables in superposition. Any measurement on a fresh variable of this memory will collapse the state... and “modify” the global state of the fresh variables. So, despite the fact that a test on $i$ cannot touch another address, it can globally act on the memory.

This paradox is of course solved when remembering that measurements and unitary operations (and measurements and measurements) do commute independently of the state on which they are applied (Remark 1). So the fact that the memory changes globally after a test is irrelevant.

7.2 Conclusion

In this paper, we have introduced a parallel, multi-token Geometry of Interaction capturing the choice effects with a parametric memory. This way, we are able to represent classical, probabilistic and quantum effects, and adequately model the linearly-typed language PCF AM parameterized by the same memory structure. We expect our approach to capture also non-deterministic choice in a natural way: this is ongoing work.

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References


A. Commutation of Tests and Updates on Memory States

The commutation of tests and updates is formally defined as follows. Assume that \( i \neq j \), that \( j \) does not meet \( \overrightarrow{K} \), and that \( \overrightarrow{K} \) and \( \overrightarrow{K}' \) are disjoint.

- Tests on \( i \) commute with tests on \( j \). More precisely, if
  - \( \text{test}(i,m) = p_{01}(\text{true},m_0) + p_{11}(\text{false},m_1) \)
  - \( \text{test}(j,m_0) = p_{00}(\text{true},m_0) + p_{01}(\text{false},m_1) \)
  - \( \text{test}(j,m_1) = p_{10}(\text{true},m_0) + p_{11}(\text{false},m_1) \)

and if
  - \( \text{test}(j,m) = q_{00}(\text{true},m_0') + q_{11}(\text{false},m_1') \)
  - \( \text{test}(i,m_0') = q_{00}(\text{true},m_0') + q_{01}(\text{false},m_1') \)
  - \( \text{test}(i,m_1') = q_{10}(\text{true},m_0') + q_{11}(\text{false},m_1') \)

then for all \( x, y, m_0 = 0, 1, m_0 = m' \) and \( p_{x \rightarrow y} = q_{x \rightarrow y} \).

- Tests of \( j \) commute with updates on \( \overrightarrow{K} \). More precisely, if
  - \( \text{test}(i,m) = p_{01}(\text{true},m_0) + p_{11}(\text{false},m_1) \)
  - \( \text{update}(\overrightarrow{K},x,m_0) = m_0' \)
  - \( \text{update}(\overrightarrow{K},x,m_1) = m_1' \)

and if \( \text{update}(\overrightarrow{K},x,m) = m' \) then
  - \( \text{test}(i,m') = p_{01}(\text{true},m_0') + p_{11}(\text{false},m_1') \).

- Updates on \( \overrightarrow{K} \) and \( \overrightarrow{K}' \) commute. More precisely:
  - \( \text{update}(\overrightarrow{K},x,\text{update}(\overrightarrow{K}',x',m)) = \text{update}(\overrightarrow{K}',x',\text{update}(\overrightarrow{K},x,m)) \)

B. Program Nets: proof of the Diamond Property

We prove that the PARS \((N, \sim)\) satisfies the diamond property (Prop.19). We write \((R, \text{ind}_R, m) \sim \mu\) for the reduction of the redex \( r \) in the raw program net \((R, \text{ind}_R, m)\).

First, we observe the following property, proven by case analysis.

**Lemma 30** (Locality of \( \sim \)). **Assume that** \( R = [(R, \text{ind}_R, m)] \) **has two distinct redexes** \( r_1 \) **and** \( r_2 \), **with** \( R \sim_1 \mu_1 \), \( R \sim_2 \mu_2 \) **and** \( \mu_1 \neq \mu_2 \). **Then the redex** \( r_2 \) **(resp.** \( r_1 \)) **is still a redex in each** \( (R', \text{ind}_{R'}, m') \in \text{SUPP}(\mu_1) \) **(resp.** \( \text{SUPP}(\mu_2) \)).

The proof of Prop. 19 goes as follows.

**Proof.** (Prop. 19.) The locality implies the following two facts: (1) If \((R, \text{ind}_R, m) \sim \mu \) with \( \mu' \neq \emptyset \), then the raw program net \((R, \text{ind}_R, m)\) contains exactly one redex.

(2) If \((R, \text{ind}_R, m) \sim_1 \mu \) and \((R, \text{ind}_R, m) \sim_2 \xi \) with \( \mu \neq \xi \), then there exists \( \rho \) satisfying \( \mu \Rightarrow \rho \) and \( \xi \Rightarrow \rho \). Concretely, \( \mu \Rightarrow \rho \) is obtained by reducing the redex \( r_2 \) in each \((R', \text{ind}_{R'}, m') \in \text{SUPP}(\mu)\), and \( \xi \Rightarrow \rho \) is obtained by reducing \( r_1 \).

Assuming \( \mu \Rightarrow \nu \) and \( \nu \Rightarrow \xi \), item 1. implies \( \nu' \Rightarrow \xi' \), and item 2. implies \( \exists \nu, \nu \Rightarrow \rho \wedge \xi \Rightarrow \rho \). Let us review some of the non-evident cases explicitly.

If \( r_1 \) and \( r_2 \) are both non-active one nodes, say \( x \) and \( y \) respectively, \((R, \text{ind}_R, m)\) reduces to \((R, \text{ind}_R, \{x \Rightarrow i, y \Rightarrow j\}, m)\) and \((R, \text{ind}_R, \{x \Rightarrow k, y \Rightarrow l\}, m)\) for some fresh indexes \( i, j, k, l \). The permutation \((i, k) \circ (j, l)\) renders the two program nets equivalent.

If both \( r_1 \) and \( r_2 \) modify memories (i.e. they perform either update or test), the property holds because the injectivity of \( \text{ind}_R \) guarantees that we always have the requirement (disjointness of indexes) of the equations given in Appendix A. Hence the two reductions commute both on memory (up to group action) and on probability. \( \square \)

C. SIAM: Multitoken Conditions, Formally

**Stable Tokens.** A token in a stable position is said to be stable. Each such token is the remains of a token which started its journey from \( DER \) or \( ONES \), and flowed in the graph “looking for a box”. This stable token therefore witnesses the fact that an instance of dereliction or of one “has found its box”. Stable tokens keep track of box copies; let us formalize this. Let \( S \) be either \( R \), or a structure associated to a box (at any depth). Given a state \( T \) of \( M_{SIAM} \), we define \( \text{Copies}_c(S) \) to be \( \{e\} \) if \( R = S \) (we are at depth 0). Otherwise, if \( S \) is the structure associated to a box node \( b \) of \( R \), we define \( \text{Copies}_c(S) \) as the set of all tokens that are a stable token on the premiss(es) of \( b \)'s principal door. Intuitively, each such \( t \) identifies a copy of the box which contains \( S \).

**Multitoken Conditions: Synchronization, Choice, and Boxes Management.** Rules marked by (i), (ii), and (iii), (iv) in Fig. 9 only apply if the following conditions are satisfied.

(i) Tokens cross a sync node \( l \) only if for a certain \( t \), there is a token on each position \( (e, s, t) \) where \( e \) is a premise of \( l \), and \( s \) indicates an occurrence of atom in the type of \( e \). In this case, all tokens cross the node simultaneously. Intuitively, insisting on having the same stack \( t \) means that the tokens all belong to the same box copy.

(ii) A token \((e, s, t)\) on one of the conclusions \( \Gamma \) of the \( \downarrow \)-box can move inside the box only if its box stack \( t \) belongs to \( \text{Copies}_c(S_0) \) (resp. \( \text{Copies}_c(S_1) \)), where \( S_0 \) (resp. \( S_1 \)) is the left (resp. right) content of the \( \downarrow \)-box. Note that if the \( \downarrow \)-box is inside an exponential box, there could be several stable tokens on each premise of the principal node, one stable token for each copy of the box.

(iii) The position \( p = (e, \epsilon, t) \) under a one node (resp. \( (e, \delta, t) \) under a ?d node) is added to the state \( T \) only if: it does not already belong to \( \text{orig}(T) \), and \( t \in \text{Copies}_c(S) \), where \( S \) is the structure to which \( e \) belongs. If both conditions are satisfied, \( T \) is extended with the position \( p \) (and \( \text{orig}(p) = p \)). Intuitively, each \((e, \epsilon, t)\) (resp. \((e, \delta, t)\)) corresponds to a copy of one (resp. ?d) node.

D. M$\$I$AM$.

The proofs of invariance, adequacy, and deadlock-freeness, all are based on the diamond property of the machine, and on a map—which we call Transformation—which allows us to relate the rewriting of program nets with the M$\$I$AM$. In this section we establish the technical tools we need. In Sec. D.2 we prove Invariance, in Sec. D.3 we prove adequacy and deadlock-freeness.

The tool we use to relate net rewriting and the M$\$I$AM$ is a mapping from states of \( R \) to states of \( R_\ell \), which we are going to introduce in this section. This tool together with confluence (due to the diamond property) allows us to establish the main result of this section, from which Invariance (Th. 27) follows.

From now on, we use the following conventions and assumptions.

- The letters \( T, U \) range over raw M$\$I$AM$ states, the letters \( T, U \) over M$\$I$AM$ states, and the letters \( T, U \) over SIAM states.

- To keep the notation light, we will occasionally rely on our convention of denoting the distribution \( \{T_1^+\} \) by \( \{T\} \) or even simply by \( T \), when there is no ambiguity.

- We assume that \( R \sim \sum_i p_i \cdot \{R_i\} \), where \( i \in \{0\} \) or \( i \in \{0, 1\} \), \( R = [(R, \text{ind}_R, m_R)] \), \( R_i = [(R_i, \text{ind}_{R_i}, m_{R_i})] \).
• Unique initial state. We assume that $R$ has a single conclusion, which has type 1. As a consequence, $\text{Dom}(\text{ind}_R) = \text{ONES}_R$, and $M_R$ has a a unique raw initial state, which is $I_R = (\emptyset, \emptyset, m_R)$. We have $I_R = (\emptyset, \emptyset, m_R)$.

• We denote by $\mathcal{S}_{I_R}$ the set of states $T$ which can be reached from $I_R$, i.e. $I_R \xrightarrow{\mu} T$ and $T \in \text{SUPP}(\mu)$.

• we do not insist too much on the distinction between raw states and states, which in this section is not relevant.

D.1 Properties and Tools

In this section, most of the time we analyze the reduction of raw program nets and raw states, because we do not need to use the equivalence relation. Which is the same: we pick a representative of the class, and follow it through its reductions.

D.1.1 Exploit the Diamond

Because the MSIAM is diamond, we can always pick a run of the machine which is convenient for us to analyze the machine. By confluence and uniqueness of normal forms, all choices produce the same result w.r.t. both the degree of termination of any distribution which can be reached (invariance), and the states which are reached (deadlock-freeness).

In case of $R \xrightarrow{\rho} \rho$ via link, update or test, we will always choose a run which begins as indicated below:

1. **Link.** Assume $(R, \text{ind}_R, m_R) \xrightarrow{\text{link}(n,j)} (R, \text{ind}_R \cup \{x \rightarrow j\}, m_R)$. The machine does the same: from the initial state the machine transitions using its reduction link(n,j), on the same one node. We can choose the same address $j$ because we know it is fresh for $m_R$. Therefore we have $(I, \text{ind}_I, m_R) \xrightarrow{\text{link}(n,j)} (U, \text{ind}_U, m_R) = \mu$.

2. **Update.** Assume $(R, \text{ind}_R, m_R) \xrightarrow{\text{update}(s)} (R', \text{ind}_{R'}, m_{R'})$. Observe that the node one $n$ in the redex is active; let $j$ be the corresponding address. We choose a run which starts with the transitions $(I, \text{ind}_I, m_R) \xrightarrow{\text{link}(n,j)} (U, \text{ind}_U, m_R)$. For any stacks $R$ and $R'$, we call the two stable positions $\{s,t\}$ and $\{s',t\}$ $\perp$-pairs. These two positions are mutually exclusive in a state, because $\text{orig}(c,s,t) = \text{orig}(c',s,t)$.

We say that two states $T, U \in \mathcal{S}_{I_R}$ are in conflict, written $T \perp U$, if $T$ contains one of the two positions of a $\perp$-pair and $S$ the other. We observe that conflict is hereditary with respect to transitions, because stable positions are never deleted or modified by a transition. Let $\uparrow(T) = \{U \mid T \xrightarrow{\rho} T' \land U \xrightarrow{\rho} U' \in \text{SUPP}(\mu)\}$. The following properties are all immediate:

1. If $T \xrightarrow{T'}$, $U \in \uparrow(T)$, then $U \xrightarrow{T'}$.
2. If $T \xrightarrow{\mu}$, then the transition is deterministic, or $\text{SUPP}(\mu) = \{U_0, U_1\}$ with $U_0 \xrightarrow{\rho} U_1$.

3. If $\exists^* \rho$, then for each $T \neq T' \in \text{SUPP}(\mu)$, $T \xrightarrow{T'}$.

States in conflict are in particular disjoint. Therefore we can safely sum them:

**Lemma 34.** Given a distribution of states $\mu \in \text{DST}(\mathcal{S}_{I_R})$,

\[
\forall T_i, T_j \in \text{SUPP}(\mu), T_i \xrightarrow{T} T_j \Rightarrow T \xrightarrow{\rho} T_j \in \uparrow(T) \mu(T) = T \in \text{SUPP}(\mu)
\]

As an immediate consequence, the following also hold:

\[
\begin{align*}
U \xrightarrow{\mu} \{T \xrightarrow{\rho} T_j \in \uparrow(T) \} & \Rightarrow \sum_{T \in \text{SUPP}(\mu)} \mu(T) \cdot \rho_T \\
\Rightarrow \sum_{T \in \text{SUPP}(\mu)} \mu(T) \cdot \rho_T & = \mu(T) \cdot \sum_{T \in \text{SUPP}(\mu)} \rho_T
\end{align*}
\]
Figure 13. The Partial Function \( \text{trsf}_{R \rightarrow R_0} \) on \( \bot \)-box Reduction.

Figure 14. The Function \( \text{trsf}_{R \rightarrow R'} \).
We have everything in place to study the action of $\text{trsf}$ on a run of the machine. What is the action of $\text{trsf}$ on a transition? By checking the definition in Fig. 14 we observe that it may be the case that $T \rightarrow \{U\}$ and $\text{trsf}_{R_{\rightarrow R_{\rightarrow}}} \{U\} = \text{trsf}_{R_{\rightarrow R_{\rightarrow}}} \{T\}$. We say that such a transition collapses for $\text{trsf}_{R_{\rightarrow R_{\rightarrow}}}$. We observe some properties:

**Lemma 40.** From a state of $M_R$, we have at most a finite number of collapsing transitions.

**Proof.** Since the reduction is surface, and since the type of any edge is finite, the set $\{(e, s, t) | e$ is an edge of the redex, $(e, s, t)$ is involved in a collapsing transition} is at most finite. Suppose there are infinitely many collapsing transitions from a state. Then there exist two or more tokens which have the same stacks involved in the sequence of transitions. They must have the same origin, and hence by injectivity they are in fact the “same” token visiting the redex twice or more. Therefore, by “backtracking” the transitions on that token, it again comes to the same edge in the redex with the same stack, hence we can go back infinitely many times. However this cannot happen in our $\text{MSIAM}$ machine, since any token starts its journey from a position in START from which it cannot go back anymore, and transitions are bideterministic on each token.

**Fact 41.** Given a transition $T \rightarrow \mu$, if $T \in [\text{trsf}_{R_{\rightarrow}}]$, then either the transition collapses, or $\text{trsf}_{R_{\rightarrow}}(T) \rightarrow \text{trsf}_{R_{\rightarrow}}(\mu)$ is a transition of $M_{R_{\rightarrow}}$.

**Lemma 42.** If $T \in [\text{trsf}_{R_{\rightarrow}}]$ and $T \rightarrow \mu$ (in $M_R$), then $\text{trsf}_{R_{\rightarrow}}(T) \rightarrow \text{trsf}_{R_{\rightarrow}}(\mu)$ holds.

**Proof.** We transform a derivation $\Pi$ of $T \rightarrow \mu$ in $M_R$ into a derivation of $\text{trsf}_{R_{\rightarrow}}(T) \rightarrow \text{trsf}_{R_{\rightarrow}}(\mu)$ in $M_{R_{\rightarrow}}$, by induction on the structure of the derivation.

- **Case** $T \overset{\tau}{\rightarrow} \{T\}$ becomes $\text{trsf}_{R_{\rightarrow}}(T) \overset{\tau}{\rightarrow} \{\text{trsf}_{R_{\rightarrow}}(T)\}$

  $T \overset{\rightarrow}{\rightarrow} \sum p_U \cdot U \rightarrow \text{trsf}_{R_{\rightarrow}}(U)$

- **Case** $T \overset{\rightarrow}{\rightarrow} \sum p_U \cdot \mu U$ We examine the left premise, checking if it collapses:
  - If it does not collapse, $\text{trsf}_{R_{\rightarrow}}(T) \rightarrow \sum p_U \cdot \text{trsf}_{R_{\rightarrow}}(U)$ is a transition of $M_{R_{\rightarrow}}$ and we have:
    
    $\text{trsf}_{R_{\rightarrow}}(T) \overset{\rightarrow}{\rightarrow} \sum p_U \cdot \text{trsf}_{R_{\rightarrow}}(U) = \text{trsf}_{R_{\rightarrow}}(U) + \text{trsf}_{R_{\rightarrow}}(\mu_U)$ by I.H.

  - If it collapses, we have $T \rightarrow U$, we also have $\text{trsf}_{R_{\rightarrow}}(T) = \text{trsf}_{R_{\rightarrow}}(U)$ and the derivation $\Pi$ is of the form:
    
    $T \rightarrow \{U\} \rightarrow U^3 \rightarrow U^3 \rightarrow \ldots \rightarrow U^3 \rightarrow \{U\}$

    By induction, $\text{trsf}_{R_{\rightarrow}}(U) \rightarrow \text{trsf}_{R_{\rightarrow}}(\mu_U)$, and therefore we conclude $\text{trsf}_{R_{\rightarrow}}(T) \rightarrow \text{trsf}_{R_{\rightarrow}}(\mu)$.

**Lemma 42.** The construction shown in Appendix D.1.1, and Lemma 36, allow us to transfer termination from $I_{R_{\rightarrow}}$ to $I_{R_{\rightarrow}}$, and to prove one direction of Prop. 39. The other direction is more delicate.

Assume that $I_{R_{\rightarrow}} q$-terminates; this implies that for a certain $n$, whenever $I_{R_{\rightarrow}} \Rightarrow_\sigma \sigma$ then $T(\sigma) \geq q$. The following Lemma builds such a sequence in a way that $\sigma = \text{trsf}_{R_{\rightarrow}}(\mu)$, with $\mu$ in $M_{R_{\rightarrow}}$. This allows us to transfer the properties of termination of $I_{R_{\rightarrow}}$ back to $I_{R_{\rightarrow}}$, ultimately leading to the other direction of Prop. 39.

**Lemma 43.** Assume $T \in [\text{trsf}_{R_{\rightarrow}}]$. For any $n$:
1. there exists $\mu$ such that $T \not\rightarrow \mu$ and $\text{trsf}_{R_i}(T) \equiv^n \text{trsf}_{R_i}(\mu)$;  
2. we can choose $\mu$ such that $T(\mu) = T(\text{trsf}_{R_i}(\mu))$.

Proof. 1. We build $\mu$ and its derivation, by induction on $n$.

$n = 1$.  
• Assume $T$ is terminal, then $\text{trsf}_{R_i}(T)$ is terminal, and $\text{trsf}_{R_i}(T) \rightarrow\rightarrow \text{trsf}_{R_i}(T)$.
• Assume there is $\mu$ s.t. $T \not\rightarrow \mu$ non-collapsing. We have $\text{trsf}_{R_i}(T) \rightarrow\rightarrow \text{trsf}_{R_i}(\mu)$.
• Assume that all transitions from $T$ are collapsing. For such a reduction, we have that $T \rightarrow T'$ and $\text{trsf}_{R_i}(T) = \text{trsf}_{R_i}(T')$. It is immediate to check that from any $T \in S_{\rho_R}$ there is at most a finite number of consecutive collapsing transitions. We repeat our reasoning on $T'$ until we find $U$ which is either terminal or has a non-collapsing transition $U \rightarrow \mu$. The former case is immediate, the latter gives $U \not\rightarrow \mu$ and therefore $T \not\rightarrow \mu$ by transitivity, and $\text{trsf}_{R_i}(T) = \text{trsf}_{R_i}(U) \rightarrow \text{trsf}_{R_i}(\mu)$, hence $\text{trsf}_{R_i}(T) \rightarrow \text{trsf}_{R_i}(\mu)$.

$n > 1$. Assume we have built a derivation of $T \not\rightarrow \mu$ with $\text{trsf}_{R_i}(T) \equiv^n \text{trsf}_{R_i}(\rho)$. We have that $\text{trsf}_{R_i}(\rho) = \sum p(U) \cdot \text{trsf}_{R_i}(U)$. For each $U \in \text{SUPP}(\rho)$, we apply the base step, and obtain a derivation of $U \not\rightarrow \mu$ with $\text{trsf}_{R_i}(U) \rightarrow \text{trsf}_{R_i}(\mu)$. Putting things together, $T \not\rightarrow \rho$ and $\text{trsf}_{R_i}(T) \equiv^n \sum p(U) \cdot \text{trsf}_{R_i}(\mu)$ by Lemma 34.

2. We now prove the second part of the claim. Let $T \not\rightarrow \mu$ be the result obtained at the previous point. Let $\{U_k\}$ be the set of states in $\text{SUPP}(\mu)$ such that $\text{trsf}_{R_i}(U_k)$ is terminal. This induces a partition of $\mu$, namely $\mu = \rho + \sum c_k \cdot U_k$. It is immediate to check that each $U_k \not\rightarrow \mu$ and $\text{trsf}_{R_i}(U_k) = \text{trsf}_{R_i}(U_k)$. Observe also that $\rho$ does not contain any terminal state. Let $\nu = \sum c_k \cdot U_k$. We have by transitivity $T \not\rightarrow (\rho + \nu)$, and $\text{trsf}_{R_i}(T) \equiv^n \text{trsf}_{R_i}(\rho + \nu)$ (because $\text{trsf}_{R_i}(\rho + \nu) = \text{trsf}_{R_i}(\mu)$). We have $\text{trsf}_{R_i}(\rho + \nu) = \text{trsf}_{R_i}(\nu)$ because $\text{trsf}_{R_i}(\nu) = \sum c_k \cdot \text{trsf}_{R_i}(U_k)$. We conclude by observing that $\text{trsf}_{R_i}(\nu) = \sum c_k$.

Summing up, we now have all the elements to prove Prop. 39.

Proof. (Prop. 39)  
$\Rightarrow$. Follows from Prop. 42, by using the construction in Sec. D.1.1, Lemma 36, and linearity of $\text{trsf}$.  
Assume $I_{R_i} \not\rightarrow^* \mu$, with $\mu$ not empty, and that the machine starts as described in Sec. D.1.1 (in case $\sim$ is link, update or test). We observe that every state $T \in \text{SUPP}(\mu)$ is contained in $\text{trsf}_{I_{R_i}}$ for some $i$. We can then prove that for each $i$ there exists a node $T_i$ such that $I_{R_i} \not\rightarrow T_i$, and such that $\nu = \sum p_{T_i} \cdot \mu_i$.  
$\Leftarrow$. Follows from Lemma 43. We examine the only nonstraightforward case. Assume $R \not\rightarrow_{\text{link}(1,m)} (R_0 \cdot R_1)$. We choose a run of the machine which starts as described in Sec. D.1.1: we have that $I_{R_i} \not\rightarrow \sum p_{T_i} \cdot (T_i)$, with $\text{trsf}_{R_i}(T_i) = I_{R_i}$ by Lemma 33. By hypothesis, $I_{R_i}$ terminates with probability at least $q_i$; assume it does so in $n$ steps. By using Lemma 43, we build a derivation $T_i \not\rightarrow \mu_i$, such that $\text{trsf}_{R_i}(T_i) \equiv^n \text{trsf}_{R_i}(\mu_i)$ and $T(\mu_i) = T(\text{trsf}_{R_i}(\mu_i))$. By Th. 11, $T(\text{trsf}_{R_i}(\mu_i)) \geq q_i$.  
Putting all together, we have that $I_{R_i} \not\rightarrow \sum p_{T_i} \cdot \mu_i$, and $I_{R_i}$ terminates with probability at least $\sum p_{T_i} \cdot q_i$.

D.3 MSIAM Adequacy and Deadlock-Freeness: The Interplay of Nets and Machines

We are now able to establish adequacy (Th. 28) and deadlock-freeness (Th. 26). Both are direct consequence of Prop. 45 below.

}\[1.\]
D.4 MSIAM: Full development of Fig. 10

Here we fully develop what was sketched as description of the MSIAM execution presented in Fig. 10.

In the first panel (A), no box are yet opened: only two tokens are generated: the derivation node emits token (a), in state (δ, δ, ϵ, ϵ), while the one-node emits token (b), in state (ε, ε)∗, and attached to a fresh address of the memory. Eventually token (a) reaches the Y-node and opens a copy: its state is now (δ, δ, ϵ, ϵ)∗. Token (b) also flows down: it first reaches the H-sync node, crosses it while updating the memory, crosses the ⊥-node and gets the new state (I, ε, ϵ). It continues through ?d with new state (I, δ, ε, ϵ), reaches the Y-entrance: its copy ID is ϵ, ϵ, and it has been opened by token (a), it can carry onto the left branch with new state (I, δ, ε, ε). It arrives at the Y-node and follow the left branch with state (ε, δ, ε, ϵ): it now hits a bot-box.

The test-action of the memory is called, and a probabilistic distribution of states is generated where the left and the right-side of the ⊥-box are probabilistically opened: the corresponding sequences of operations are represented in Panel (B0) for the left side, and Panel (B1) for the right side.

In Panel (B0): the left-side of the bot-box is opened and its one-node emits token (c), in state (δ, δ, ϵ, ϵ): note how the box stack of this newly-created token is the one of the copy of the Y-box it sits in. In any case, the token also comes equipped with a fresh address from the memory, and carries downward. When it reaches the entrance of the Y-box, coming from the left it exits and eventually reaches the conclusion of the net. Note how we end up with a normal form: token (b) and (a) are stable at doors of boxes.

In Panel (B1): the right-side of the bot-box is opened and its one-node emits a token, that we can also call (c), also in state (ε, δ, ϵ, ϵ). The ?d-node emits token (d) in state (δ, δ, δ, ϵ, ϵ): this token flows down and gets to the entrance of the Y-box; it stops there in state (δ, y(δ, δ, δ, ϵ, ε)) and opens a new copy of the Y-box. Token (c) goes down, arrives at the Y-entrance and enters this new copy (of ID y(δ, δ, δ, ε)). It hits the corresponding copy of the ⊥-box, and the test-action of the memory spawns a new probabilistic distribution.

We focus on panel (C10) on the case of the opening of the left-side of the ⊥-box: there, a new token (e) is generated (with a fresh address attached to it) and goes down. It will exit the copy of ID y(δ, δ, δ, ε), enter the first copy, goes over the axiom node, and eventually exits from this first Y-box-copy. It is now at level 0, and goes to the conclusion of the net. The machine is in normal form.

E. PCF^L: Adequacy

The translation of PCFAM closures into program nets is given in Fig. 15 and 16. There, we assume that the translation is with respect to the fixed pair (Ind, m). The partial map IndAM is depicted with a dotted line, and corresponds exactly to the parameter to the map (-)Ind AM.

With this definition, the well-typed closure y1 : A1, ..., ym : Am |→ (M, Ind, m) : B can now simply be mapped to the program net MInd AM.

We prove here the adequacy theorem (Th. 29).

Th. 29 (Recall). Let |→ M : α, then M [p] if and only if M[1] [p].

Before proving the theorem, we first establish a few technical lemmas which analyze the properties of the translation (-)1.

Lemma 46. Assume that M = (M, Ind, m) is PCF closure that |→ M : α, and ρ a distribution of such closures. We have:
1. M is a normal form if and only if M[1] is a normal form.
2. T(ρ) = T(μ[1]).

Lemma 47. Under the hypotheses of Lemma 46:

Corollary 48. Under the hypotheses of Lemma 46:
1. If M[1] ⇒ μ, then there is μ s.t. M |→ μ with M[1] = μ.
2. If M[1] ⇒ k μ, then there is μ s.t. M ⇒ m μ and M[1] ⇒ m μ1, with m ≥ k.

Proof. (1) Immediate consequence of Lemma 46 and 47 (2) By induction.

We are now ready to prove Th. 29.

Proof of Th. 29. Assume M [p] and M[1] [p]; we want to prove that p[term] = p[net].

p term ≤ p net. It follows from the following. Assume M ⇒ μ with T(μ) = q, then M[1] ⇒ m μ1 (by Lemma 47) and T(μ[1]) = q (by Lemma 46).

p term ≥ p net. We prove that if M[1] ⇒ μ then it exists μ with M ⇒ μ and T(μ) ≥ T(μ). Assume M[1] ⇒ μ. By Corollary 48, M ⇒ m μ and M[1] ⇒ m μ1, with m ≥ k. By Uniqueness of Normal Forms (Th. 11.1) we have that T(μ[1]) ≥ T(μ). By Lemma 46, T(μ) = T(μ[1]), from which we deduce the statement.
Figure 15. Translation of PCF$^{\text{LL}}$ into Nets.

Figure 16. Translation of PCF$^{\text{LL}}$ into Nets.