Improved Class-K amplifier for headset applications

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Abstract—This paper proposes an improved Class-K audio amplifier as an alternative solution to Class-AB and Class-G audio amplifiers for Headset applications. The Class-K architecture consists in a linear and a switching amplifier connected in parallel to the same speaker. The advantage of this solution is to associate the power efficiency of the switching amplifier with the linearity of a linear amplifier. However, the Class-K may suffer from static consumption. Three methods are presented in order to compensate the topology’s disadvantages: a ternary modulation, a two-stage linear amplifier and a low idle switching amplifier. In this paper, we present the Class-K operating principle, a topological comparison and solutions to improve high static consumption. The simulation results prove the validity of these solutions, achieving less than 2mA of static current.

Keywords—Analog circuits; hybrid audio amplifier; static consumption; low idle switching amplifier; two-stage linear amplifier; ternary modulation.

I. INTRODUCTION

During the last few years, there has been a growing market demand for audio amplifiers in multimedia equipment such as computers or mobile phones. Audio amplifiers in those equipments, despite they only have little space available, become smaller and smaller with a high output power and less consumption. Before switching amplifiers [1], linear amplifiers were the only electrical solution to amplify audio signals. They are still used in numerous applications thanks to their very good linearity when they reproduce audio signals. Audio amplifiers are usually classified in three groups: linear amplifiers, switching amplifiers and hybrid amplifiers. Each group contains different structures named: Class-A, B, AB and C for linear amplifiers, Class-D, E and F for switching amplifiers and Class-G, H and K hybrid amplifiers.

Switching amplifiers, especially Class-D amplifiers [2] are currently used for handsfree applications in embedded systems because they have a sufficient linearity (less than 1% of total harmonic distortion) associated with a high efficiency (90% at maximum power) [3]. But they are generally not used for high end audio applications, such as headset because the audio quality specification is higher and the static current consumption must be lower than few mA.

The common Class-AB amplifiers are a well established solution, but they suffer from a high thermal dissipation. Class-G is a serial hybrid evolution described in figure 1. Although Class-G improves the efficiency, our study let us think that Class-K could fit better in this kind of applications.

V_in

SMPS

Figure 1. Standard block diagram and signals of a two level Class-G (Class-AB using a Switched-Mode Power Supply).

The objective of this work is the study of such a solution for capless headset (with symmetrical supply). Class-K topology will be introduced: principle, static consumption, power and efficiency analysis are given. Then Class-K will be compared to Class-AB and Class-G. After that, solutions to improve consumption are proposed. Finally, simulation results will be given to prove the validity of these solutions.

II. CLASS-K AUDIO AMPLIFIER

The prior work [3] used the hysteresis topology [4] but this architecture is not applicable for headset applications, because a known switching frequency is preferred.

A. Class-K principle

Figure 2. Block diagram of “binary PWM Class-K”.

The Class-K using binary PWM architecture (Class-K1 or Class-K1B), proposed by [5], is presented in figure 2. The principle of Class-K is to use a switching amplifier (SA) controlled by the output current of a linear amplifier (LA). They are both connected in parallel to the load ($R_L$). The SA drives the load through an inductance and could be seen as a current source controlled by LA output current. The LA feedback control adjusts the output voltage $V_s$ to the reference audio signal $V_{in}$.

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1) Modeling Class-K PWM

The Class K1 PWM amplifier design is modeled in Matlab Simulink environment to characterize its behavior (the SimElectronics toolbox has been used). The models used for this system are a result of subsequent simplifications including: infinite LA bandwidth ($A_{lb}$ close to infinite, $\tau_{la}$ equal to zero), double sided linear ramp generation, ideal SA power stage, stable power supply and ground, linear inductor and resistors, purely resistive load and ideal comparator. However, this level of modeling can be used in order understand the behavior of this system. Following simulations use $R_{in}=R_{out}=100\,k\Omega$, $R_{load}=1\Omega$, $R_{bat}=32\Omega$, $V_{bat}=3.6\,V$, $K=1$ and $V_{cm}=1\,V$.

2) Power delivered by Linear and Switching Amplifier

The power delivered by LA and SA is one of the most relevant characteristics of a Class K audio amplifier. The efficiency of this structure is directly dependent of the ratio $i_{out}/i_{la}$. In fact, the LA and SA have respectively low and high efficiencies. In steady-state, without considering the high frequency component, the output currents in both amplifiers are expressed in (1).

$$i_{la} = \frac{R_{LS}f_{pwm}}{K_f}i_s \quad i_{sa} = 1 - \frac{R_{LS}f_{pwm}}{K_f}i_s$$  

Where $K_f = R_s G_{pwm} K = \frac{2 R_s V_{bat}}{\Delta V}$, $\Delta V$ is the amplitude of the ramp, $V_{bat}$ the power supply voltage. The figure 3 shows the LA, SA and output currents when the input reference a rail to rail pure sine wave at 1kHz ($L=200\mu H$, $f_{pwm}=100\,MHz$, $\Delta V=40\,mV$). In this case, the ratio between LA and load current is 18%. The SA provides 82% of the output current, providing a high efficiency to the Class-K architecture.

3) Static consumption

The high gain of the LA feedback compensates the output voltage oscillation introduced by the PWM modulation. The LA current compensates the inductor current ripple all the time [6]. The LA has the same function as an active filter to compensate the PWM high-frequency components. This interaction causes a static consumption depends on the low-pass filter time constant ($L/R$) and the PWM frequency. The LA and SA static currents can be expressed as in (2).

$$I_{0_{rms}} = \frac{V_{bat}}{2\sqrt{3}f_{pwm}}$$  

With $V_{bat}=3.6\,V$, $f_{pwm}=10\,MHz$ and $L=20\mu H$, the static current is $I_0=5.2\,mA$, equivalent to the 4.8mA static current of a Class-AB [7] but a large value compared to the 1.2mA static current of a Class-G [8]. This confirm that Class-K suffer from high static consumption compare to other topologies. Solutions to improve this problem will be given in section III.

4) Power efficiency analysis

a) SA power losses

The SA power consumption given in (3) is composed of the power to the load $P_s$ conduction losses $P_{cond}$ switching losses $P_{sw}$, and quiescent power $P_q$ [9].

$$P_s = \frac{V_{rms}I_{rms}}{R_s} + R_{sw} \left( I_{rms}^2 + \frac{V}{K_f} \right) + 2C_p V_{bat} f_{pwm} + 2V_{bat} Q_{eq}$$  

With $R_{sw}$ the resistor value of SA’s power transistors, $C_p$ the transistor gate capacitance, $I_{qsa}$ the quiescent current of PWM modulator and current sensing block, $I_0$ the RMS static consumption. The RMS SA current, RMS output voltage and average of static current can respectively be expressed as:

$$I_{sa_rms} = \frac{MV_{bat}}{\sqrt{2}} \left( 1 - \frac{R_{LS}}{K_f} \right) \quad V_{rms} = \frac{MV_{bat}}{\sqrt{2}} \quad I_0 = \frac{V_{bat}}{4f_{pwm}}$$  

b) LA power losses

The LA power consumption is composed of the dissipate power in the power stage ($V_{la}I_{la}$) and quiescent power (bias current and oscillation current losses explained in the previous subsection) as shown in (5).

$$P_{la} = \frac{1}{\eta_{la}} P_u + 2V_{bat} I_{qsa} + 4V_{bat} f_{pwm} \left( 1 - \frac{M}{\sqrt{2}} \right) e^{-\frac{V_{bat}}{K_f}}$$  

Where $\eta_{la}$ is the efficiency of the LA amplifier, $I_{qsa}$ the LA quiescent current, and $P_u$ the power provided to the load. The efficiency and the load power can be expressed as:

$$\eta_{la} = \frac{\pi}{4} M \quad P_u = \frac{M V_{bat}}{2K_f}$$  

c) Class-K PWM efficiency

The efficiency [10] of Class-K PWM is:

$$\eta_k = \frac{P_s}{P_{la} + P_{sw}} \quad P_s = \frac{M V_{bat}}{2K_f}$$  

Figure 4 illustrates the efficiency of Class-K PWM, our proposed efficiency model and a Classical Class-AB amplifier solution when the output power varies (conditions: $C_p=100pF$, $R_{sw}=4\Omega$, $f_{ina}=1\,kHz$, $L=200\mu H$, $f_{pwm}=1\,MHz$, $R/K_f=0.18$).
B. Topological comparison

Table I presents a quick comparison between the different audio amplifier topologies. Class-AB has the best quality reproduction but it has a reduced efficiency. Class-G is efficient but has a low reproduction quality and the Class-K and K are good tradeoffs between audio quality and efficiency.

### TABLE I. COMPARISON BETWEEN DIFFERENT AUDIO AMPLIFIERS.

<table>
<thead>
<tr>
<th>Class</th>
<th>Reproduction quality</th>
<th>Efficiency (full scale)</th>
<th>Efficiency (middle power)</th>
<th>Efficiency (low power)</th>
</tr>
</thead>
<tbody>
<tr>
<td>AB</td>
<td>++</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>D</td>
<td>-</td>
<td>++</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td>G</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td>K</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

+ = high-quality; + = good; - = poor; - = reduced

1) **Comparison with Class-AB**

The figure below shows the consumption current ratio between Class-AB and Class-K1 PWM. The main advantage of the Class-K structure is the power efficiency at medium and high output power (superior to 1mW in the above example). The static current is the main drawback to develop this system for integrated and low static consumption audio amplifiers, such as in mobile phones.

![Class K is superior](image_url)

**Figure 5.** Class-K PWM vs Class AB consumption ratio.

2) **Comparison with Class-G**

The main advantage is that most of the power is provided by the efficient SA and that the LA controls the output voltage to correct non linearities. Another advantage is the equivalent resistance of the amplifiers which could be smaller (compared to Class-G) because of parallel connection. The main drawback of Class-K PWM is idle consumption due to the LA’s compensation of the SA’s output ripple current. The second drawback is that the LA’s idle power consumption could be higher than LA of Class-G, even if the LA provides less current, because this LA is powered between -V_{bat} and V_{bat} instead of a portion of V_{bat} in Class-G. The last drawback: the SA is always modulating in opposition to Class-G which could use a Pulse-frequency mode (PFM) for its SMPS. Three solutions can be used to limit these drawbacks: ternary modulation, two stages LA and SA power stage with low idle consumption.

III. SOLUTIONS TO IMPROVE STATIC CONSUMPTION

A. Ternary modulation

The limitation for binary PWM (Class-K1B) is the static consumption introduced by the coupling between LA and SA. The proposed solution is the use of a ternary PWM modulation (Class-K1T) to reduce static current consumption. If a three level SA is used, the SA’s output voltage can be set to zero when there is no input, instead of switching between low and high supply voltage. In this case, the LA amplifier has not to compensate the ripple, because there is almost none.

![Block diagram of Class-K1T.](image_url)

**Figure 6.** Block diagram of Class-K1T.

**Figure 7.** Class-K consumption (binary and ternary) vs. output power.

The figure 8 shows the current consumption ratio between the Class-K1T and Classical linear and Class-G solutions. We consider that the SA’s power stage switching losses are reduced by reducing the amplitude of the gate command voltage (P_{sw}=0.5C_{g}V_{bat}^{2}). The Class-G has both power supplies (V_{bat}, V_{bat}) and the Class-G switching power supply which provides the V_{bat} has the same efficiency as Class-K’s LA (C_{g}=100pF, R_{on}=4Ω). Efficiencies are given in (8).

![ClassK1T consumption versus others amplifiers solutions.](image_url)

**Figure 8.** ClassK1T consumption versus others amplifiers solutions.

\[
\eta_G = \eta_{la} \frac{\eta_{sms}}{p_{sms}} = \frac{\pi MV_{bat}}{4V_{bat}G} \\
\eta_{sms} = \frac{p_{sms}}{p_{sms}+R_{on}I_{sms}+0.5C_{g}V_{bat}V_{bat}+I_{pwm}V_{bat}} \\
\eta_{la} = \frac{4V_{bat}G}{\pi MV_{bat}} \\
p_{sms} = \frac{M V_{bat}^{2}}{2R_{la}} \\
I_{sms} = \frac{p_{sms}}{V_{bat}} \\
\]

(8)
The Class-K’s consumption is always lower than with the Class-AB solution. For Class-G comparison (at $f_\text{in}=10\text{kHz}$), the Class-K efficiency gain depends on the Class-G’s supply (here, 1.2V). At 0.9V, the Class-G is the best solution between 1mW and 15mW, because of the improvement of its LA efficiency (30% more than with 1.2V). The simulation with an audio track (voice music style) at 1mW confirms the Class-K’s gain. The Class-AB, G and K efficiencies are respectively 4.7%, 11.7% and 14.4%. At 10mW, the gap increases: 16%, 27% and 40%. The same results are obtained with a techno music style. To conclude, the Class-K solution reduces the power consumption between 20 and 30%, in function of the output power. The Class-K’s efficiency gain depends on the electrical implementation of the SA’s power stage and the static current of PWM, current sensing and LA. If we consider the MOS drift performance which composes the Class K SA power stage, its consumption increases (ratio between $R_\text{on}$ and $C_p$ is lower than a Classical high voltage MOS in Class G’s SMPS).

**B. Two stages linear amplifier**

Using the LA and SA connected in parallel will reduce the load seen from the LA’s side, involving a lower static current of LA for the same gain bandwidth product.

![Two-stage Miller CMOS OTA principle with gain response.](image)

For the LA, 1 MHz is a good GBW. In theory [11], a ratio of three for $f_{\text{nd}}$ is taken for a phase margin of about 70°. In reality, 1.2 times more is enough because 45° phase margin is sufficient.

$$f_{\text{nd}} = \frac{g_{m1}}{2\pi C_L} = 1.2\ GBW \quad GBW = \frac{g_{m1}}{2\pi C_L} \quad g_{m2} = \frac{i_\text{d}}{V_{\text{P-P}} N_0}$$

With $C_L=C_{Lb}=1nF$, $V_P=25mV$ and $N_0=2$; $g_{m1}=7.6mS$ and $I_d=380\mu A$. In the first stage, if $I_1=15\mu A$, $G_{m1}=0.3mS$ and $C_{Lb}=47.7pF$. A two-stage Miller CMOS OTA, instead of a three-stage is a possible solution for Class-K compared to the three-stage from the Class-G.

**C. Switching amplifier power stage**

The main idle loses are the gate drive, the ripple current provide from the coil and parasitic capacitances. Those losses can be reduced using zero voltage switching, choosing the optimal power transistors size and using a CMOS technology with double oxide or drain extension.

**IV. SIMULATION RESULTS**

Simulations with Cadence using STMicroelectronics 0.25μm technology transistor models show that a two stage linear amplifier in parallel with a switching amplifier using a ternary modulation is a good solution to reduce static consumption. For 16Ω load, we choose 33μH and 33nF for the LC filter, and a switching frequency of 3MHz. The static consumption is estimated at 2mA for a single channel with a THD equivalent to Class-G ($V_{\text{rms}}=0.7V$, $F=1\text{kHz}$, $\text{THD}_{\text{max}}=74\text{dB}$). Figure 10 shows that the ratio between LA and load current is 14% and the SA provides 86% of the output power.

![Figure 10. LA, SA and load currents.](image)

**V. CONCLUSION**

This article presents an original topology to increase the efficiency of an audio amplifier driving a headset. It has been shown that it is possible to use linear amplifier coupled with a switching amplifier to reproduce a good audio signal with little extra power dissipation. Different solutions to reduce static current losses of the Class-K topology are proposed. The use of ternary modulation, two-stage LA implementation and low idle SA are discussed. The simulation results show that the Class-K amplifier topology could be an alternative solution to linear amplifier for headset applications.

**REFERENCES**


