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MIL : A language to build program analysis tools through static binary instrumentation

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Abstract—As software complexity increases, the analysis of code behavior during its execution is becoming more important. Instrumentation techniques, through the insertion of code directly into binaries, are essential to program analyses such as performance evaluation and profiling. In the context of high-performance parallel applications, building an instrumentation framework is quite challenging. One of the difficulties is due to the necessity to capture coarse grain behavior, such as the execution time of different functions, as well as finer-grain behavior in order to pinpoint performance issues.

In this paper, we propose a language, MIL, for the development of program analysis tools based on static binary instrumentation. The key feature of MIL is to ease the integration of static, global program analysis with instrumentation. We will show how this enables both a precise targeting of the code regions to analyze, and a better understanding of the optimized program behavior.

I. INTRODUCTION

As software complexity increases with the development of multicore architectures, high performance parallel applications are increasingly difficult to tune for performance, to debug and profile. Due to compiler optimizations, runtime interactions and complex shared memory hierarchies, capturing the runtime behavior of the code is an essential step in code analysis. The purpose of binary instrumentation is to insert new code into an executable in order to collect and analyze information concerning an execution. Tools offering binary instrumentation such as Dyninst [1], MAQAO [2], Pebil [3], Pin [4] or Valgrind [5], are at the heart of code analysis tools used today. For performance analysis, a first coarse grain analysis is usually achieved in order to identify hotspots, and on these hotspots, a finer grain analysis capturing more details, follows. In order to adapt to the level of details required and to avoid the cost of an indiscriminate and expensive fine-grain instrumentation, several instrumentation languages have been proposed [6], [7], [8]. Instrumentation languages help to define where to insert the instrumentation probes, based on the structure of the binary code in terms of functions, loops, and sometimes blocks or instructions. However, compiler optimizations may change deeply the structure of the code, from the source to the binary, and this limits the effectiveness of such approaches. Indeed, if the compiler generates two versions of a loop, one vectorized and the other not, the binary instrumentation techniques proposed only report performance analysis for each loop, independently of the other. Similarly, when a function “foo” has been inlined or cloned by the compiler as it occurs for OpenMP codes, current instrumentation techniques cannot instrument the “foo” inlined versions (no longer functions), nor relate instrumentation results of cloned versions to the “foo” function. For instance, Figure 1 shows the output of the TAU profiler [9] using Dyninst [1] on the bt_A NAS OpenMP benchmark for the thread 1 (out of twelve), the three most time-consuming functions have names that do not match those of the source code functions, x_solve, y_solve and z_solve. The reason is that the compiler has generated new functions, associated to the parallel for constructs and the profiler has not found the correspondence with the source code functions. For optimized codes, the main challenge for instrumentation languages is therefore not only to enable an efficient description of the code fragments to instrument but also to report information relevant for users.

In this paper we propose a binary rewriting framework and a domain specific instrumentation language, MIL, for the development of code analysis tools. This language is built on top of MAQAO, a static performance analysis tool[10]. The original contributions of our approach are:

- MIL, a versatile language for instrumentation: the language MIL can be used to gather information on large variety of events, from functions to loops, blocks and instructions for control-flow profiling or value-profiling.
The probes inserted in the binary code can be user-defined, enabling for instance hardware counter profiles, and written either in MIL (MILRT runtime), in C (through an external library) or in assembly. Besides the precise location of these probes, their parameters can be defined by scripts, using a rich API of static analysis. In particular, probes can have parameters dependent on some static property of their insertion location.

- A framework for optimized multi-threaded code analysis: compiler optimizations can generate functions for OpenMP codes with complex control-flow. MIL enables the developer of code analysis tools to focus on functions appearing in the source code, independently of any name mangling, inlining or transformation due to OpenMP directives.

- A low-overhead instrumentation: We combine techniques presented in Dyninst[1] with more aggressive techniques for adding instrumentation code. In particular, we introduce in MIL interpreter new techniques for low-overhead instrumentation of OpenMP optimized codes.

The paper is organized as follows: Sections II and III present the instrumentation language and describe how the binary code is restructured using different algorithms. Section IV shows how this tool is integrated into the TAU parallel performance system, along with an example of a simple function profiler, fully written in MIL for multi-threaded codes. Finally, section V presents the evaluation of the overhead due to instrumentation and a comparison with other existing tools.

II. INSTRUMENTATION LANGUAGE

MIL is a scripting language to define binary code instrumentation. The interpreter of MIL scripts is detailed in Section III and is built on top of MAQAO [2]. Running a MIL script with an input executable produces a new instrumented executable. The possible locations where instrumentation can be inserted are called events and the description of what to instrument corresponds to event filters. The definition of the probes, i.e. the code to insert for a given event, is also given in MIL. We present thereafter how to express both in MIL.

A. Abstract Code Structures and Filters

To define instrumentation points, the following structural abstractions can be manipulated: the program itself, its functions, loops, basic blocks, instructions and a particular case of instructions, the call sites. These notions are usual structurations of programs and correspond here to structural abstractions found in the binary code. Loops correspond to only natural loops, and functions may have multiple entries. To define precisely where to insert some instrumentation, we define the notion of event as being a particular location in these structures, as for instance the entries and exits of a loop. The events associated to a particular structure are summarized in Table 1.

The structures defining the events are described in a hierarchical way, reflecting the nesting of functions, loops, basic blocks and instructions.

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### Table I. Structural Abstractions and Associated Event Names.

<table>
<thead>
<tr>
<th>Event names</th>
<th>program, functions</th>
<th>callsites, instructions</th>
<th>loops</th>
</tr>
</thead>
<tbody>
<tr>
<td>entry, exit</td>
<td>entries, exits</td>
<td>before, after</td>
<td></td>
</tr>
</tbody>
</table>

### Table II. Structural Abstractions and Associated Filter Mechanisms.

<table>
<thead>
<tr>
<th>Whitelist, blacklist</th>
<th>program, functions</th>
<th>callsites</th>
<th>loops</th>
<th>blocks</th>
<th>instructions</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>target name</td>
<td>id</td>
<td>id</td>
<td>address</td>
<td></td>
</tr>
</tbody>
</table>

#### Table

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</tr>
</tbody>
</table>

MIL is an object-oriented and event-directed language. It extends the Lua syntax[11] providing additional classes and constructs to specifically manipulate all the concepts of our DSL (binary instrumentation), namely, structural objects (functions, loops, ...), events, filters and probes. For a complete set of examples showing all the language capabilities and an exhaustive description of the syntax, please refer to the MAQAO tool project page [10]. We also provide language files for the vim editor. An Eclipse plugin is envisioned.

Figure 2 depicts the rationale of a MIL script. We select a binary and target structural objects. For this example, "Object" is used as a generic container for Function, Loop, Block, Instruction or Callsite (Binary being a special one). Then we can specify filters and events related to objects. The final step is to create probes for each selected event. More details on each stage is given in the next subsections.

Instrumentation overhead is one of the dominant concerns when considering how a binary rewriting tool is used to enable performance measurement. There are two ways to reduce the overhead of instrumentation at binary level: reduce the time taken by the probes themselves or reduce the number of structures instrumented by applying filters. In order to be able to restrict the field of objects to be processed, a filtering mechanism is mandatory. The filtering mechanisms associated to a particular structure are summarized in Table II. A set of filters may be defined for every structure and can be either a list, a built-in filter or a user-defined filter. A filter using lists can define whitelists and blacklists for any structure. Depending upon the given structural object, its main attribute is used to apply the filtering. For instance, the whitelist filter defined by the "calc" followed by blacklist filter "_test?" selects all functions with name beginning with calc that does not end in _test. For callsites, the whitelist filter "calc" selects all instructions calling functions beginning with calc. It is also possible to select a set of structures based on their structural attributes (built-in). So far, only one built-in attribute, depth is defined for loops. It is possible to select loops with this filter at a given depth ((depth = 3)) or using the key words inner, outer for relative depths.

Figure 3 presents an example of multiple (here two) events,
Each using a nested definition. The first event nest counts the number of innermost loops, of function “foo1”, that only has one basic block. Hence using a nest of function, loop and basic block events. The second event nest counts the total number of iterations passed in outermost loops of function “foo2”.

<table>
<thead>
<tr>
<th>Function: isinner_oneblockloop(loop)</th>
</tr>
</thead>
<tbody>
<tr>
<td>if(loop:get_nblocks() == 1) then return true; end</td>
</tr>
<tr>
<td>return false; end</td>
</tr>
</tbody>
</table>

```lua
--First function event
fct1 = mb:addFunction():addFilterWL("foo1");
loop1 = fct1:addLoop();
loop1:addFilterBI({depth = "inner");
loop1:addFilterUser(isinner_oneblockloop);
block1:newEvent("count_blocks",my.so);

--Second function event
fct2 = mb:addFunction():addFilterWL("foo2");
loop2 = fct2:addLoop();
loop2:addFilterBI({depth = "outer");
block2:newEvent("count_blocks",my.so);
```

---

**B. Instrumentation Probes**

After having selected target instrumentation location through events, it is possible to describe the probes to insert into the binary. It is possible to define the probes either in Lua, or to provide the name of the probe function with the name of a shared library containing it, or to define a string with inline assembly code. With the first method, defining probes in Lua, the complete instrumentation configuration, including filters, probes, can be defined in one single file. We believe this can ease the use of MIL and help developers to define new profiling analyses. In this case, a call to this function, through the Lua interpreter is inserted in the binary and the script of the function is appended to the binary. As many calls to an interpreter may generate large overheads, the Lua Just-In-Time compiler [12] is added to the binary instead of the interpreter.

External calls to precompiled libraries containing the probes have been used by other tools, such as Dyninst [1]. Concerning the insertion of assembly text, we propose a gcc-like inline assembly that handles loops and global variables. Note that both external calls and inline assembly can be used at the same time for the same instrumentation point. When inserting a call to an external function, it is possible to disable context saving. This may be useful when the inserted function already saves and restores all the registers it will be using. Even if most of the users will only use the insertion to external calls or Lua functions, it is important to be able to insert assembly code because it enables significant optimization opportunities.

Given an event, any number of probes can be inserted. For each event, the following attributes can be specified:

- **Assembly code** to insert before/after the current probe,
- **Name** of the function to be called,
- **Library containing the function to be called,**
- **Name of the function, for functions defined in libraries or in Lua (in the MIL script)**
- **Parameters of the called function if any.**

The available parameters types are:

- **Immediate**
- **String**
- **Global variable**: Global variables are declared at the beginning of the specification file and have default values (immediate or string). They are usually used to store the return values of inserted functions and then passed to others.
- **Memory**: the default behavior is to return the target address of the instruction (of a jump, a load or a store for instance). This enables to capture memory references (accesses) or the value pointed by the memory reference (specified through an additional option). It can also help capturing complex control flow in case of indirections (computed destination). This type of parameter is only available for instruction-level events.
- **User defined function.**

User defined functions allow to pass to the probes any value computed statically from the analysis of the binary. Note that the probe is executed during the execution of the application, the evaluation of the parameters of these probes is at the instrumentation time. These functions receive the object (pointer) of the structure instrumented to perform a variety of queries and operations. Given a structure object, MIL is able to provide an access to the MAQAO Framework API (in Lua). It is beyond the scope of this paper to describe how this occurs internally. Detailed documentation is available on the MAQAO website [10]. Such user-defined parameters can depend on the instrumentation site and can be used to pass the information to the probe that the current loop is vectorized or unrolled for instance.
C. Using MIL to reduce instrumentation overhead

The first step to reducing overhead is to limit the number of code fragments to instrument. The filtering mechanism proposed in Section II-A proposes a simple way to instrument only some parts of the code. Other analyses may require more elaborated filters. Mussler et al. [8] use a predefined group of structural properties on the code as filters. Predefined filters may apply to limited class of applications, but selecting a priori which parts of the code are of interest is in general intractable. We propose user-defined filters, introduced as a more generic and complementary approach to predefined filters.

Optimizing instrumentation time also concerns the way the instrumentation is inserted into the code. Inserting a function call in a binary application has a cost, namely, the call instruction itself and the instructions to save the context before the call and restore it after. Inserting assembly instructions instead of calling a function removes this overhead. It may seem an extreme optimization, but it could be effective in cases where an instrumented routine contain loops that themselves call other functions. Although this kind of optimization requires architecture-specific considerations, it can reduce significantly the cost of inserted instrumentation calls.

III. MIL InterPRETER

The instrumentation language interpreter is developed as a new module for MAQAO [10]. MAQAO is a framework for analyzing and optimizing binary codes and it combines binary disassembly, rewriting, and assembly with analysis to identify code semantics and reconstruct control flow. The framework relies on usual compiler algorithms to detect functions, loops and basic blocks. MIL interpreter complements this information from the binary code with additional static analyses for handling optimized OpenMP codes. For the generation of the instrumented code, the interpreter drives a module of MAQAO, named MADRAS, that proposes a very low level API for instrumentation of x86_64 codes. MADRAS only considers instructions (no loops, functions, or blocks). This same module is used for disassembling the binary code.

Figure 4 shows the components of the instrumentation language and its integration in the MAQAO framework. Blacks arrows describe the components involved in the basic workflow of MIL. Gray arrows depict the additional possible interactions with the MAQAO framework.

A. Static Binary Instrumentation

Several approaches have been considered for instrumentation. In contrast to source code instrumentation (such as proposed in OPARI[13] for instance), or instrumentation that operates at an intermediate language level, binary analysis and instrumentation starts with the program code in its final executable form. Source instrumentation, while flexible, has the disadvantage of requiring recompilation of the application. Besides, the modification of the code can alter the effects of compiler optimizations. Working with the binary code avoids recompilation and preserves any optimization performed by the compiler. However, it does present additional challenges that need to be overcome to deliver a robust instrumentation solution. Below we discuss the issues that arise, the different alternatives, and our approach.

In general, static binary rewriting has two important advantages compared to the dynamic instrumentation. First, because the whole executable code is available when inserting instrumentation, static rewriting is more robust, able to perform more instrumentation requests, and can implement optimization methods more easily. Second, instrumentation occurs only once and before execution. Subsequent runs of the program will include the instrumentation.

For MIL, we use the MAQAO module, MADRAS, to disassemble binary. MADRAS can insert function calls or assembly instructions, delete instructions, or modify them by changing their opcode or operands. Inserted function calls can be wrapped with instructions for saving the context (contents of registers and stack frame) and restoring it after the inserted call, thus ensuring that the execution of the inserted function is transparent for the executable. MADRAS is also able to insert global variables and reference them in inserted or modified code. The MIL interpreter drives MADRAS in order to handle optimized OpenMP codes, as described in the following section.

B. Advanced Static Analysis

Multithreaded and optimized binary codes can present some specificities that introduce challenging binary analysis problems. Four of the major issues we encountered when analyzing codes, as far as code structure is concerned, were handling indirect branches, interleaved functions, inlining and probe insertion in some (difficult) cases. Our solutions to these issues are discussed below. To illustrate our explanations, we will be using br and dc (class A) benchmarks from NPB-OMP3.3 [14], and 312.swim benchmark (Medium) from SPEC OMP 2001 [15], all compiled by the Intel Fortran compiler (ifort) with -O3 optimization.

1) Indirect Branch Resolution: There is a major issue with indirect branches if not handled because they can hide exits
In order to obtain the complete set of exits of a function, we need to resolve indirect branches within functions. We introduce the concept of conditional probes. It consists of a regular probe combined with a set of conditions. The core idea is to set a condition on the target of the indirect branch once resolved. When considering function boundaries, the condition holds on the set of intervals that describes the limits of the function in terms of addresses. If the target is outside these intervals, then it is an exit and the probe would be executed. This algorithm is implemented internally inside MIL interpreter and requires no input from the users. If desired, users can disable indirect branch resolution in the configuration part of their script.

2) Interleaved Functions: At source level, it is relatively straightforward to identify the structure of a function, and functions have one entry and multiple exits (returns). While exits can be a little tricky to instrument in source, when we consider the general problem at binary level, optimizations achieved by the compiler may produce a more complex code structure. In binary, functions are only labels and it is even possible for two functions to share common blocks (due to compiler optimizations). These interleaved functions make the abstraction of the code more complex to handle and are generated for instance by the Intel compiler for OpenMP codes. When it comes to instrument a function, specific measures have to be taken.

To detect interleaved functions, we apply a connected component search on the control flow graph (CFG) of a given function, in our static analysis phase, and make the interleaved functions appear as separate components. If we consider the *bt* benchmark, the multi-threaded part of the code containing OpenMP directives (i.e., the part of code that will be called by the OpenMP runtime) is inlined. Figure 5 reveals a part of the control flow graph of one of the most time-consuming functions of the *bt* benchmark after MIL advanced static analyses. We can observe that it has successfully separated each component of the CFG. By default, MIL default behavior is to consider each component as a regular function. The name of the function is the same as the container function concatenated with a unique suffix and may be different when, for instance, inlining is detected. It is possible to disable this behavior in the configuration section (properties) of a MIL script file.

Let us consider a more complex example with the *swim* benchmark. This application contains four main (most consuming time) functions called from the program entry function. Taking a closer look to the code, we observe that three of these functions are actually inlined in the main routine. The inlined functions are called from the OpenMP runtime and entry/exit points are merged with the ones of the main routine. If only the main routine entry and exits points were instrumented, we would miss accounting for the three inlined routines. In fact, basic time profiling methods show only main and the routine not inlined as the two dominant time-consuming functions. The connected component analysis of MIL can discover the inlined functions and correctly apply function level instrumentation. The most important point of this approach is solving the problem using a static analysis, which is essential to reduce the instrumentation runtime overhead.

3) Inlined Functions: In the previous paragraph, we mentioned the *swim* benchmark example where functions where inlined. In the OpenMP class codes, that is what usually happens because the multi-threaded part of the code is actually called by the OpenMP runtime. As far as Intel compilers are concerned, the starting address of the multi-threaded code to execute is a pointer passed as a parameter of the function of the runtime which is responsible for calling that code. In general, detecting inlining is at least a challenging task and may be impossible at binary level. In MIL., we added a new heuristic that uses debug symbols, when available, to detect inlined functions. The instructions of functions that are inlined have a specific source line, the call site source line. Given the control-flow graph of a function, we look for subgraphs with basic blocks that have a high percentage (80%) of instructions that have that property. If we consider again interleaved functions, this heuristic works almost every time (with Intel compilers) and helps figuring out the name of the inlined function. Actually the name is given by the destination function of the call site. This algorithm is implemented internally and requires no input from the users. If desired, users can disable inlined functions detection in the configuration part of their script.

Even if it can be considered as a weak approach, we propose this debug information based heuristic because it is by far more accurate and faster than our original pattern matching algorithm (which does not require debug symbols and can be used as a fallback). It works with GNU and Intel compilers, considering that most users use one of these two compilers (in HPC) for a variety of languages (Fortran, C or C++)

4) Probe Insertion Issues: We introduce the concept of conditional probes in order to control the conditions under which a probe should be executed or not. After inserting a probe, a set of conditions can be applied on it. Thanks to this approach, it is possible to solve challenging issues.

One recurrent concern when dealing with instrumentation is the ability to insert probes wherever the users asks for. The usual solution involves using function relocation which actually does not work with function pointers since they...
continue to point to the original function. Inserting probes anywhere is not always possible at limited cost. For instance, on x86_64 architecture, the dc benchmark contains 11 functions with insufficient space for probe insertion happens. The common workaround is the concept of trampolines. It is used to find the required space close to the instrumentation site. This works most of the time. However, when considering one byte instructions (i.e. return instruction), trampolines are useless. Indeed, the smaller branch instruction that can be used for trampolines need at least two bytes. Therefore, when trampolines cannot be found or instructions are too short, the only existing technique is to resort to a trap instruction which has a size of one byte. Basically, a trap instruction invokes a system trap (signal) handler that can then execute the probe. The induced overhead is however consequent, larger by a factor of 15 compared to a regular probe.

We propose a new algorithm, which is part of MIL internals, that solves most of these cases, including the issue observed in the dc benchmark. Algorithm 1 details our approach. We perform a control flow analysis to figure out the predecessors of the current block where instrumentation should have taken place and instrument them. We go through these predecessors and verify that there is enough space to insert probes. If not, we have no choice but to insert a trap instruction. If there is enough space in all the predecessor blocks, then we have to determine for each of them if their target is the current insertion block or not (where the probe must be inserted). One case is quite complex, when considering a conditional branch. Since we only can insert the probe before it, we must add a condition so that the probe is only executed if we are sure that the flow is going to the current instrumented block. When considering the previous example, that means when branching to the exit block of the function.

In a nutshell, our method minimizes, and even removes, the number of trap instructions needed to correctly instrument a function.

5) Debug symbols: MIL works even without debug information. It is only used in order to relate assembly (disassembled binary) instructions to source line codes. As a consequence, only heuristics and analyses based on source lines will fail. Thus, all the major features and improvements we described, compared to the other instrumentation frameworks, still work. Having said that, we reasonably think that debug information will be most often present.

IV. BUILDING PERFORMANCE TOOLS

A systematic performance analysis approach must adopt a measurement methodology where critical performance bottlenecks can be identified at a coarse level and then instrumentation at a finer level can pinpoint performance issues. The challenge is to create a performance analysis system that supports both flexible instrumentation that preserves code properties relevant to the user and lightweight performance measurement that keeps overheads to a minimum.

The TAU Performance System [9] from the University of Oregon is a performance evaluation toolkit that supports several instrumentation, measurement, and analysis alternatives. TAU presents a good target to prototype a MIL-based instrumentation tool because it has challenging requirements. The goal in integrating TAU with MIL was to simplify the usage of TAU and create an efficient binary rewriter for multi-threaded applications. A new script, named tau_rewrite, has been added to the TAU distribution in order to add instrumentation to binary files and dynamic shared objects using MIL (included in the Program Database Toolkit). The tool enables users to inject a specified TAU measurement library while rewriting the executable.

Figure 6 shows the instrumentation file in MIL for the TAU performance tool. Probes calling the TAU runtime are placed at the entries and exits of the functions of the program. For each processed function, a specific registration call is added to the stub of functions executed when the binary is loaded. It is a more flexible approach to generate a stub of calls while discovering functions. At the end of the program, results are dumped by the TAU runtime. This is all what is necessary to enable binary instrumentation for the TAU performance measurement demonstrated below. The code achieving the same functionality, for Dyninst, requires around 200 lines of code, according to the authors.

Figure 7 depicts a simple standalone profiler completely written in MIL using embedded probes. The aim here is to show that we can easily and quickly implement a performance tool without having to actually manipulate complex data structures. The example contains two main sections, namely, runtime code and the events. The events part first specifies

```
Algorithm 1: InsertProbe

input : probe to insert
output : SUCCESS or FAILURE

inst ← GetInsertInst(prob);
block ← GetBlockFromInst(inst);
if IsSmallBlock(block) then
    predBlocks ← GetPredBlock(block);
    foreach pb in predBlocks do
        if IsSmallBlock(pb) then
            Insert a trap instruction (INT3)
        end // InstIsCondBranch(LIB) then
        if InstIsUncondBranch(LIB) then
            ProbInsert(inst.prob,BEFORE)
        else // InstIsCondBranch(LIB)
            brTargB ← GetBranchTarg(LIB);
            if brTargB == block then
                BC ← GetOppositeBranchCond(LIB);
                CV ← ExtractCompareVal(BC);
                ProbCondInsert(inst.prob,CV,BEFORE);
            else
                ProbInsert(inst.prob.AFTER);
            end
        end // InstIsCondBranch(LIB) then
        if InstIsUncondBranch(LIB) then
            ProbInsert(inst.prob.BEFORE);
        else
            ProbInsert(inst.prob.AFTER);
        end

```

that we will target function instrumentation, and that we will be using runtime code written in Lua (embedded probes). Then instrumentation probes are specified targeting the entries and exits of functions. The code of these probes is defined in the first section of the script. We create a global table (each thread will have its own) that will contain timing information (incremented by each call). At the end of the execution, the table is dumped.

V. EXPERIMENTS

In addition to evaluating MIL from a functional standpoint, it is important to compare the quality of the instrumentation framework on real applications and against existing binary rewriting tools. The OpenMP NAS parallel benchmarks [14] are used for testing instrumentation speed and execution overhead. For all experiments TAU is used for performance measurements.

Parallel applications such as OpenMP codes are optimized and transformed by the compilers in a way that hampers static binary instrumentation. Besides usual compiler optimizations, parallel regions are transformed into new functions and called through function pointers.

In the following experiments, we want to compare both the overhead of instrumented parallel NAS benchmark codes and overall the precision of the resulting data. To achieve this, the TAU profiling tool \(^1\) is configured to use MIL, Dyninst [1], PEBIL [3] tools. While using the latest versions of PEBIL and MIL, we must mention that we used Dyninst 7.0.1 because it covered more benchmarks (more crashes with Dyninst 8). To be completely fair, we verified that there was no overhead difference between versions 7 and 8.

\(^1\) We use the following TAU package:
http://tau.uoregon.edu/tau/releases/tau-2.21.2p2.tgz and the associated PDT
toolkit http://www.cs.uoregon.edu/research/tau/pdt-3.19p1.tar.gz
benchmarks but have huge overheads for PEBIL in all cases. PEBIL behaves like MIL in half of the Algorithm 1).

Concerning instrumentation techniques, we propose an approach very similar to what is presented in Dyninst [1], [16]. Also from the Dyninst Project, the DynC language [17] is a subset of C with a domain specification for selecting the location of a resource. Compared to MIL, user-defined filters in DynC can only be of limited types of statements (assignments, if-then-else, calls to "mutatee" functions). In MIL, these filters are written in Lua language with MAQAO API using all features of the Lua language itself (loops, function declaration, control structures) along with call to external functions, inline assembly and all the other features which are specific to instrumentation. More recently Dyninst Project introduced PatchAPI which is an intent to perform new types of instrumentation through CFG annotation.

PEBIL [3] or Saxena et al. [18]. More precisely, Dyninst uses two trampolines (two branches) before reaching the instrumentation code. This is due to the capacity of Dyninst to add/remove at runtime instrumentation code. We choose instead to have a static approach, able to insert code with low overhead. This difference shows on the previous section.

Work of Saxena et al. [18] uses an offline approach for binary disassembling and a backend based on nasm assembler for generating machine code for new instructions introduced during rewriting. With MIL, all assembly instructions added or modified are directly modified in the binary form (no textual representation). This enables assembly binary instruction modifications and injection.

Comparing PEBIL [3] and our work, PEBIL resorts to whole function relocation in order to apply overhead reduction techniques. However, function relocation does not work with OpenMP codes, since function pointers are passed to the OpenMP runtime. MIL is able to handle OpenMP codes because it uses block relocation and advanced static analyses

![Figure 8. Comparing overhead time on NAS OMP benchmarks for MIL, Dyninst and PEBIL using TAU. X axis reports the overhead ratio compared to the original run. Lower is better. Overhead ratios greater than 10 are cut. A zero ratio means a crash at runtime](image-url)
<table>
<thead>
<tr>
<th>%Time</th>
<th>Exclusive msec</th>
<th>Inclusive msec</th>
<th>#Call</th>
<th>#Subrs</th>
<th>Inclusive Name</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>total msec</td>
<td>usec/call</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>100.0</td>
<td>256</td>
<td>1:04.767</td>
<td>1</td>
<td>1012</td>
<td>.TAU application</td>
</tr>
<tr>
<td>32.4</td>
<td>20,744</td>
<td>21,005</td>
<td>201</td>
<td>207576</td>
<td>x_solve#omp#loop#1</td>
</tr>
<tr>
<td>32.0</td>
<td>20,525</td>
<td>20,720</td>
<td>201</td>
<td>155905</td>
<td>y_solve#omp#loop#1</td>
</tr>
<tr>
<td>31.5</td>
<td>20,260</td>
<td>20,427</td>
<td>201</td>
<td>136524</td>
<td>z_solve#omp#loop#1</td>
</tr>
<tr>
<td>2.2</td>
<td>1,402</td>
<td>1,402</td>
<td>202</td>
<td>0</td>
<td>compute_rhs#omp#region#1</td>
</tr>
<tr>
<td>1.0</td>
<td>515</td>
<td>666</td>
<td>2</td>
<td>100001</td>
<td>initialize#omp#region#1</td>
</tr>
<tr>
<td>0.2</td>
<td>150</td>
<td>150</td>
<td>100001</td>
<td>0</td>
<td>exact_solution</td>
</tr>
<tr>
<td>0.2</td>
<td>138</td>
<td>138</td>
<td>100001</td>
<td>0</td>
<td>1 binvcrhs [THROTTLED]</td>
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</table>

(a) TAU profiler using MIL

<table>
<thead>
<tr>
<th>%Time</th>
<th>Exclusive msec</th>
<th>Inclusive msec</th>
<th>#Call</th>
<th>#Subrs</th>
<th>Inclusive Name</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>total msec</td>
<td>usec/call</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>100.0</td>
<td>63</td>
<td>8:48.699</td>
<td>1</td>
<td>1012</td>
<td>.TAU application</td>
</tr>
<tr>
<td>33.1</td>
<td>2:53.907</td>
<td>2:54.752</td>
<td>201</td>
<td>136524</td>
<td>void targ4189a1()</td>
</tr>
<tr>
<td>33.0</td>
<td>2:52.955</td>
<td>2:54.255</td>
<td>201</td>
<td>207576</td>
<td>void targ413191()</td>
</tr>
<tr>
<td>32.8</td>
<td>2:52.373</td>
<td>2:53.392</td>
<td>201</td>
<td>155905</td>
<td>void targ414bf1()</td>
</tr>
<tr>
<td>0.7</td>
<td>2,656</td>
<td>3,541</td>
<td>2</td>
<td>100001</td>
<td>1770777 void targ402baf()</td>
</tr>
<tr>
<td>0.3</td>
<td>1,378</td>
<td>1,378</td>
<td>202</td>
<td>0</td>
<td>6826 void targ40c003()</td>
</tr>
<tr>
<td>0.2</td>
<td>896</td>
<td>896</td>
<td>1</td>
<td>0</td>
<td>896002 void targ4042de()</td>
</tr>
<tr>
<td>0.2</td>
<td>885</td>
<td>885</td>
<td>100001</td>
<td>0</td>
<td>9 void exact_solution()</td>
</tr>
</tbody>
</table>

(b) TAU profiler using Dyninst

<table>
<thead>
<tr>
<th>Exclusive</th>
<th>Inclusive</th>
<th>#Call</th>
<th>#Subrs</th>
<th>Inclusive Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>msec</td>
<td>total msec</td>
<td></td>
<td></td>
<td>usec/call</td>
</tr>
<tr>
<td>100.0</td>
<td>1:05.057</td>
<td>1:05.900</td>
<td>1</td>
<td>607742</td>
</tr>
<tr>
<td>0.2</td>
<td>153</td>
<td>153</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.2</td>
<td>137</td>
<td>137</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.2</td>
<td>132</td>
<td>132</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.2</td>
<td>130</td>
<td>130</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.2</td>
<td>112</td>
<td>112</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.2</td>
<td>108</td>
<td>108</td>
<td>100001</td>
<td>0</td>
</tr>
<tr>
<td>0.1</td>
<td>59</td>
<td>59</td>
<td>815</td>
<td>0</td>
</tr>
</tbody>
</table>

(c) TAU profiler using PEBIL

Fig. 9. Comparison of pprof tool output for thread 1 of NAS OMP bt A benchmark when MIL, Dyninst and PEBIL tools being used by TAU profiler

To detect interleaved functions. Andrew Bernat et al. [19] also suggest that the conservative approach of PEBIL is dangerous because it assumes that particular instructions (e.g., calls) are safe to move without transformation. PEBIL focuses on providing a way to insert code snippets (avoiding a call in the trampoline) and minimizing context saving. MIL offers similar mechanisms with the nowrap option, reducing context saving and being able to directly specify inline assembly code. Moreover, PEBIL is an API-oriented approach which is less flexible than an instrumentation language.

For OpenMP performance analysis, POMP [20] proposes a performance monitoring interface for OpenMP. Tools implementing this interface, such as OmpTrace[21] based on Dynamic Probe Class Library[22] or OPARI[13] are based on source to source modifications, with the inherent known limitations: instrumentation may prevent compiler optimization, the code analyzed is not the code the user want to analyze. In INTONE [23], OpenMP directives are directly instrumented by the compiler. The approach taken with MIL is to provide instrumentation able to capture per thread information. OpenMP parallel loops, OpenMP API functions can therefore be instrumented and results of the instrumentation can expose OpenMP parallel execution to the performance system. Besides most compilers implement directives by inserting calls to the runtime. This is dependent of the OpenMP runtime but can provide a larger implementation of the POMP interface. Going further and capturing runtime decisions (size of chunks for instance) is not handled by MIL so far.

Finally, several instrumentation languages have been proposed in the past [6], [7], [8]. Atune-IL instrumentation language corresponds to pragma inserted in the source code. These pragmas are then handled to the instrumentation tool. This approach enforces recompilation of the application. Muslcer et al. propose an instrumentation tool driven by configuration files. The filters used in their instrumentation language do not handle blocks or instructions and value profiling does
not seem possible, as it is in our tool. However, they propose predefined filters using some more elaborate metrics on the code. Predefined filters may apply to limited class of applications, but selecting a priori which parts of the code are of interest is in general intractable. This is not the approach taken here in MIL. Besides, their tool is based on Dyninst for binary rewriting.

VII. CONCLUSION

In this paper, we present MIL, a rich instrumentation language that reduces the complexity of writing performance analysis tools for high performance computing. Using static binary instrumentation that does not require additional compilation pass, MIL proposes a rich interface to instrument applications for a large range of uses, from profiling of functions, hardware counter analysis to value profiling. MIL provides a filtering mechanism to instrument only some specific code fragments, from functions, loops, to individual assembly instructions, and different types of instrumentations can be performed simultaneously on different code fragments. In this paper, we illustrated the capabilities of MIL on parallel OpenMP applications. Besides, a scripting mechanism relying on an API for code analysis offers the possibility to extend instrumentations and performance analyses.

We believe MIL offers a unique way to implement and investigate new performance analysis techniques, able to tackle the challenging complexity of performance tuning for multi-threaded applications. We have demonstrated the flexibility of MIL through the study of different scenarios, exploring different granularities and through its integration in TAU performance analysis framework. The MIL interpreter offers a rich abstraction layer based on static analysis and a robust binary rewriting tool. Execution overheads have been evaluated on NAS Benchmarks and compared to Dyninst and PEBIL, similar instrumentation frameworks. They show that the instrumentation provided by MIL has a low overhead.

For future work, we plan to extend the language to support OpenMP events. We also plan to introduce an iterative instrumentation approach in order to automatically take into account previous profiles. It may be a key feature in designing systematic performance evaluation tools, refining at each step the identification of performance issues.

REFERENCES


