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SIMS analyses applied to open an optical window in 4H-SiC devices for electro-optical measurements

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Abstract. 4H-SiC vertical bipolar power diodes have been fabricated with bilayer metallic anode contact based on an Al-Ti-Ni ohmic contact and a thick Al over-metallization. An optical window of 100 × 100 µm\textsuperscript{2} has been created through the anode contact with a SIMS Cameca IMS 4F equipment using Cs\textsuperscript{+} primary ions at 10 kV and with a beam spot size of 500 nm. The current/voltage characteristics of the diodes show that the SIMS process does not induce an increase of the leakage currents in forward nor in reverse bias. OBIC UV photogeneration occurs under the optical window and not under the contact metal.

Introduction

Wide bandgap semiconductors and silicon carbide materials in particular are very promising for high temperature, high-power and high-frequency device applications. In order to reduce the fabrication cost by decreasing the on-state resistance and the thickness of the epitaxial layers, the two epitaxial layer parameters, thickness and doping level, must be optimized for a projected blocking voltage, like the n-base in high voltage devices [1]. Device structure design needs an understanding of the avalanche mechanism in reverse biased junctions with a precise determination of the impact ionization coefficient in the required electrical field range.

To understand the avalanche mechanism in the reverse biased junction, Optical Beam Induced Current (OBIC) is a suitable method that enables to extract the ionization coefficients [2, 3]. During OBIC measurements, an UV laser beam has to penetrate in the SiC epilayer close to the junction under the metallization contact. Thus, an optical window is needed through the contact metallization on the surface which could be the anode contact in the case of vertical bipolar diodes.

We propose and demonstrate in this paper a simple method to open this window on SiC vertical bipolar devices. This method can be extended to other wide band-gap semiconductors like diamond or other hard technological materials.

Extraction of impact ionization coefficients using OBIC requires a breakdown in the bulk of the main junction under the avalanche mode. Therefore, the lateral protection such as mesa and/or JTE, but also the optical window, must be accurately designed and fabricated [4]. In general, an optical window in the anode is realized by photolithography patterns. With this method we have realized several runs of SiC diodes and frequently we had difficulty in obtaining bulk breakdown in the avalanche mode. We therefore propose an alternative method that could be useful in specific cases, in particular by enabling us to focus on the geometry of the lateral periphery of the vertical power devices.
Our method adopts Secondary Ion Mass Spectrometry (SIMS) analyses to open, locally, the contact metallization as a technological step in device fabrication for OBIC.

Moreover, the real time analyses of the SIMS spectra enables an accurate definition of the etching process at the metal/SiC interface, which may be critical when thin junctions are created in the surface by low energy ion implantation.

**Experimental**

Vertical bipolar power diodes have been fabricated on a commercial n$^+$ 4H-SiC substrate with an n-type drift and a top p-type anode-epilayer. Details on the fabrication process have been published elsewhere [5]. Ohmic contacts for the anode have been realized in two steps: first, by patterning followed by a rapid thermal annealing a 250 nm layer, based on Al, Ti and Ni, and secondly, by adding a 500 nm thick Al over-metallization on top of the contacts. The diodes have a circular geometry with a diameter of 1 mm (Fig. 1).

The optical window has been opened with a SIMS Cameca IMS 4F equipment [6]. Cs$^+$ primary ions have been accelerated at 10 kV, the impact energy was 14.5 keV and the spot size of the beam was 500 nm. As shown in Figure 1 the crater (created) on the surface by the scanned Cs$^+$ primary ion beam is well defined with vertical sides, and is about 100 × 100 µm$^2$ large. In order to avoid side contamination effects, the SIMS analyzed zone is smaller with a diameter of about 30 µm.

Finally, I-V current/voltage measurements and OBIC analyses have been performed.

**Results and discussion**

The SIMS spectra obtained on the diode from Figure 1 is presented in Figure 2. The total depth (of the crater) measured with a stylus profilometer, is 0.98 µm. In Figure 2, we remark the stripping of the different metal layers, with the Al over-metallization at the top surface, the intermediate Al, Ti and Ni ohmic contact interface and finally the SiC epilayer corresponding to the flat C$^{12}$ and Si$^{28}$ profiles.

Figure 3 presents typical electrical I-V current/voltage characteristics of the diodes in forward and reverse bias at room temperature, before and after the SIMS process. These characteristics have not a significant evolution, concluding that the SIMS process does not induce an increase of the leakage currents in forward or reverse bias. The same forward current is obtained at 3V and the avalanche breakdown of the p-n junction occurs at the same reverse bias voltage. In Ref. [5] we
have shown that the avalanche multiplication is the main breakdown mechanism in this kind of device and that the positive temperature coefficient is very weak.

![SIMS crater total depth: 0.98 µm](image)

Fig. 2: SIMS elementary spectra of the anode contact metallization through the three zones: over-metallization, ohmic-contact interface and the front of SiC epilayer.

OBIC measurements have been performed on these diodes with an UV laser beam (351 nm) with a spot size of 3µm diffusing through the cratering formed by SIMS. Figure 4 presents the OBIC photocurrent obtained at 0 and -50V in reverse bias polarization. It is clearly demonstrated that photogeneration occurs under the optical window created by the SIMS crater and not under the metal layer enclosing this crater. This is a crucial point for OBIC technique. This result validates our method of opening of a window in the metal layer using a SIMS analysis.

![Current/voltage characteristics](image)

Fig. 3. Current/voltage characteristics of the diode compared before and after SIMS in forward (a) and reverse (b) bias.

As an ultimate proof for the suitability of this method, the experimental extraction of the impact ionization coefficients in 4H-SiC has been performed from OBIC analyses on these diodes with an optical window opened by SIMS. More details on these results are presented in another paper in this conference [7].
Summary

We propose and demonstrate in this paper a simple method of opening a window on SiC vertical bipolar devices. This enables to study experimentally the avalanche mechanism in reverse biased junction by electric-optical measurements. Our method consists in using SIMS analyses to locally open the contact metallization as a technological step with the Cs+ primary ion beam by stopping accurately the etching process at the metal/SiC interface. The SIMS process does not induce an increase of the leakage currents in forward or reverse bias on the bipolar diodes we studied. OBIC measurements performed on these diodes with an UV laser ray clearly demonstrated photogeneration under the optical window and not under the metal layer. The photocurrent evolution with the diode reverse bias enables extraction of the impact ionization coefficients.

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