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K. Nouri, Kamel Haddadi, O. Benzaïm, T. Lasri, M. Feham. Substrate integrated waveguide (SIW) inductive window band-pass filter based on post-wall irises. *European Physical Journal: Applied Physics*, 2011, 53 (3), pp.33607-1-5. 10.1051/epjap/2010100057 . hal-00672781

HAL Id: hal-00672781

<https://hal.science/hal-00672781>

Submitted on 22 Feb 2012

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Substrate Integrated Waveguide (SIW) inductive window band-pass filter based on post-wall irises

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Received : 1 September 2009

Published online :

Abstract. In this article, we demonstrate a new inductive-window 5.245-GHz-band-pass filter based on post-wall irises by standard PCB process on Epoxy FR4 substrate. A new and easy to build microstrip-to-waveguide transition that consists of three tapers is also designed to connect the filter to standard measurement system. Both simulated results and measurements have shown insertion-loss lower than 5 dB within 14% bandwidth around 5.245 GHz and input return loss better than 25 dB over the frequency range.

PACS. 41.20.-q Applied classical electromagnetism - 41.20.Jb Electromagnetic wave propagation; radiowave propagation.

1 Introduction

Filters are widely used in satellite and modern communication systems. General requirements for these filters are low insertion loss, high power capacity, small size and high rejection with low cost realization [1]-[2].

Rectangular waveguide components have advantages over other planar transmission structures for microwave and millimeter-wave band applications because of their low loss and high Q-factor. But their high cost and difficult integration to planar circuits prevent these components from being used in low-cost high-volume applications. Recently, a new concept of substrate integrated waveguide (SIW) technique has been proposed [3].

Many components such as antennas, power dividers, filters and duplexers, based on SIW technology have been reported [4]-[6]. This technology offers a good option for the design of microwave and millimeter-wave filters. It provides a low profile, low cost standard printed circuit boards (PCB) and low weight scheme while maintaining high performance [7]-[9].

SIW structures are realized by two bilateral walls of periodic metallic via-hole arrays in substrate and grounded

planes which can be easily interconnected with other elements of the system on a single substrate platform without tuning. This system can be miniaturised into small package (called the system in package - SIP) with small size and low cost [10]. This paper presents the design and performance of a new inductive-window filter using post-wall irises operating around 5.245 GHz. We begin with a brief analysis of C-band SIW-microstrip lines. Then, a new stepped transition is proposed to connect SIW circuits to microstrip ones. The filter is designed and fabricated by using low cost printed circuit board (PCB) technology. The measurements are performed by means of a vector network analyzer (HP8720C). Electromagnetic simulations and measured responses of the filter are presented to validate the proposed approach. Compared to previous studies related to SIW filters using post-wall irises, a wide bandwidth operation of about 14% and return loss better than 25 dB are achieved by the proposed filter associated to stepped transitions [11]-[13].

2 Integrated-Waveguide Analysis

A substrate integrated waveguide (SIW) is made of metallic via-hole arrays in the substrate between top and bottom metal layers replacing the two metal sidewalls. A comparison between a conventional waveguide and a SIW is given in Fig. 1.

The propagation properties in the SIW and in the conventional metallic rectangular waveguide (RWG) are very similar. In particular, the electromagnetic field distribution is TE_{10} -like [14]. Thus, the initial dimensions of the SIW resonator cavity can be determined by the conventional resonant frequency formula of metallic waveguide resonator, where the length and width of the metallic cavity, L_G and W_G , should be replaced by the equivalent width W and length L of the SIW cavity because of the presence of vias sidewall.

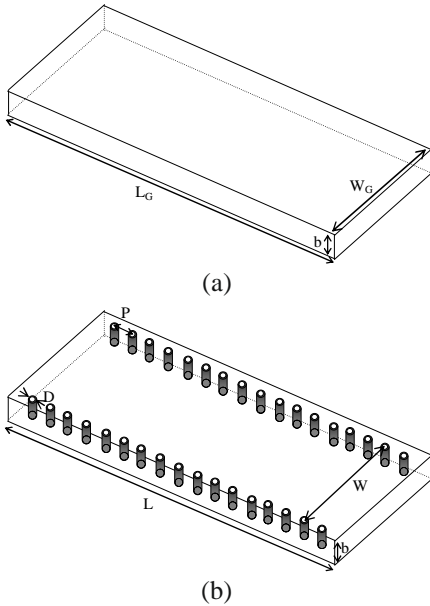


Fig.1. Configuration of the waveguides. (a) Conventional rectangular waveguide. (b) On-substrate integrated waveguide.

A SIW cavity can be designed by using the following relations [8, 14, 15]:

$$L = L_G - \frac{D^2}{0.95P} \quad (1)$$

$$W = W_G - \frac{D^2}{0.95P} \quad (2)$$

The equations (1) and (2) denote respectively the relations between the dimensions of the conventional and the SIW waveguides. The parameters D and P are the diameter and the period of via holes respectively.

We propose in this section an electromagnetic analysis of a SIW cavity at the test frequency 5.5 GHz. In comparison with conventional waveguide, the SIW is an artificial periodic waveguide. Therefore, the post-diameter D and the period P of the posts may significantly affect the propagation in the SIW and the return loss at the input ports. In order to ensure the propagation of the electric field in the synthesized waveguide section, an optimization of P and D is proposed. A full-wave finite-element method (FEM) based on a commercial software package (HFSS) is used for simulations. The top, middle, bottom and sidewall metalizations are all copper and the dielectric material is an Epoxy FR4 substrate with $\epsilon_r = 4.4$ and $\tan \delta = 0.015$. The distance between the rows of the centers of via holes is $W = 19$ mm. After optimization, D is set to 1 mm and P to 1.8 mm.

The figure 2 shows the electric fields distribution in the cross-sectional view in the conventional rectangular waveguide and in the SIW waveguide.

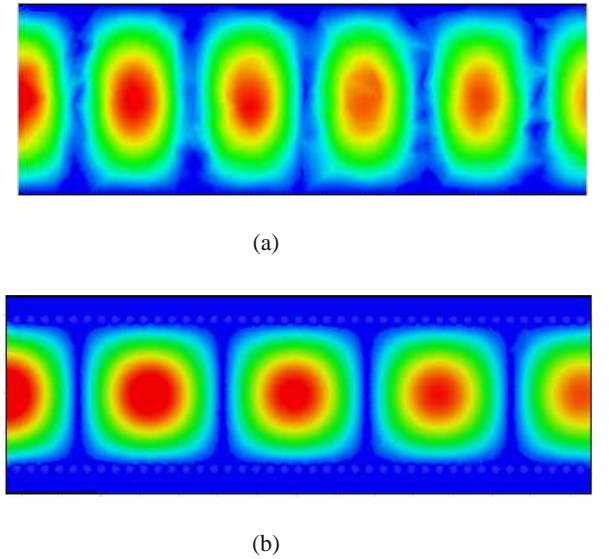


Fig.2. Electric fields distributions in a rectangular waveguide and in a SIW. $F = 5.5$ GHz (HFSS simulation). (a) RWG. (b) SIW.

From these graphs, one can note that the dominant mode of the SIW is quite similar to the TE_{10} mode of the conventional waveguide. The maximum field is present at the middle of the guide.

In order to perform robust S-parameters measurements of the SIW circuits, and to connect them to standard measurement equipment, a transition from microstrip to the SIW is proposed in the next part of this paper.

3 Microstrip Stepped transition

Many approaches have been reported for the integration of waveguides and planar circuits [7, 9, 17, 18, 19]. In this work, a new and easy to build microstrip to SIW planar stepped transition is discussed. A schematic view of a SIW with two stepped transitions is shown in Fig. 3.

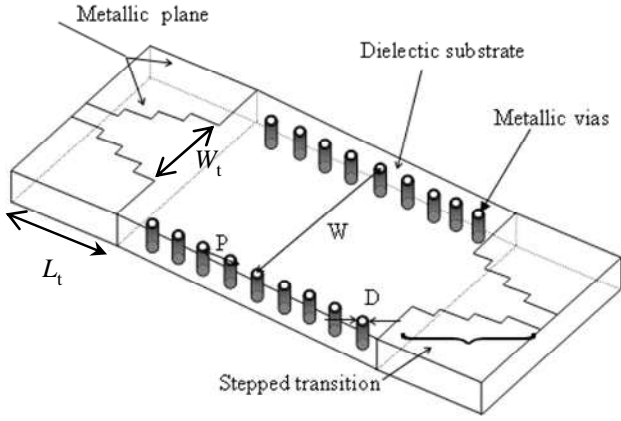


Fig.3. Topology of the SIW with two stepped transitions.

As shown in Fig. 3, the transition comprises three tapers. The dimensions of the tapers as a function of L_t and W_t are summarized in table 1.

Taper i	1	2	3
Width W_{ti} (mm)	$W_t/3$	$2W_t/3$	W_t
Length L_{ti} (mm)	$L_t/3$	$L_t/3$	$L_t/3$

Tab. 1. Geometric parameters of the microstrip to SIW planar stepped transition.

In order to achieve a low-loss broadband response, the transition is designed by simultaneously considering both impedance matching and field matching. Thus, due to the electric field distribution in the SIW, each transition is connected to the center of the width of the SIW, since the electric field of the fundamental mode is maximum in this place [20]. The optimization of the transition is performed by means of electromagnetic simulations by varying the dimensions (L_t , W_t) of the stepped geometry. After optimization, the dimensions retained are $W_t = 5.82$ mm and $L_t = 24.9$ mm.

The distribution of the electric field is given in Fig. 4.

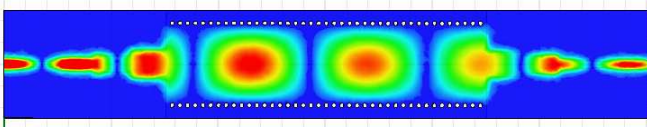


Fig.4. Electric field distribution in SIW with stepped transitions. $F = 5.5$ GHz (HFSS simulation).

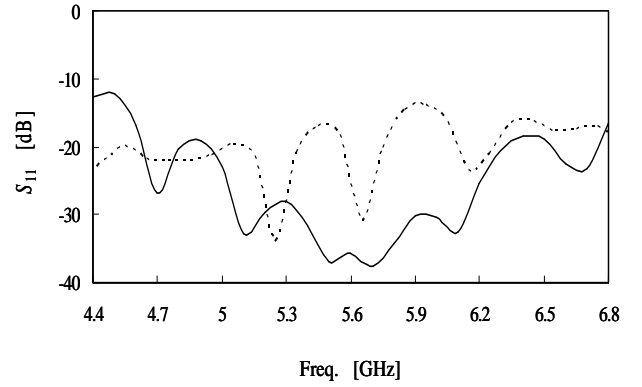
This figure denotes a good input/output electric field matching.

The proposed SIW with stepped transitions was fabricated on a substrate of Epoxy FR4 under a standard PCB process. A photograph of the prototype is shown in Fig. 5.

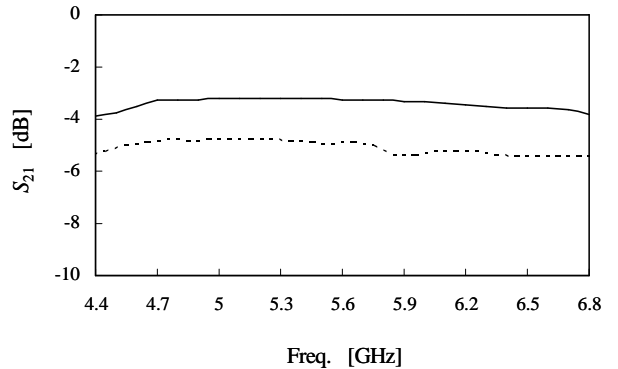


Fig.5. Photograph of the manufactured SIW with stepped transitions.

To measure the S-parameters, a SMA connector is connected to each port. The measured S-parameters are performed with a vector network analyzer (HP8720C) in the frequency band 4.4 – 6.8 GHz. The simulated and measured results are shown in Fig. 6.



(a)



(b)

Fig.6. Simulated and measured S-parameters of the SIW with stepped transitions (— HFSS Simulation - - - Measurement). (a) S_{11} as a function of frequency. (b) S_{21} as a function of frequency.

From the figure 6-a, the measured return loss characteristics are less than 13 dB in the frequency range 4.4 – 6.8 GHz. From the figure 6-b, the measured insertion loss are around 5 dB. The discrepancies of about 1.5 dB with the simulated insertion loss are mainly due to the SMA connectors that have not been taken into account in the simulation. Thus, one can conclude that these results are in good agreement with the simulated ones and validate the proposed microstrip-SIW transition.

In the last part of this work, a SIW inductive band-pass filter based on post-wall irises with stepped transitions is proposed.

4 SIW inductive window band-pass filter

Filters with operation frequencies around 5 GHz and wide bandwidths will be needed in future Wireless Local Area Network (WLAN) applications. Judging from its electrical performance, the SIW is a good compromise between the air-filled rectangular waveguide and planar circuit [21]. Therefore, a 4th degree SIW cavity filter with a bandwidth of 730 MHz around 5.245 GHz is designed and realized in this last part. The cavities of the filter using inductive post-wall irises are shown in Fig. 7.

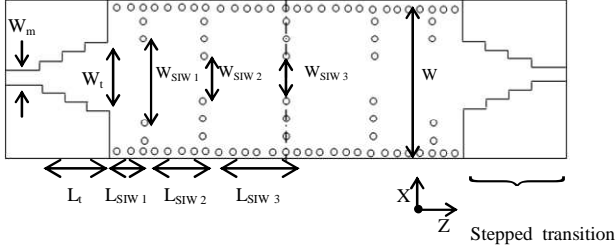


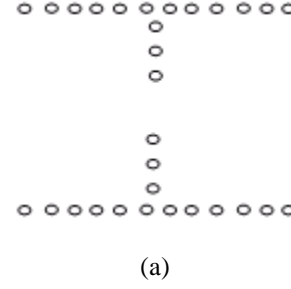
Fig. 7. Geometry of SIW inductive-window filter using post-wall irises. $F = 5.245$ GHz.

The dimensions of the filter are selected as, $W = 24$ mm, $D = 1$ mm, $P = 2$ mm. The structure comprises four resonant cavities coupled by five metal irises (Fig. 8-a). The design revolves on the determination of the lengths L_{SIW_i} ($i = 1, \dots, 3$) of the cavities and the widths W_{SIW_i} ($i = 1, \dots, 3$) of the resonators. To that end, a general method is briefly presented to design a filter based on post wall irises. First, the integrated waveguide is mapped to a conventional rectangular waveguide using the relations (1) and (2). As illustrated in Fig. 8-b, a T-network can model an iris in rectangular waveguide. The theory developed by Marcuvitz in [22] for iris in a waveguide is used to calculate X_a and X_b . The T-network is then transformed into a K -inverter (Fig. 8-c) using (3) and (4) [23].

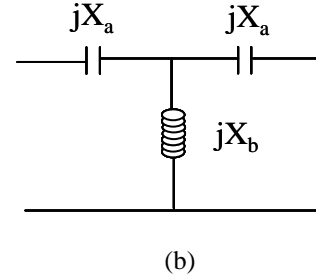
$$K = Z_0 \left| \tan\left(\frac{\phi}{2}\right) + \tan^{-1} \frac{X_a}{Z_0} \right| \quad (3)$$

$$\phi = \tan^{-1} \left(\frac{2X_b}{Z_0} + \frac{X_a}{Z_0} \right) - \tan^{-1} \frac{X_a}{Z_0} \quad (4)$$

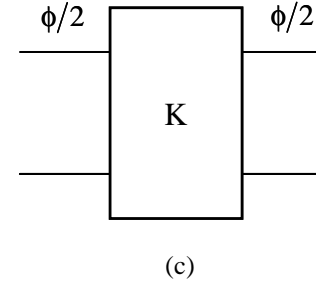
Using this model, the filter is designed on the basis of the well-known synthesis techniques for inductive irises in rectangular waveguide. Practically, the lengths and widths of SIW cavities obtained by this procedure will be adjusted a little bit to satisfy the specifications by using a full-wave analysis with HFSS.



(a)



(b)



(c)

Fig. 8: Design process of inductive post-wall irises filter. (a) post-wall iris in waveguide. (b) Equivalent T-network. (c) Equivalent K -inverter network.

With the model presented, we have designed the 4th-degree SIW filter. We used a 0.8 mm thick dielectric substrate with a relative permittivity of 4.4 and a loss tangent of 0.015. Table 2 summarizes the dimensions of the filter after HFSS optimization of the theoretical dimensions.

i	1	2	3
L_{SIW_i} (mm)	6.6	13.8	15.1
W_{SIW_i} (mm)	11.96	9.42	8.9
$W_m = 1.4$ mm, $W_t = 5.82$ mm, $L_t = 21.6$ mm			

Tab. 2: Dimensions of SIW inductive-window filter.

The Photograph of the manufactured SIW filter with stepped transitions is given in Fig. 9.

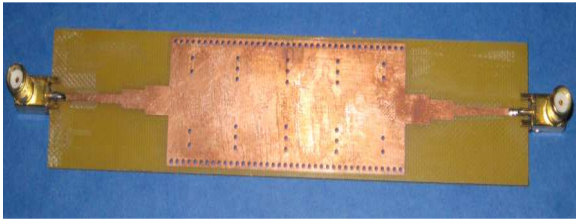


Fig. 9. Photograph of the manufactured SIW inductive filter.

The HFSS and measured reflection and transmission coefficients are shown in Fig. 10.

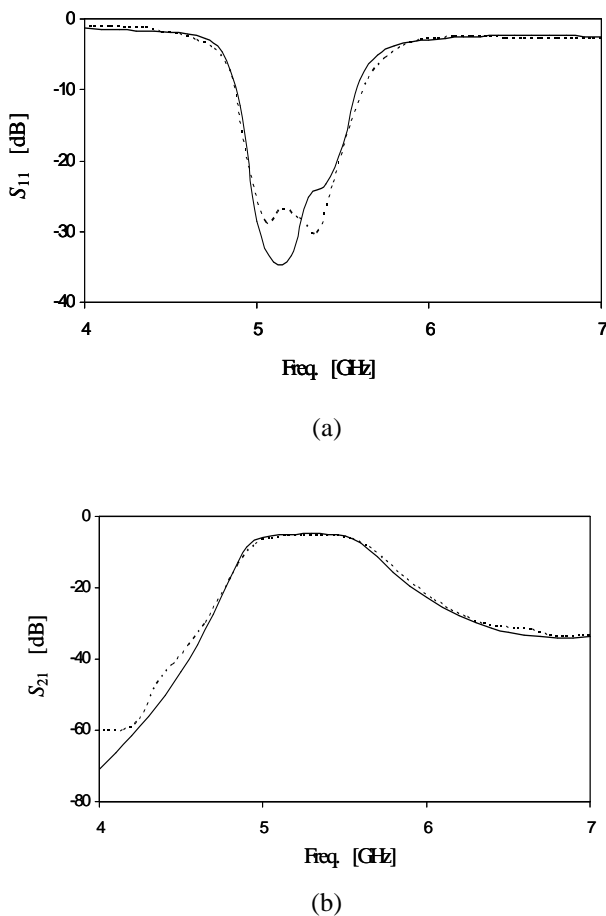


Fig. 10. Return losses and insertion losses of the SIW inductive-window filter with post-wall irises a) S_{11} as a function of frequency. (b) S_{21} as a function of frequency. (— HFSS Simulation - - - Measurement).

One can note a good agreement between the simulation and measurement results. In the passband, the insertion loss is around 5 dB and the return loss is less than 25 dB.

5 Conclusion

A SIW cavity filter with post-wall irises using a new type microstrip line to SIW transition is proposed and demonstrated around 5 GHz. The fabricated 4th degree filter exhibits an insertion loss of 5 dB and the return loss is better than 25 dB in the passband. The proposed filter has small size, low loss, and can be directly integrated with other circuits without any additional mechanical assembling tuning. Additionally, the structure proposed is easily scalable over microwave and millimeter frequency ranges.

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