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A Turbo Iteration Algorithm In 16QAM Hierarchical Modulation

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Abstract—In the next generation communication protocol “DVB-SH”, it offers a new transmitting scheme based on the turbo code, OFDM modulation and 16QAM hierarchical modulation to increase the flexibility in the information delivery. This scheme is using Hierarchical modulation to enhance the UE (user equipment) performance which is in the center of a sector and also ensure the basic service when UE move to the region of a sector. Hierarchical modulation uses two different bit streams on to different constellation of the constellation map. This scheme ensures two different bit streams have corresponding BER performance and then bring on the flexibility to the transmitting service. The simulation results indicates that in the Rayleigh and Gaussian channel, this scheme can gain a very high BER performance of high priority bit stream, and in the high SNR, it can also gain a high BER performance of low priority bit stream.

Keywords- turbo iteration; soft-input soft-output; hierarchical modulation; OFDM

I. INTRODUCTION

At 14th February 2007, the DVB Steering Board has approved the DVB-SH(satellite services to handhelds) specification in Geneva at the 53rd meeting of the DVB Steering Board[1]. DVB-SH includes features such as turbo coding, OFDM modulation and hierarchical modulation to ensure transmission of high quality video information, at the same time it can also take account of the low quality information when the SNR condition is good enough. Since the handhelds terminals are always in the moving conditions, so the hierarchical modulation can satisfied this users who are in the center of one sector to receive high quality signals(high priority bits& low priority bits), when the user is moving to the region of one sector, it can ensure receiving the basic signals(high priority bits).

The model discussed in this paper is a turbo code with 16QAM-OFDM system which based on the DVB-SH scheme. The turbo code has excellent performance by using the turbo iteration. But in the traditional schemes with Gray constellation mapping, the total performance of iteration is not

used up. If designed properly, we can gain better performance. So not only the DVB project but also LTE project are interested in this technology now[2-4].

This paper discusses a new iteration scheme which mixed with the hierarchical modulation. It can achieve an additional gain compare to the traditional schemes. With the different modulation factor($\alpha=1, 2, 3$) and different modulation mapping, the BER performance are different and the BER performance of the novel modulation ideal is better than which of the traditional mapping.

II. SCHEME DESCRIPTION

A. modulation scheme

Figure 1 shows the system simulation scheme. The feature blocks include turbo encoder, hierarchical modulation and OFDM modulation. At the turbo encoder port, the HP (high priority bit stream) bits and the LP (low priority bit stream) bits are separated and inputted into two turbo encoders, then are interleaved. After that, the two bit streams are fixed together and sent to the modulator, in the modulator, they are mapped onto the respective constellation location. The OFDM modulation block consists of S/P block, IFFT block and CP block. OFDM demodulation block consists of CP deleting block, P/S block, and FFT block. The simulation processing is supposed to be synchronized.

B. Algorithm Descriptions

1) Turbo decoding algorithm

The turbo code principle is provided by C.Berrou at ICC'93 meeting firstly, after a fast developing, now there are several decoding algorithm like: MAP algorithm, soft-Viterbi algorithm and so on[5]. In [5-8], it provides the design principles of turbo code and the iteration decoding method. In the MAP decoder, it decides the log a posterior probability

(LAPP) ratio defined as^[9]:

$$L(u_t) = \log\left(\frac{p(u_t = 1|y)}{p(u_t = 0|y)}\right) \quad (1)$$

Incorporating the code's trellis, this may be written as:

$$L(u_t) = \log\left(\frac{\sum_{s \in 1} p(s_{k-1} = s', s_k = s, y) / p(y)}{\sum_{s \in 0} p(s_{k-1} = s', s_k = s, y) / p(y)}\right) \quad (2)$$

Consider (2), if we want to calculate the value of $L(u_t)$, first we need to compute $p(s', s, y) = p(s_{k-1} = s', s_k = s, y)$, in BCJR algorithm [7] for doing this is:

$$p(s', s, y) = \alpha_{t-1}(s') \cdot \gamma_t(s', s) \beta_t(s) \quad (3)$$

we define the modified probabilities:

$$\tilde{\alpha}_t(s) = \alpha_t(s) / p(y_1^t)$$

and $\tilde{\beta}_t(s) = \beta_t(s) / p(y_{t+1}^T | y_1^t)$

In summary, we modify BCJR-MAP algorithm by using (2) and (3) to obtain:

$$L(u_t) = \log\left(\frac{\sum_{s \in 1} \tilde{\alpha}_{t-1}(s') \cdot \gamma_t(s', s) \cdot \tilde{\beta}_t(s)}{\sum_{s \in 0} \tilde{\alpha}_{t-1}(s') \cdot \gamma_t(s', s) \cdot \tilde{\beta}_t(s)}\right) \quad (4)$$

we can deduct that $\gamma_t(s', s)$ has the direct ratio to [10]:

$$\gamma_t(s', s) \propto e^{-\frac{1}{2\sigma^2}|y - hs|^2} P(s|s') \gamma_{dec}(u|y) \quad (5)$$

where $e^{-\frac{1}{2\sigma^2}|y - hs|^2}$ is the information from the channel, and $P(s|s')$ is the probability from state s transform to s' , and $\gamma_{dec}(u|y)$ is the extrinsic probability for receive data u from the symbol y .

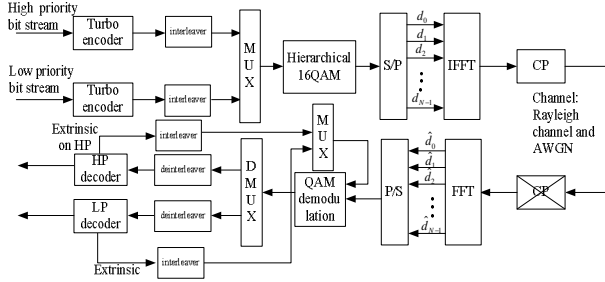
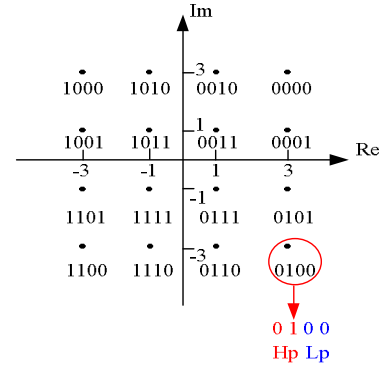


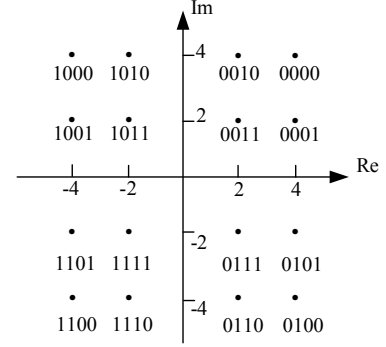
Figure 1 hierarchical modulation simulation scheme

2) Modulation Mapping

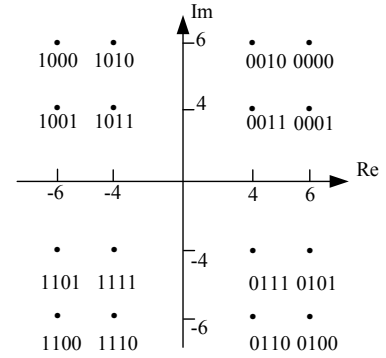
The modulation mapping scheme is depicted in reference [1]. When the 16QAM modulation is used in the modulation block, and when the modulation factor $\alpha=1,2,4$, the modulation mapping is shown in figure 2. the normalize factor of the three mapping is $\sqrt{10}$, $\sqrt{20}$, $\sqrt{52}$.



(a) α equal to 1



(b) α equal to 2



(b) α equal to 4

Figure 2 16QAM hierarchical modulation

The modulation location of Hp bits and Lp bits are depicted in figure 2 (a). This mapping scheme is different to the traditional gray mapping, we can see that the four symbols in every quadrant have the same Hp bits but different Lp bits, this is also called constellation overlapping in reference [2] which ensure the Hp bits to be transmitted correctly.

3) De-modulator to Decoder Iteration Algorithm

Considering on the mapping constellation given at section 2.2.2, there are four bits in each mapping symbol. So the APP of one bits in a symbol can be depicted as [10]:

$$\begin{aligned} P(d_k^1 | y_k) &= \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} P(d_k^1 \dots d_k^4 | y_k) \\ &= \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} \frac{P(y_k | d_k^1 \dots d_k^4) P(d_k^1 \dots d_k^4)}{P(y_k)} \end{aligned} \quad (6)$$

Where $d_k^1, d_k^2, d_k^3, d_k^4$ is the four bits in symbol y_k . Since we consider the interleaver to be ideal, $d_k^1, d_k^2, d_k^3, d_k^4$ is independent. So (6) can be reformulated as:

$$P(d_k^1|y_k) \approx \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} \frac{P(y_k|d_k^1, \dots, d_k^4)P(d_k^1) \dots P(d_k^4)}{P(y_k)} \quad (7)$$

Which can be expressed in the case of a white Gaussian noise n_k with zero mean and variance σ^2 and under the Rayleigh fading process of h_k :

$$P(d_k^1|y_k) \propto \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} e^{-\frac{1}{2\sigma^2}|y_k - h_k s(d_k^1, \dots, d_k^4)|^2} \quad (8)$$

Now we have known that Formula (5) has given a way to get the MAP decoding results. Re-formula (5) in the new condition, we can get:

$$\gamma_i(m', m) = \frac{P\{c_i^1(m', m); c_i^2(m', m)\}P(y_k|\pi_i^1)P(y_k|\pi_i^2)}{P\{c_i^1(m', m); c_i^2(m', m)\}} P\{s_i = m|s_{i-1} = m'\} \quad (9)$$

Where C_i^j is d_k^j after interleaving.

In formula (7), $P(d_k^j)$ indicates the priority probability of d_k^j , if we consider 0 and 1 are transmitted in the same probability, we can consider $P(d_k^j = 0) = P(d_k^j = 1) = 0.5$.

But to gain a better performance of turbo code, we can rewrite (7) in new form:

$$P_{dem}(d_k^1|y_k) = \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} \frac{P\{y|s(d_k^1, d_k^2, d_k^3, d_k^4)\}}{P(y_k)} \gamma_{dec}(d_k^1) \gamma_{dec}(d_k^2) \gamma_{dec}(d_k^3) \gamma_{dec}(d_k^4) \quad (10)$$

Where $\gamma_{dec}(d_k^j)$ replaces $P(d_k^j)$ and $\gamma_{dec}(d_k^j)$ is the a priori probability of d_k^j . Now using the iteration between demodulator and decoder structure in figure 1, we can get the extrinsic probability $\gamma_{dec}(d_k^j)$ from the MAP decoder and send this extrinsic probability back to the demodulator as the a priori probability to improve the results. One more thing need be paid attention to is that (10) is optimal at gray mapping constellation, considering on hierarchical modulation, (10) should be re-formula as:

$$P_{dem}(d_k^1|y_k) = \sum_{d_k^2, d_k^3, d_k^4 \in \{0,1\}} \frac{P\{y|s(d_k^1, d_k^2, d_k^3, d_k^4)\}}{P(y_k)} \gamma_{dec}(d_k^2) \underbrace{\gamma_{dec}(d_k^3) \gamma_{dec}(d_k^4)}_{\text{unreliable}} \quad (11)$$

This formula is for Hp bits d_k^1 and d_k^2 , and for Lp bits d_k^3 and d_k^4 , it would be:

$$P_{dem}(d_k^3|y_k) = \sum_{d_k^1, d_k^2 \in \{0,1\}} \frac{P\{y|s(d_k^1, d_k^2, d_k^3, d_k^4)\}}{P(y_k)} \underbrace{\gamma_{dec}(d_k^1) \gamma_{dec}(d_k^2)}_{\text{reliable}} \gamma_{dec}(d_k^4) \quad (12)$$

$P_{dem}(d_k^1|y_k)$ and $P_{dem}(d_k^3|y_k)$ are derived from (11) and (12), the a priori probability of d_k^2 is same to that of d_k^1 , d_k^4 are same to d_k^3 . It can be depicted from (11), (12) that for the Hp bits, the extrinsic probability can be considered reliable so

the probability can be used at the Lp decoding, but on the contrary, the extrinsic probability of Lp bits is not reliable so be discard at the decoding process of Hp bits.

C. Simulation Results

The system simulation is done at the ITPP simulation platform, using Monte-Carlo simulation method. The generator polynomial of the turbo encoder is $(23, 35)_8$, constraint length is 5, interleaving depth is 1024, and the total package sent is 800, so there are 1024 bits in every package. The turbo iteration time is 8, and demodulation to decoding iteration time is 5, receiving symbols are passed through Rayleigh and AWGN channel. At first we consider on the BER performance of gray map constellation, Set Partitioning constellation and hierarchical modulation with $\alpha=1$ shown in figure 3. from the figure we can depict that the hierarchical modulation has the best performance in the three constellation at low SNR, though after number of iterations and at higher SNR the other two can get better performance than they are at lower SNR. This result can indicate select hierarchical modulation may achieve a better performance at low SNR for the Lp bits.

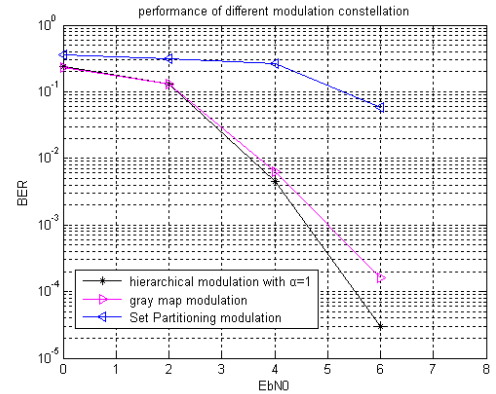


Figure.3 BER performance of different constellation mapping

Figure.4 shows the BER performance of different α hierarchical modulation. The other parameters are the same to the above simulation. From the figure we can depict that when increase α , the BER performance of Hp can get better but at the same time the BER of Lp is worse. We can choose a proper scheme according to the SNR in the center of a sector and the SNR at the margin of a sector.

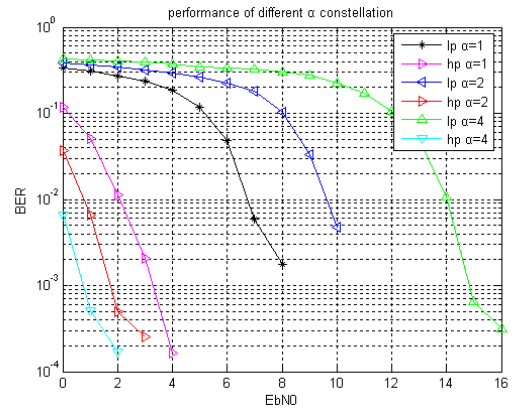


Fig. 4 BER of hierarchical modulation with different α

Figure 5 shows the BER performance of new iteration scheme depicted in this paper. The constellation is hierarchical with $\alpha=1$. The other parameters are the same to the above. From the curve we can see that when increase the iteration times between the demodulator and the decoder can get a better performance about 0.5 dB when iteration time is 4. But the performance do not increase highly when iteration time increase from 1 to 4. So we can find an optimal iteration scheme is to do 1 iteration between demodulator and decoder first, then do the turbo decoding iteration. This scheme is shown in table 1. If we use scheme 2 in the table to do the decode calculation we can save about 24 times iteration but get almost same results, if we want to get better BER we can use scheme 3 and 4, from the table we can depict these scheme do not increase the decoding algorithm more complexity, and can gain a better performance.

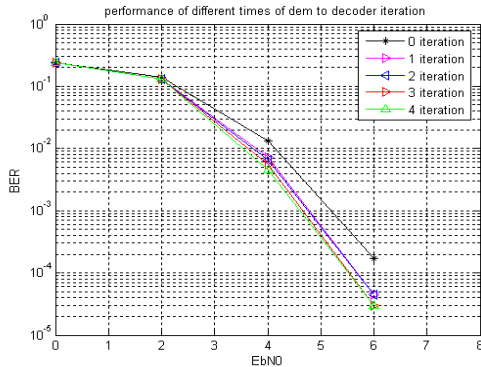


Figure.5 BER performance of different demodulation-decoder iterations

Table 1 iteration time scheme

| DI \ TI | Scheme1 | Scheme2 | Scheme3 | Scheme4 |
|-----------------|---------|---------|---------|---------|
| 0 | 40 | 8 | 8 | 8 |
| 1 | 0 | 8 | 8 | 8 |
| 2 | 0 | 0 | 8 | 8 |
| 3 | 0 | 0 | 0 | 8 |
| 4 | 0 | 0 | 0 | 0 |
| Total iteration | 40 | 16 | 24 | 32 |

(TI: turbo iteration ; DI: demodulation iteration)

III. CONCLUSIONS

In this paper, we discuss the new constellation mapping in the next generation communication scheme. And the new constellation can get better BER performance without adding complexity to the constellation as to the system. And according to the constellation style and system feature, there is a new iteration scheme brought forward. The new iteration scheme can make the system a 0.5dB better BER performance without adding complexity. So this scheme can be used in the same level of the current hardware to be realized.

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