



HAL
open science

Bumpless transfer for discrete-time switched systems

Ivan Mallocci, Laurentiu Hetel, Jamal Daafouz, Claude Iung, Rémi Bonidal

► **To cite this version:**

Ivan Mallocci, Laurentiu Hetel, Jamal Daafouz, Claude Iung, Rémi Bonidal. Bumpless transfer for discrete-time switched systems. American Control Conference, ACC 2009, Jun 2009, St Louis, Missouri, United States. pp.1078-1083, 10.1109/ACC.2009.5160127 . hal-00414113

HAL Id: hal-00414113

<https://hal.science/hal-00414113>

Submitted on 7 Sep 2009

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.



Bumpless transfer for discrete-time switched systems

I. MALLOCI, L. HETEL, J. DAAFOUZ, *Member, IEEE*, C. IUNG, *Member, IEEE*, and R. BONIDAL

Abstract—A bumpless transfer method for discrete-time switched linear systems is presented. It is based on an additional controller which is activated at the switching time for reducing the control discontinuities. Dwell time conditions to guarantee the stability of the closed-loop system are provided. Simulation tests on the Eisenhüttenstadt hot strip mill of ArcelorMittal are shown.

I. INTRODUCTION

In practical control problems, several linear controllers are often used to control the same non-linear plant, one for each operating point. This strategy avoids the non-linear control design, certainly more complicated. Nevertheless, switching among more controllers implies control discontinuities and undesired transient behaviors. Moreover, industrial systems are often characterized by saturations due to the actuators limits and do not accept these discontinuities. Consequently, the dynamics performance can be largely modified and the stability may not be guaranteed anymore. Slope saturations of the control signal are well-known for their destabilizing effect. The solution of this problem is called bumpless transfer.

A description of most popular strategies for the bumpless transfer problem can be found in [8], [9], [10] and [13]. One of the first bumpless transfer schemes is proposed by [11] for non-linear plants. The idea consists in pre-setting the off-line controller state in order to reduce the transient behavior at the switching time. In [18] and [19], results of [11] are generalized for controllers which are not bi-proper and have a minimum phase. A linear quadratic (LQ) optimization method is introduced to minimize the distance between the on-line and the off-line controller output. In [5], the discontinuity of the controller output is reduced resetting the fast dynamics of the controller at the switching time. In [21], the desired transient behavior is obtained using the \mathcal{L}_2 anti-windup structure [17].

Although the bumpless transfer problem has been widely studied in literature, only few articles address the switched systems framework. In [1], a bumpless transfer solution for continuous-time switched systems where the order of the controller is smaller than the order of the plant is given. The idea consists in forcing the output of the activated controller

to be equal to the plant input at the switching time. An analogous strategy is proposed in [7] for continuous-time LPV systems. However, as pointed out in [20] and [21], a constraint on the controller output does not guarantee better performances of the plant output.

In this article, a bumpless transfer control design for discrete-time switched systems is presented. We propose an additional controller which is activated at each switching time. The controller and the plant output are forced to follow a desired profile for a given period of time. This minimizes the control discontinuity guaranteeing the plant output performances. The solution is based on the LQ optimization theory, that has been introduced on the bumpless transfer framework by [19]. Stability of the closed-loop system is guaranteed by LMI conditions [2] using multiple Lyapunov functions [3] and the dwell time approach [15].

The article is organized as follows. In the next section, the problem is formulated. In section III, the optimization criterion to design the bumpless transfer controller (BT controller) is detailed. In section IV, stability conditions for the closed-loop system are investigated. In section V, simulation results of the Eisenhüttenstadt hot strip mill (Germany) are presented. Finally, a conclusion is given.

II. PROBLEM FORMULATION

Consider the discrete-time switched system

$$\begin{cases} x_{k+1} = A_{\sigma(k)}x_k + B_{\sigma(k)}u_k \\ y_k = C_{\sigma(k)}x_k \end{cases} \quad (1)$$

where $x \in \mathbb{R}^n$ is the state, $u \in \mathbb{R}^r$ is the control signal, $y \in \mathbb{R}^m$ is the output signal and $\sigma(k) : \mathbb{N} \rightarrow \Gamma = \{1, \dots, N\}$ is the switching signal, which is assumed to be unknown a priori but available in real time. The minimal interval of time between two switchings D_j is assumed to be known. Moreover, the pair (A_j, B_j) is supposed to be controllable and the pair (A_j, C_j) observable, $\forall j \in \Gamma$. Furthermore, the state x is supposed to be available for feedback and the plant matrices (1) are assumed to be well-known. A state-feedback control law

$$u_k = K_{\sigma(k)}x_k \quad (2)$$

which stabilizes the closed-loop system (1)-(2) is given. In order to reduce the amplitude of the control signal discontinuities, different strategies are possible. Here, a BT controller which is activated at the switching time for the

This work has been supported by grants from "la région Lorraine, France" and ArcelorMittal Maizières Research.

I. Mallocci, L. Hetel, J. Daafouz and C. Iung are with the Centre de Recherche en Automatique de Nancy, UMR 7039 CNRS - Nancy Université, ENSEM, 2, Avenue de la forêt de Haye 54516 Vandoeuvre-lès-Nancy, France. Corresponding author: Jamal.Daafouz@ensem.inpl-nancy.fr.

R. Bonidal is with ArcelorMittal Maizières, R&D Industrial Operations, BP 30320, F-57283 Maizières-lès-Metz Cedex, France.

period of time $\tau_j^M < D_j$ is proposed, that is:

$$u_k = \begin{cases} K_j x_k + u_k^{bt} & \text{if } \tau_j \leq \tau_j^M \\ K_j x_k & \text{if } \tau_j > \tau_j^M \end{cases} \quad (3)$$

where $u_k^{bt} \in \mathbb{R}^r$ is the BT controller output and $\tau_j \in \Xi = \{1, \dots, \tau_j^M\}$ the number of times between the present time k and the last switching. For each mode $j \in \Gamma$, the closed-loop system (Fig. 1) can be written as

$$\begin{cases} x_{k+1} = (A_j + B_j K_j) x_k + B_j u_k^{bt} \\ y_k = C_j x_k. \end{cases} \quad (4)$$

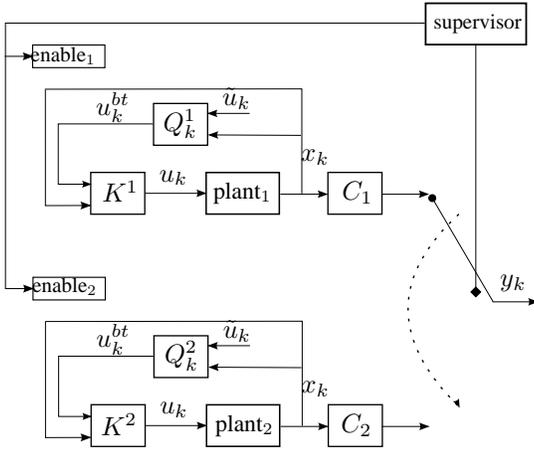


Fig. 1. Closed-loop system with $\Gamma = \{1, 2\}$

The BT controller Q^j is designed in the next section. The signal $u_k - \tilde{u}_k^j$ is minimized using a LQ criterion, where \tilde{u}^j represents the desired profile of the control signal. For simplicity reasons, a straight line is chosen as desired profile. Let t_j be the switching time from the subsystem i to the subsystem j , $\forall (i, j) \in \Gamma \times \Gamma$. We can define

$$\tilde{u}_{\tau_j}^j = \tilde{u}_j^0 + p_j \tau_j \quad (5)$$

where

$$\tilde{u}_j^0 = K_i x_{t_j-1} \quad (6)$$

is the control signal value at the time before the switching and p_j determines the slope of the desired profile, i. e.

$$p_j = \frac{1}{\tau_j^M} (K_j x_{t_j} - K_i x_{t_j-1}). \quad (7)$$

We obtain a value of p which depends on the control signal discontinuity (Fig. 2). In order to guarantee the performances of the plant output y , also the difference between y and the desired plant output \tilde{y} is minimized.

III. BUMPLESS TRANSFER CONTROLLER DESIGN

In this section, the method to design the BT controller is presented. In order to follow the desired profiles \tilde{u} and \tilde{y} , the problem is formulated as a classical LQ optimization problem. In the bumpless transfer framework, this solution

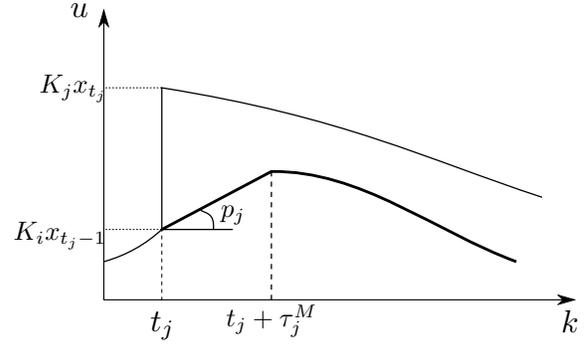


Fig. 2. u evolution with BT controller switched on (thick line) and switched off (thin line)

has been proposed by [19], where the difference between the on-line and the off-line controller output is minimized before each switching. Moreover, the difference between the on-line and the off-line controller input is minimized. This strategy initializes the controller state and then reduces the transient behavior on the plant output. Since the anti-bumpless action consists in pre-setting the state of the off-line controller before the switching, the method does not address control systems without memory, such as state-feedback control laws. Another problem concerns the stability of the closed-loop system, which is not guaranteed when arbitrary switchings occur. To solve these problems, for discrete-time switched systems we propose a BT controller which is activated at each switching time. Stability conditions for the closed-loop system are given in the next section. For each mode $j \in \Gamma$, the design of the BT controller is based on the minimization of the following quadratic cost function:

$$J_j = \phi_{T_j^j}^j + \frac{1}{2} \sum_{k=0}^{T_j^j-1} [z_k^{u'} W_j^u z_k^u + z_k^{y'} W_j^y z_k^y], \quad (8)$$

with

$$z_k^u = u_k - \tilde{u}_k^j \quad (9)$$

$$z_k^y = y_k - \tilde{y}_k^j \quad (10)$$

$$\phi_{T_j^j}^j = \frac{1}{2} z_{T_j^j}^{u'} P^j z_{T_j^j}^u \quad (11)$$

where W_j^u and W_j^y are positive definite weighting matrices of appropriated dimensions. $T_j^j = \tau_j^M + 1$ is the terminal time and P^j is a positive semi-definite terminal weighting matrix. For simplicity reasons, we consider $\tilde{y}^j = 0$. The next theorem shows how to compute the signal u^{bt} . The proof, which is based on the Pontryagin's minimum principle [4], is similar to the one given in [19].

Theorem 1: Given the system (4), the quadratic cost function (8) and the terminal time T_j^j , the BT controller which minimizes the signals z^u and z^y is given by

$$u_k^{bt} = Q_k^j \begin{bmatrix} x_k \\ \tilde{u}_k^j \\ g_{k+1}^j \end{bmatrix} \quad (12)$$

with

$$Q_k^j = \begin{bmatrix} (\tilde{N}_{k+1}^j \Pi_{k+1}^j A_j - K_j)' \\ (I + \tilde{N}_{k+1}^j \Pi_{k+1}^j B_j)' \\ -\tilde{N}_{k+1}^{j'} \end{bmatrix}' \quad (13)$$

and

$$\tilde{N}_{k+1}^j = -(W_j^u)^{-1} B_j' (I - \Pi_{k+1}^j \tilde{B}_j)^{-1},$$

$\forall j \in \Gamma$. The values of Π^j and g^j are given by the equations

$$\Pi_k^j = A_j' (I - \Pi_{k+1}^j \tilde{B}_j)^{-1} \Pi_{k+1}^j A_j + \tilde{C}_j \quad (14)$$

and

$$g_k^j = A_j' (I - \Pi_{k+1}^j \tilde{B}_j)^{-1} (g_{k+1}^j - \Pi_{k+1}^j B_j \tilde{u}_k^j) \quad (15)$$

with

$$\begin{aligned} \tilde{B}_j &= -B_j (W_j^u)^{-1} B_j' \\ \tilde{C}_j &= C_j' W_j^y C_j. \end{aligned}$$

The bound condition is

$$\begin{aligned} \Pi_{T_f^j}^j &= 0 \\ g_{T_f^j}^j &= 0. \end{aligned} \quad (16)$$

Remark 1: In the finite horizon approach, the knowledge of all the future values of \tilde{u} is required in order to solve (15) backward in time. Then, in general this method cannot be applied to solve practical problems, as discussed in [19]. Nevertheless, from (5), in our case all the values of \tilde{u} can be computed in the finite horizon Ξ . Only the knowledge of x_{t_j-1} and x_{t_j} is needed. Since these informations are available at each switching time, the method can always be used.

IV. STABILITY ANALYSIS

In the previous sections, we assumed that the BT controller is switched on for τ_j^M times. As the original controller (2) has been designed without taking into account this fact, the stability is not guaranteed anymore. Then, in this section, a stability condition for the closed-loop system (4) is given. For each mode $j \in \Gamma$, the closed-loop system

$$x_{k+1} = (A_j + B_j K_j) x_k + B_j u_k^{bt} \quad (17)$$

can be written in the equivalent form

$$v_{k+1} = Y_j(\tau_j) v_k \quad (18)$$

where

$$v_k = \begin{bmatrix} x_k \\ x_{k-1} \\ \tilde{u}_k^0 \\ p_k \end{bmatrix}$$

is the augmented state and the signals \tilde{u}^0 and p are defined in section II. The representation of a switched system with an augmented state approach is justified in [12]. We distinguish two phases on the interval between two switchings:

– *The bumpless transfer phase:* the BT controller is on. We find

$$Y_j(\tau_j)|_{\tau_j=1} = \begin{bmatrix} \bar{H}_{\tau_j+1}^j & \bar{L}_{\tau_j+1}^j & 0 & 0 \\ I & 0 & 0 & 0 \\ 0 & K_i & 0 & 0 \\ \frac{K_j}{\tau_j^M} & -\frac{K_j}{\tau_j^M} & 0 & 0 \end{bmatrix}$$

and

$$Y_j(\tau_j)|_{2 \leq \tau_j \leq \tau_j^M} = \begin{bmatrix} \bar{A}_{\tau_j+1}^j & 0 & \bar{U}_{\tau_j+1}^j & \bar{P}_{\tau_j+1}^j \\ I & 0 & 0 & 0 \\ 0 & 0 & I & 0 \\ 0 & 0 & 0 & I \end{bmatrix}.$$

In this case, the stability of (18) is not guaranteed. The construction of Y_j is detailed in the appendix I.

– *The recuperation phase:* the BT controller is off. We have

$$Y_j(\tau_j)|_{\tau_j > \tau_j^M} = Y_j^s = \begin{bmatrix} A_j + B_j K_j & 0 & \cdots & 0 \\ I & 0 & \cdots & 0 \\ 0 & \cdots & \cdots & 0 \\ \vdots & & & \vdots \\ 0 & \cdots & \cdots & 0 \end{bmatrix},$$

where Y_j^s is Schur and constant $\forall j \in \Gamma$.

Given the minimal interval of time between two switchings D_j , the value of τ_j^M can always be reduced in order to make the system (18) stable. Let define the transition matrix between two switching times

$$Z_j(D_j, \tau_j^M) = \prod_{\tau_j=1}^{\tau_j^M} Y_j(\tau_j)|_{\tau_j \leq \tau_j^M} Y_j^s (D_j - \tau_j^M - 1). \quad (19)$$

The following theorem checks the closed-loop system stability.

Theorem 2: Given D_j , if there exist positive definite matrices $P_{Y_j} = P_{Y_j}'$, $P_{Z_j} = P_{Z_j}'$ of appropriate dimensions and scalars τ_j^M such that the LMIs

$$Y_j^s P_{Y_j} Y_j^s - P_{Y_j} \prec 0 \quad (20)$$

$$Z_j' P_{Z_j} Z_j - P_{Z_j} \prec 0 \quad (21)$$

$$Z_j' P_{Y_j} Z_j - P_{Z_j} \prec 0 \quad (22)$$

$$Y_j^s P_{Z_j} Y_j^s - P_{Y_j} \prec 0 \quad (23)$$

are verified $\forall (i, j) \in \Gamma \times \Gamma$, then the closed-loop system (18) is asymptotically stable.

We give an idea of the proof. In the case of arbitrary switching law, a necessary condition for the asymptotic stability of the closed-loop system (18) is that each subsystem is stable. Since for assumption Y^s is Schur, from (19) there exists a value of τ_j^M such that also the subsystem Z_j is Schur, $\forall j \in \Gamma$. In Fig. 3, the system evolution is shown for $\Gamma = \{1, 2\}$. For the closed-loop system stability,

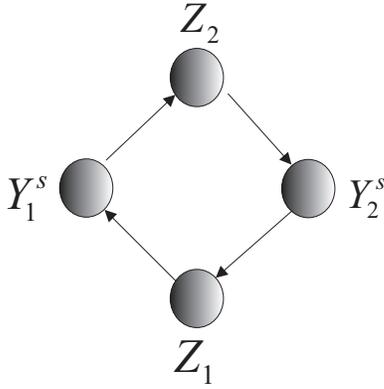


Fig. 3. System evolution

TABLE I
BT CONTROLLER DATA

j	2	3	4
D_j	64	38	30
τ_j^M	30	15	6
W_j^u	I	$100I$	$10I$
W_j^y	I	I	I

we also must check that switching among the subsystems leads to a stable behavior. This is the meaning of the LMIs (22) and (23). Thus, the conditions of Theorem 2 guarantee a decreasing trajectory after switching. This is equivalent to say that the closed-loop system (18) is stable [16].

V. SIMULATION RESULTS

In this section, the strategy proposed in the previous sections is applied to the Eisenhüttenstadt hot strip mill (HSM) of ArcelorMittal. The rolling process consists in crushing a metal strip between two rolls in inverse rotation for obtaining a strip with constant and desired thickness. A HSM is the association of several stands in a line, where each stand is constituted by a set of rolls. The lateral movement of the strip, with reference to the mill axis, may induce a decrease of the product quality and rolls damage. Then, in order to improve the reliability and the process quality, this displacement must be reduced [6]. At the end of the treatment, the strip leaves the stands one after the others. Each time the strip leaves a stand, the system dynamics changes. The HSM is modeled as a switched system with four subsystems and three switchings [14]. For each subsystem $j \in \Gamma$, a different discrete-time state-feedback control gain K_j has been designed. Given the weighting matrices W^u , W^y and the minimal dwell time D_j for a product of the HSM database, the choice of the τ_j^M values summarized in Table I allow to find a solution for the LMI conditions of Theorem 2. This guarantees the stability of the closed-loop system. I denotes an identity matrix of appropriate dimension. Since the system never switches back to the first

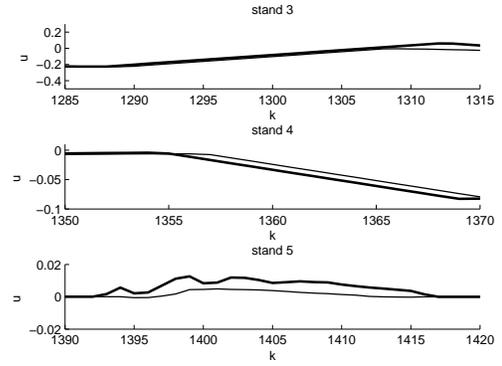


Fig. 4. u

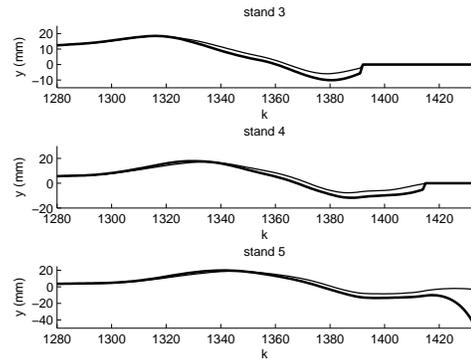


Fig. 5. y

subsystem, no BT controller is designed for $j = 1$.

The signal u^{bt} is computed applying Theorem 1. Equations (13) and (14) can be solved off-line. At the opposite, to compute the equation (15) we need to know the x_{t_j-1} value, where t_j is the switching time to the subsystem j . Then, it can be computed only on-line, at the switching time. Since the output signal y corresponds to the displacement that has to be minimized, we choose $\tilde{y} = 0$. Each stand is controlled by a different control signal. For the simulated product, switchings occur at the instants $k = 1290$, $k = 1354$ and $k = 1392$. In Fig. 4 it is shown the controller output u for the last three stands. For each stand, we propose a zoom of the zone corresponding to the biggest bumps on u . The bold line shows the u evolution without BT control whereas the thinnest line shows the u evolution when the BT controller is on. The evolution of the strip displacement y is shown in Fig. 5. As expected, performances are better when the BT controller is on (thinnest line). In particular, the strip displacement in the exit of the system (*stand 5* in Fig. 5) is reduced from 40 to 8 mm.

VI. CONCLUSION

In this article, a bumpless transfer method for discrete-time switched systems has been proposed. The BT controller has been designed using a LQ optimization method. The idea consists in forcing the controller output and the plant

output to follow a desired profile. This allows to avoid the control signal discontinuities and to improve the system performances. A LMI criterion guaranteeing the stability of the closed-loop system is proposed. Simulation tests on the Eisenhüttenstadt HSM of ArcelorMittal are provided.

APPENDIX I CONSTRUCTION OF Y_j

Consider the switching from the subsystem i to the subsystem j . From Theorem 1, when the BT controller is on

$$u_k^{bt} = Q_j^j \begin{bmatrix} x_k \\ \tilde{u}_{\tau_j}^j \\ g_{\tau_j+1}^j \end{bmatrix} \quad (24)$$

where

$$\tilde{u}_{\tau_j}^j = \tilde{u}_j^0 + p_j \tau_j \quad (25)$$

with

$$\tilde{u}_j^0 = K_i x_{t_j-1} \quad (26)$$

and

$$p_j = \frac{1}{\tau_j^M} (K_j x_{t_j} - K_i x_{t_j-1}). \quad (27)$$

The evolution of the signal g^j in (24) is given by (15), which can be rewritten as

$$g_{\tau_j}^j = G_{\tau_j}^{j,u} \tilde{u}_j^0 + G_{\tau_j}^{j,p} p_j \quad (28)$$

where

$$G_{\tau_j}^{j,u} = - \sum_{\eta=0}^{\tau_j^M - \tau_j} \left(\prod_{\zeta=\tau_j+1}^{\tau_j^M+1-\eta} M_{\zeta}^j \right) \Pi_{\tau_j+1-\eta}^j B^j,$$

$$G_{\tau_j}^{j,p} = - \sum_{\eta=0}^{\tau_j^M - \tau_j} \left(\prod_{\zeta=\tau_j+1}^{\tau_j^M+1-\eta} M_{\zeta}^j \right) \Pi_{\tau_j+1-\eta}^j B^j (\tau^M - \eta)$$

and

$$M_{\tau_j}^j = A_j' (I - \Pi_{\tau_j}^j \tilde{B}_j)^{-1}.$$

The closed form (28) allows to express (15) as a function of \tilde{u}_j^0 and p_j . Then, the system (17) becomes

$$x_{k+1} = \bar{A}_{\tau_j+1}^j x_k + (\bar{B}_{\tau_j+1}^j - B_j \tilde{N}_{\tau_j+1}^j G_{\tau_j+1}^{j,u}) \tilde{u}_j^0 + (\tau_j \bar{B}_{\tau_j+1}^j - B_j \tilde{N}_{\tau_j+1}^j G_{\tau_j+1}^{j,p}) p_j$$

with

$$\bar{A}_{\tau_j}^j = (I + B_j \tilde{N}_{\tau_j}^j \Pi_{\tau_j}^j) A_j$$

and

$$\bar{B}_{\tau_j}^j = B_j (I + \tilde{N}_{\tau_j}^j \Pi_{\tau_j}^j B_j).$$

When $\tau_j = 1$, $t_j = k$. Then \tilde{u}_j^0 and p_j can be initialized as function of x_k and x_{k-1} . Using (26) and (27), we find

$$Y_j(\tau_j)|_{\tau_j=1} = \begin{bmatrix} \bar{H}_{\tau_j+1}^j & \bar{L}_{\tau_j+1}^j & 0 & 0 \\ I & 0 & 0 & 0 \\ 0 & K_i & 0 & 0 \\ \frac{K_j}{\tau_j^M} & -\frac{K_i}{\tau_j^M} & 0 & 0 \end{bmatrix}$$

with

$$\bar{H}_{\tau_j}^j = \bar{A}_{\tau_j}^j + \frac{1}{\tau_j^M} (\bar{B}_{\tau_j}^j - B_j \tilde{N}_{\tau_j}^j G_{\tau_j}^{j,p}) K_j$$

and

$$\bar{L}_{\tau_j}^j = (\bar{B}_{\tau_j}^j - B_j \tilde{N}_{\tau_j}^j G_{\tau_j}^{j,u}) K_i - \frac{1}{\tau_j^M} (\bar{B}_{\tau_j}^j - B_j \tilde{N}_{\tau_j}^j G_{\tau_j}^{j,p}) K_i.$$

When $2 \leq \tau_j \leq \tau_j^M$, \tilde{u}_j^0 and p_j remains constant. We have

$$Y_j(\tau_j)|_{2 \leq \tau_j \leq \tau_j^M} = \begin{bmatrix} \bar{A}_{\tau_j+1}^j & 0 & \bar{U}_{\tau_j+1}^j & \bar{P}_{\tau_j+1}^j \\ I & 0 & 0 & 0 \\ 0 & 0 & I & 0 \\ 0 & 0 & 0 & I \end{bmatrix}$$

with

$$\bar{U}_{\tau_j}^j = \bar{B}_{\tau_j}^j - B_j \tilde{N}_{\tau_j}^j G_{\tau_j}^{j,u}$$

and

$$\bar{P}_{\tau_j}^j = \tau_j \bar{B}_{\tau_j}^j - B_j \tilde{N}_{\tau_j}^j G_{\tau_j}^{j,p}.$$

When the BT controller is off $u_k^{bt} = 0$, then

$$Y_j(\tau_j)|_{\tau_j > \tau_j^M} = Y_j^s = \begin{bmatrix} A_j + B_j K_j & 0 & \cdots & 0 \\ I & 0 & \cdots & 0 \\ 0 & \cdots & & 0 \\ \vdots & & & \vdots \\ 0 & \cdots & & 0 \end{bmatrix}$$

is constant $\forall j \in \Gamma$.

REFERENCES

- [1] A.B. Arehart and W.A. Wolovich. Bumpless switching controllers. In *Conference on Decision and Control*, 1996.
- [2] S. Boyd, L.E. Ghaoui, E. Feron, and V. Balakrishnan. *Linear Matrix Inequalities in system and control theory*. Society for Industrial and Applied Mathematics, 1994.
- [3] S. Branicky. Multiple Lyapunov function and other analysis tools for switched and hybrid systems. *IEEE Transactions on Automatic Control*, 43(4):475–482, 1998.
- [4] A. E. Bryson and Y. C. Ho. *Applied optimal control*. John Wiley, 1975.
- [5] S.Y. Cheong and M.G. Safonov. Bumpless transfer for adaptive switching controls. In *IFAC World Congress*, 2008.
- [6] J. Daafouz, R. Bonidal, C. Iung, P. Szczepanski, N. Naumann, and U. Koschack. New steering control at EKO Stahl finishing mill. In *The Iron & Steel Technology Conference and Exposition*, 2008.
- [7] K. Dong and F. Wu. Online switching control design of LFT systems. In *Conference on Decision and Control*, 2006.
- [8] C. Edwards and I. Postlethwaite. Anti-windup bumpless transfer schemes. *Automatica*, 34(2):199–210, 1998.
- [9] S.F. Graebe and A. Ahlen. *The control handbook*. CRC press, 1996.
- [10] R. Hanus. Anti-windup and bumpless transfer: A survey. In *12th IMACS world congress*, 1988.
- [11] R. Hanus, M. Kinnaert, and J. Henrotte. Conditioning technique, a general anti-windup and bumpless transfer method. *Automatica*, 23(6):729–739, 1987.
- [12] L. Hetel, J. Daafouz, and C. Iung. Equivalence between the Lyapunov-Krasovskii functional approach for discrete delay systems and the stability conditions for switched systems. In *IFAC workshop on Time Delay Systems*, 2007.
- [13] M.V. Kothare, P.J. Campo, M. Morari, and N. Nett. A unified framework for study of anti-windup designs. *Automatica*, 30(12):1869–1883, 1994.
- [14] I. Mallocci, J. Daafouz, C. Iung, R. Bonidal, and P. Szczepanski. Switched system modeling and robust steering control of the tail end phase in a hot strip mill. *To appear in Nonlinear Analysis: Hybrid Systems and Applications*, 2009.

- [15] A.S. Morse. Supervisory control of families of linear set-point controllers - part 1: Exact matching. *IEEE Transactions on Automatic Control*, 41:1413–1431, 1996.
- [16] P. Peleties and R.A. DeCarlo. Asymptotic stability of m -switched systems using Lyapunov-like functions. In *American Control Conference*, 1991.
- [17] A. R. Teel and N. Kapoor. The \mathcal{L}_2 anti-windup problem: Its definition and solution. In *European Control Conference*, 1997.
- [18] M.C. Turner and D.J. Walker. Modified linear quadratic bumpless transfer. In *American Control Conference*, 1999.
- [19] M.C. Turner and D.J. Walker. Linear quadratic bumpless transfer. *Automatica*, 36:1089–1101, 2000.
- [20] L. Zaccarian and A. R. Teel. A common framework for anti-windup, bumpless transfer and reliable designs. *Automatica*, 38(10):1735–1744, 2002.
- [21] L. Zaccarian and A. R. Teel. The $\mathcal{L}_2(l_2)$ bumpless transfer problem for linear plants: Its definition and solution. *Automatica*, 41(7):1273–1280, 2005.