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Compiling Functional Types to Relational Specifications for Low Level Imperative Code

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Abstract
We describe a semantic type soundness result, formalized in the Coq proof assistant, for a compiler from a simple functional language into an idealized assembly language. Types in the high-level language are interpreted as binary relations, built using both second-order quantification and separation, over stores and values in the low-level machine.

Categories and Subject Descriptors F.3.1 [Logics and meanings of programs]: Specifying and Verifying and Reasoning about Programs—Mechanical verification, Specification techniques; F.3.3 [Logics and meanings of programs]: Studies of Program Constructs—Type structure; D.3.4 [Programming Languages]: Processors—Compilers; D.2.4 [Software Engineering]: Software / Program Verification—Correctness proofs, formal methods

General Terms Languages, theory

Keywords Compiler verification, type soundness, relational parametericity, separation logic, proof assistants

1. Introduction
What kinds of correctness properties do we wish to establish of our compilers? The most obvious answer is that when a high-level source program is compiled to produce a low-level target, the behaviour of the target agrees with a high-level semantics of the source. But we usually also want to be sure that the target code satisfies certain safety or liveness properties, ensuring that ‘bad things’ don’t happen, or that ‘good’ ones do. Such properties include memory safety, adherence to information flow policies, termination, and various forms of resource boundedness. For applications in language-based security, the good news is that (fancy) type-like properties of this kind, which at least seem less complex to state and check than full functional correctness, are the important ones. On the other hand, for such applications one would really like to certify the code that actually runs, which involves formalizing and verifying type-like properties of machine code. How to do that is the problem we address here.

The approach is essentially that of our earlier work on specifying and verifying memory managers (Benton 2006) and type preservation for a simple imperative language (Benton and Zarfaty 2007), and we will not repeat all the methodological arguments here. Briefly, we want to find low-level specifications that should be satisfied by target code compiled from source phrases of particular high-level types. Ideally, these specifications should be both modular and expressed without reference to concepts that are specific to the particular high-level language. This is important: we are not just interested in proving properties of complete, closed programs. We want (and have) to prove things about the result of linking or replacing bits of compiled code with code from elsewhere, including both the runtime system (which may be handcrafted machine code) or code compiled from other high-level languages. For maximum flexibility and strong guarantees, we would like these specifications to be semantic, i.e. defined extensionally in terms of the observable behaviour of programs rather than in terms of a purely syntactic type system for low-level code. One way of characterizing our goal is that we would like to know just what contract should be satisfied by a C or assembly language function that is intended to behave as an ML value of some (possibly higher order) type.

In previous work, we have proposed that these goals can be achieved by giving a semantics for high-level types as relational specifications over low-level code, and we have shown how this works out in the case of a very simple imperative language. The main contribution of the present paper is to show how such a low-level relational interpretation of types can be extended to a language with higher-order functions. The definitions and results presented here have been formalized in the Coq proof assistant. As we will explain, both our general low-level reasoning framework and its encoding in Coq have been improved relative to our earlier work. The Coq development is available from the authors’ home pages.

2. Source and Target Languages
2.1 The Low Level Target
The idealized low level machine code into which we compile is the same as in our previous work. There is a single datatype, the natural numbers, though different instructions treat elements of that type as code pointers, heap addresses, integers, etc. The store is a total function from naturals to naturals and the code heap is a total function from naturals to instructions (immutable and distinct from the data heap). Computed branches and address arithmetic are perfectly allowable. There is no built-in notion of allocation and no notion of stuckness or ‘going wrong’: the only observable behaviours are termination and divergence. There are no registers; we simply adopt a programming convention of using the first few memory locations in a register-like fashion.

The Coq specification of our machine involves an inductive type of instructions, including halting, direct and indirect loads, stores and jumps, arithmetic and tests. Details can be found in the.
proof scripts or our earlier paper (Benton and Zarfaty 2007); here we just use an obvious pseudocode in which, for example

\[ 100: \text{[5]} \leftarrow \text{[6]} + 1; \]

means that the instruction at code address 100 reads the contents of the memory location following that pointed to by location 6, and stores the result in location 5. The mutable state of our machine is specified by

Definition state := nat → nat.
Definition program := nat → instruction.
and there is then one a one-step transition function

Definition sem_instr (ins:instruction) (s:state) (pc:state) : option (state * nat) := ...

mapping an instruction, state and program counter to either a new state and program counter, or None in the case that the instruction is a halt. A configuration is thus a triple of a program, a state and a program counter. All our notions of behaviour arise from the primitive notion of termination:

Fixpoint kstepterm (k:nat) (p:program) (s:state) (l:nat) {struct k} : Prop :=
  match k with
  | 0 ⇒ False
  | (S j) ⇒
    match sem_instr p l s 1 with
    | None ⇒ True
    | Some (s', l') ⇒ kstepterm j p s' l'
end.

Definition terminates p s l :=
  \exists k, kstepterm k p s l.

So a configuration terminates if there is some natural number \( k \) such that it terminates within \( k \) steps.

2.2 The High Level Source Language

Our source is a fairly conventional simply-typed, call-by-value functional language with recursion. The only mild novelty is that we have included a rudimentary form of refinement typing on the functional language with recursion. The only mild novelty is that the instruction at code address 100 reads the contents of the memory location following that pointed to by location 6, and stores the result in location 5. The mutable state of our machine is specified by

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    | Some (s', l') ⇒ kstepterm j p s' l'
end.

Definition terminates p s l :=
  \exists k, kstepterm k p s l.

So a configuration terminates if there is some natural number \( k \) such that it terminates within \( k \) steps.

3. The Compiler

The compiler is a structurally-inductive Coq function that takes as input an untyped source expression \( e \) and produces two pieces of low-level code: an auxiliary section, containing the code for the bodies of functions occurring in \( e \), and a main section, which actually computes the value of \( e \). In more detail:

Fixpoint compile (e : Exp)
  (aux_code: list instruction)
  (aux_next : nat)
  (alloc dealloc: nat) {struct e}
  : ( (nat -> list instruction * nat * nat)
    * list instruction * nat) := ...

Here aux_code is a list of already-generated auxiliary instructions, aux_next is the code address from which further auxiliary instructions should be produced, and alloc and dealloc are the entry points of the allocation and deallocation routines with which the compiled code will eventually be linked. The second and third components of the return value are the extended auxiliary code and updated aux_next. The first component of the return value is a function that takes as input a start address for the main code and produces a triple comprising the instructions of the main code, the amount of stack space required by those instructions and the (slightly unnecessary) next free main code address. The generation of the main code is delayed in this way because we do not initially know how large the auxiliary code will be, and hence where we may produce the main code.

When compiled code is running, the memory is broadly divided into a number of regions:

- The low-numbered locations 0-9, used in a register like fashion for passing arguments and returning results by compiled functions and by the allocator routines, as workspace and as special registers (stack pointer etc.).
- The private storage of the memory allocator, comprising free memory and whatever private datastructures the allocator module uses to keep track of what is free.
- The language heap, storing allocated pairs, closures and environments. We sometimes call this the ‘cloud’.
- A linked list of allocation records comprising the call stack, each of which includes a local stack for expression evaluation.

The important pseudo-registers are
- arg and ret, which are used in the call/return convention of the memory allocator.
In the figure, the zeroth environment entry (the argument to the current function call) is a heap-allocated pair, the entry at position one is a recursive closure and that at position two is the same pair as at position zero.

**Pairs.** To construct a pair, we assume that elements of the pair have already been constructed on the top of the stack (in the ‘wrong’ order). We then need to call the allocator. We load the register arg with the size of the required block (here 2) put the return address lab+4 into ret and jump to the allocation routine at address alloc. On return to lab+4, ret will point to a fresh block of two cells. We then (in a slightly optimized way) pop the top two elements from the stack, store them in the new block and push the address of that block:

\[
\text{lab: } [\text{lab}] \leftarrow [\text{sp}] - 1 \\
\text{[arg]} \leftarrow 2 \\
\text{[ret]} \leftarrow \text{lab+4} \\
\text{jmp alloc}
\]

\[
\text{lab+4: } [[\text{ret}]] \leftarrow [[\text{sp}]] \quad \text{// store snd} \\
[[\text{ret}]+1] \leftarrow [[\text{sp}]+1] \quad \text{// store fst} \\
[[\text{sp}]] \leftarrow [\text{ret}] \quad \text{// push pair}
\]

In the figure, the argument to the current function is the pair \((\text{true,0,\text{false}})\).

**Closures** To compile a lambda abstraction \(\lambda x.M\) we compile, in the auxiliary code, the code for the body \(M\), preceded by a header that allocates space for a new activation record and does bits of callee-saving, and followed by an epilogue that restores caller context and returns a value. Then in the main code, we build and push a closure value that pairs the current environment with a pointer to the wrapped body code.

The function header expects an argument and a pointer to the closure being called to be on the top of the caller’s stack. It allocates stack_size+5 words for the new activation record, grabs the stored environment from the closure object and stores it at offset 1 in the new activation record. The argument is then popped from the caller’s stack and stored at offset zero in the new activation record. The caller’s environment is stored at offset 2, and his stack pointer at offset 3. We then set the environment pointer up for the body of the function by setting it to the base of the new activation record and set the stack pointer to offset 4 within the record. Note how the first element of the new environment list (the argument and the pointer to the rest of the environment) is contiguous with the remainder of the activation record. Here is the code:

\[
\text{lab: } [\text{arg}] \leftarrow \text{stack_size + 5 allocate frame} \\
[\text{ret}] \leftarrow \text{lab + 3} \\
\text{jmp alloc} \\
\text{lab+3: } [\text{wk}] \leftarrow [[\text{sp}]] \quad \text{take closure ptr}
\]
The saved environment and stack pointer can be seen in the figure as the two left-pointing arrows from the current frame to the parent frame. The head cell of the current environment is the darker pair of cells at the base of the current frame.

The function epilogue has the job of restoring the caller’s environment and stack pointer, putting the value computed by the function on the caller’s stack, deallocating the callee’s frame and returning to the caller’s code. The interesting points here are that we do not deallocate the first two elements of the frame that we originally allocated, as they are part of the environment and may by now be shared by other closures, and that the return address is stored at offset 4 in the caller’s frame:

```
lab:  [wk] ← [sp]  save return value
       [sp] ← [env]+3  restore old sp
       [sp] ← [wk]  push return value
       [wk] ← [env] +2  base to free

[env] ← [env]+2  restore old frame
[env] ← [env]+2  size to free
[ret] ← lab+8
jmp dealloc
lab+8:  jmp [env]+4  return to caller
```

In the figure, one can see the return address to which we will jump when returning from the current call at offset 4 in the parent frame, pointing into the program.

The code to actually build a closure allocates a new pair, fills in the first field with the code pointer to the appropriate function header and the second with the current environment, and pushes the address of the pair onto the stack.

**Application.** The code produced for an application assumes that there is an argument and a pointer to a closure on the stack. It stores a return address in the current frame and then branches to the code pointer part of the closure:

```
lab:  [[env]+4] ← lab+3
       [wk] ← [sp]
       jmp [sp]
lab+3:  ...
```

**Recursive closures.** The compilation scheme for recursive function $\text{Fix}(x) = M$ relies on that non-recursive functions. We first...
extend the environment with an unfilled hole using the following code:

\[
\begin{align*}
\text{lab:} & \quad [\text{arg}] \leftarrow 2 \\
\text{[ret]} & \leftarrow \text{lab+3} \\
\text{jmp alloc} & \\
\text{lab+3:} & \quad [[\text{ret}]+1] \leftarrow [\text{env}] \\
\text{[env]} & \leftarrow [[\text{env}]+1] \quad / / \text{restore}
\end{align*}
\]

We then compile code to build the closure for \(\lambda x.M\) in this environment, just as above. After that, we tie the recursive knot by overwriting the hole with the address of the constructed closure and finally restore the original environment:

\[
[[\text{env}]] \leftarrow [[\text{sp}]] \quad // \text{knot} \\
\text{[env]} \leftarrow [[\text{env}]+1] \quad // \text{restore}
\]

In the figure, the value on the top of the stack (and at position 1 in the environment) is a recursive closure. Observe that the environment part of the closure, the second component, points to the two element environment drawn within the cloud, and that the zeroth value in that environment is the closure itself.

4. Low-level Relations

As we said in the introduction, we specify low-level code in terms of binary relations, rather than the unary predicates that are more common in Hoare-style program logics. There are three main types of relation with which we work: over states, over natural numbers and over programs.

In defining relations over states, we will make much use of a form of separating conjunction, \(\otimes\). Previously we worked with relations equipped with accessibility maps (Benton and Leperechey 2005), specifying the (state-varying) part of the state about which a particular relation cares (its support), and used disjointness of supports to define the separating conjunction. However, supported relations do not admit general notions of disjunction or existential quantification, which made some definitions in our previous work rather complex: we had to define combinators for special shapes of existentially quantified formula in which the satisfying witness was always uniquely determined.

Furthermore, every inductive relation required separate inductive definitions for the relation and for its support and an inductive proof that the one supported the other, which was rather painful. In the current work, we have moved from supported relations over total states to working with relations over partial states (which is just the way everybody else working in separation logic does things). Our original motivation for eschewing partial states was that we did not wish to introduce any fictional notion of ‘going wrong’ into the operational semantics. We still manage to achieve that, however, by defining the operational semantics over total states (a special case of partial ones) and restricting attention to relations that are preserved by extension of partial states.

There is also an additive conjunction of relations, \(\times\), which plays a larger role in giving the semantics to our functional language than was the case for an imperative one, because there are more potentially shared immutable runtime datastructures in the functional case.

We use various different relations over natural numbers, but the most interesting construction is the ‘perp’ operation \((\perp)^\prime\), which takes a binary relation on states, \(R\), and produces a relation over natural numbers \(R^T\). This is the way we specify code pointers: \(R^T\) relates two natural numbers \(l\) and \(l'\) if for any states \(s\) and \(s'\) related by \(R\), executing from \(l\) in initial state \(s\) and from \(l'\) in state \(s'\) yields equitermination: either both executions halt or both executions diverge. Equitermination is our basic notion of equivalence of observable behaviour, and all our program specifications will be in terms of equitermination when placed in related contexts.

Of course, we can only talk about whether or not jumping to a particular address in a particular state halts or not if we specify the program as well. For this reason, all our state relations and natural number relations are also parameterized by a pair of programs. Finally, to deal with recursion, we also index all our relations by natural number step-indices (Appel and McAllester 2001). The intuition here is that if a relation represents a particular notion of equivalence, then its \(k\)-th approximant is ‘indistinguishable within \(k\) steps’. Rather than manipulate natural numbers everywhere (as we did in our previous work), we now use a modal operator (Appel et al. 2007) to tweak step-indices just where they matter. A further advantage of making this change is that we no longer build step manipulation into the definition of \((\cdot)^\prime\), which means that there is now a well-behaved Galois connection between relations on states and relations on natural numbers.

We now sketch the more formal definitions of the relations and relation constructors with which we use.

4.1 Relations on states and naturals

Definition 1 (State relation). A relation in stateRel is a predicate over two partial states, two programs and a step index, satisfying a monotonicity condition with respect to decreasing indices and extension of partial states:

Record stateRel : Type := mkStateRel {
\[ R \colon pstate \rightarrow pstate \rightarrow \text{program} \rightarrow \text{program} \rightarrow \text{nat} \rightarrow \text{Prop}; \]
stateRel_cond : \(\forall s_1 s_2 s'_1 s'_2 p_1 p_2 k_1 k_2, (R s_1 s_2 p_1 p_2 k_1 \land s_1 \subseteq s'_1 \land s_2 \subseteq s'_2 \land k_2 \leq k_1) \rightarrow R s'_1 s'_2 p_1 p_2 k_2 \).
}

where pstate denotes partial functions from nat to nat and \(s \subseteq s'\) means inclusion of partial functions. stateRel are ordered by:

Definition stateRelEq (R R' : stateRel) := \(\forall s s' p p' k, R s s' p p' k \rightarrow R' s s' p p' k\), and we will write \(\leq\) for this order.

Disjointness of partial states allows us to define a multiplicative tensor product, or separating conjunction, of relations (Reynolds 2002; Yang 2007), which we here give in mathematical notation:

Definition 2 (Separating conjunction). The separating conjunction \(R \otimes R' \) of two relations in stateRel is the stateRel defined by:

\[
(R \otimes R') s s' p p' k \iff \exists s_1 s_2 s'_1 s'_2, s = s_1 \# s_2 \land s' = s'_1 \# s'_2 \land \land R s_1 s' p p' k \land R' s_2 s'_2 p p' k
\]

where \(s = s_1 \# s_2\) means that the domains of \(s_1\) and \(s_2\) are disjoint and that the union of \(s_1\) and \(s_2\) is equal to \(s\).

Definition 3. An element of natRel is a relation between two natural numbers, two programs and a step index, satisfying a monotonicity condition:

Record natRel : Type := mkNatRel {
\[ R \colon pstate \rightarrow \text{program} \rightarrow \text{program} \rightarrow \text{program} \rightarrow \text{program} \rightarrow \text{nat} \rightarrow \text{Prop}; \]
natRel_cond : \(\forall l l' p p' k_1 k_2, (R l l' p p' k_1 \land k_1 \leq k_2) \rightarrow R l l' p p' k_2 \).
}

and again there is a natural order:

Definition natRelEq (r r' : natRel) := \(\forall l l' p p' k, r l l' p p' k \rightarrow r' l l' p p' k\).
We overload \textsf{Top} to mean the constantly total relation on both states and naturals. The following is a (non-exhaustive) list of some of the primitive \textsf{stateRel}s we will combine with \( \otimes \) to build specifications:

\[
\begin{align*}
\text{(natRel)} & \Rightarrow \exists l, l' s(n_1) = l \land s'(n_2) = l' \land l \ll p p' k \quad (r \in \text{natRel}) \\
\text{(stateRel)} & \Rightarrow \exists m, m_2 s(n_1) = m \land s'(n_2) = m_2 \\
\text{(liftP)} & \Rightarrow P \quad (P : \text{Prop}) \\
\text{(topfrom)} & \Rightarrow \forall n', n \geq m \Rightarrow n' \geq m' \Rightarrow (\{n, n'\} \rightarrow -)
\end{align*}
\]

The separating conjunction is crucial when we wish to make an update to the store whilst guaranteeing that certain other parts of the relation are not invalidated. We will also need to reason about the relatedness of closures, pairs and environments stored in the heap of our functional language, which are all allowed to share in unpredictable ways. To combine these specifications, we need a non-separating (Cartesian, additive) form of conjunction:

\begin{definition}[Additive conjunction]
The conjunction \( R \times R' \) of two relations in \textsf{stateRel} is the relation defined by

\[
(R \times R') s s' p p' k \iff R s s' p p' k \land R' s s' p p' k.
\]

\end{definition}

\begin{proposition}
Viewing \textsf{stateRel} with the order \( \preceq \) as a category, \( \otimes \) gives a symmetric monoidal structure and \( \times \) a cartesian product. These are related by the distributive law:

\[
(R \times S) \otimes T \preceq (R \otimes T) \times (S \otimes T).
\]

\end{proposition}

\subsection{An adjunction with natRel}

We relate relations on states and relations on code pointers via an adjunction between \textsf{stateRel} and \textsf{natRel}.

\begin{definition}[adjunction \textsf{Perp}] \textsf{Perp} \dashv \textsf{Perpnat}.
The two functors (order-reversing functions) \textsf{Perp} and \textsf{PerpNat} defined by

\begin{align*}
\text{Definition \textsf{Perp}} \quad (S : \text{stateRel}) & := \\
& \text{fun } s p p' l l' k \Rightarrow \forall s s' j, \\
& (j \leq k \land S \land s' p p' j) \rightarrow \\
& (\text{kstepterm } j \land \text{terminates } p p' l l') \land \\
& (\text{kstepterm } j \rightarrow \text{terminates } p s s' l l').
\end{align*}

\begin{align*}
\text{Definition \textsf{PerpNat}} \quad (L : \text{natRel}) & := \\
& \text{fun } s s' p p' l l' k \Rightarrow \forall s s' j, \\
& (j \leq k \land L \land s' p p' j) \rightarrow \\
& (\text{kstepterm } j \land \text{terminates } p s s' l l') \land \\
& (\text{kstepterm } j \rightarrow \text{terminates } p s s' l l').
\end{align*}

yield an adjunction (Galois connection):

\[
\begin{array}{c}
\text{stateRel} \\
\downarrow
\end{array}
\quad
\begin{array}{c}
\text{natRel} \\
\downarrow
\end{array}
\quad
\begin{array}{c}
\text{Perp} \\
\downarrow
\end{array}
\quad
\begin{array}{c}
\text{PerpNat}
\end{array}
\]

In mathematical notation, we’ll write \( R^\top \) for the image of the relation \( R \) by \textsf{Perp}. In our previous work, we defined \textsf{Perp} with a strict less than \( (<) \) relation on the step indices, which gave an operator without an adjoint, a point to which we will return later.

\begin{definition}[judgement]
We say two programs \( p \) and \( p' \) are equidivergent, or equiterminate under the relation \( R \in \text{stateRel} \) at points \( l \) and \( l' \) if they are in the relation \( R^\top \) for all indices:

\[
\models p, p' \triangleright l, l' : R^\top \iff \forall k, R^\top l l' p p' k.
\]

The above form of judgement is the one that we will use in specifying the relatedness of program fragments. Instead of saying that two programs are related if they map states satisfying a prerelation into states satisfying a postrelation, we specify and reason backwards, in a ‘continuation-passing style’, saying that two programs are related when if the exit points are equidivergent under the postrelation then the entry points are equidivergent under the prerelation.

\begin{example}
For any two programs \( p \) and \( p' \) containing the following two instructions

\[
\begin{align*}
&[[5]] \leftarrow [[[5]] + 1] \\
&[[5]] \leftarrow [0]
\end{align*}
\]

at respective addresses \( l, l + 1 \) and \( l', l' + 1 \), and for any \( R \in \text{stateRel} \), if

\[
\models p, p' \triangleright l + 2, l' + 2 : \\
(0 \rightarrow (28, 15)) \odot (5 \rightarrow 13) \odot (13 \rightarrow -) \odot R^\top 
\]

then

\[
\models p, p' \triangleright l, l' : \\
(0 \rightarrow (28, 15)) \odot (5 \rightarrow 13) \odot (13 \rightarrow (28, 15)) \odot R^\top
\]

This is just the ‘doubling up’ of a very simple unary property, but it serves to illustrate a simple use of perping and the way in which we use quantification over separated relations to make frames explicit: we do not have a ‘frame rule’ like that of separation logic, but achieve much the same effect using second-order quantification. Later, we’ll see more interesting relational judgements involving non-trivial behavioural relationships between data and code pointers in the stores on the two sides. Note that specifications like this are ‘partial’, in that they are also satisfied by a pair of programs which always diverge (or indeed always halt) when started at the respective entry points. Relational reasoning is stronger regarding termination than traditional partial correctness in unary logics, since termination can be captured via equitermination with something that terminates, but the diagonal part of this kind of specification on code (i.e. which program fragments are related to themselves) does not rule out divergence.

\subsection{Internalised quantification}

We define \textsf{stateRel}s quantified both universally and existentially over arbitrary Coq types in a straightforward higher-order style.

\begin{definition}[quantifiers]
If \( X \) is a type and \( h : X \rightarrow \text{stateRel} \), define

\[
(\exists x) s s' p p' k \defeq \exists x, h x s s' p p' k
\]

\[
(\forall x) s s' p p' k \defeq \forall x, h x s s' p p' k
\]

\end{definition}

\subsection{The modality and the Löb rule}

So far, most of our constructions have essentially just passed step indices around. The reason they are there is to enable inductive reasoning about looping and recursion. Previously, we did this by making the index \( j \) strictly smaller than \( k \) in the definition of \textsf{Perp} and did explicit induction over indices to prove loops and recursion. Following the work of Appel et al. (2007), we now abstract and
encapsulate some of this reasoning in a modality $\diamond$, which satisfies a form of the ‘Löb rule’ (sometimes also called the Gödel-Löb axiom). This modal rule is given by

$$
\frac{\diamond \alpha \vdash \alpha}{\vdash \alpha}
$$

understood as “if $\alpha$ is valid assuming that $\alpha$ is valid in the future, then $\alpha$ is always valid”. Reading ‘in the future’ as ‘after the machine has taken at least one step’, this yields an induction scheme that we can use to reason about recursion.

**Definition 8 ($\diamond$ modality).** The modality $\diamond$ (‘later’) is defined on elements $r$ of $\text{natRel}$ by

$$
\diamond r \vdash p' p' k \iff \exists j < k, r \vdash p' j
$$

The modality $\diamond$ satisfies a lemma that is the essence of a continuation-passing version of the Löb rule:

**Lemma 1 (Löb rule).** For all programs $p$ and $p'$, all locations $\text{ptr}$, $\text{ptr}'$, all program points $l$ and $l'$, and all $\text{stateRel}s$, the following rule is sound:

$$
\vdash p, p' \vdash l, l' : ((\text{ptr}, \text{ptr}' \vdash l, l') \otimes R) \otimes R
$$

$$
\vdash p, p' \vdash l, l' : ((\text{ptr}, \text{ptr}' \vdash l, l') \otimes R)
$$

5. Specifications and Verifications

In this section we explain the specifications for compiled code and the allocation module on which compiled code relies.

5.1 Allocator specification

Code produced by the compiler expects to be linked with a memory allocation module, which it uses to allocate and deallocate activation records and to allocate data structures in the heap. The reasoning is properly modular: we have an independent specification of the allocator to which the specification of compiled code refers, and we independently verify the output of the compiler and particular allocator implementations. The specification of the allocator is essentially the same as in our previous work, but with a slight twist relating to termination.

We have said that we generally use specifications that do not rule out divergence. This works for interpreting types of our high-level language, since those all include divergent programs. However, were we to try to prove that two different low-level program fragments (e.g. the compilation of two different source expressions) were in the relation associated with a type, relying only on an allocator specification that allowed divergence, there would be a problem: if the two fragments make different calls to the allocator, then we would not be able to prove that the programs equiterminate. So we have slightly modified the allocator specification of our earlier work to ensure totality.

**Definition 9 (total judgment).** We define when two programs $p$ and $p'$ satisfy **total judgment** $R_{\text{pre}} R_{\text{post}} p' p l_0 l_0'$ for program points $l_0$ and $l_0'$, $R_{\text{pre}} : \text{nat} \rightarrow \text{nat} \rightarrow \text{stateRel}$ and $R_{\text{post}} : \text{stateRel}$ thus

$$
\text{total\_judgment} \quad R_{\text{pre}} R_{\text{post}} p' p l_0 l_0' \iff 
\forall l_1 l_1' k \ s_0 s_0' \ (R_{\text{pre}} l_1 l_1') \ s_0 s_0' p' p \ k \Rightarrow
\exists j_1 j_1' s_1 s_1' \ \text{kstep\_predict} \ j_1 p s_0 s_1 l_0 l_1 \ \wedge 
\text{kstep\_predict} \ j_1 p' s_0' s_1' l_0' l_1' \ \wedge 
R_{\text{post}} s_1 s_1' p' p \ k
$$

where $\text{kstep\_predict} \ j_1 p s_0 s_1 l_0 l_1$ means that the program $p$ takes $j_1$ steps from the configuration $(s_0, p, l_0)$ to the configuration $(s_1, p, l_1)$.

Note that total judgements are more like those of traditional Hoare logics, and that this is less behavioural (i.e. more intensional) than our other form of specification: the relation $R_{\text{pre}}$ is explicitly parameterized by the return addresses, so we are asserting that particular program counters will be reached.

We now define the prerelations and postrelations used in the specs of the three entry points of the allocator, initialization, allocation and deallocation. Each of these is parameterized by a relation $R_a$ which will be a private invariant relating two different, equivalent, allocators. $R_a$ will be existentially quantified (i.e. made abstract) on the outside of the whole module. Each of the other parameters will be universally quantified over the total judgement for that particular entry point.

The parameterized relations $\text{RPre\_init}$ and $\text{RPost\_init}$ for the initialization routine of the allocator are given by

**Definition RPre\_init ($R_a$; stateRel) ($n_5$ $n_5'$ $n_6$ $n_6'$ l 1 l': nat):**

$$
\begin{align*}
(\text{ret} \rightarrow 1, l') & \otimes (\text{sp} \rightarrow n_5, n_5') \otimes (\text{env} \rightarrow n_6, n_6') \\
& \otimes \text{Topfrom} 10 10.
\end{align*}
$$

**Definition RPost\_init ($R_a$; stateRel) ($n_5$ $n_5'$ $n_6$ $n_6'$ : nat):**

$$
\begin{align*}
R_a & \otimes (\text{ret} \rightarrow -) \otimes (\text{sp} \rightarrow n_5, n_5') \\
& \otimes (\text{env} \rightarrow n_6, n_6').
\end{align*}
$$

**Definition total\_init Ra p p' init\' :=**

forall $n_5 n_5' n_6 n_6'$, total\_judgment

$$
\begin{align*}
& \text{RPre\_init Ra } n_5 n_5' n_6 n_6' \text{ p p' init\'.}
\end{align*}
$$

The intuition here is that if one calls the initialization routines of two related allocators, passing return addresses $l$ and $l'$, and with the values $n_5$ and $n_5'$ in the pseudo-register $\text{sp}$ on the two sides, and $n_6$ and $n_6'$ in $\text{env}$, then the two sides will each take some number of steps to reach $l$ and $l'$ in two states in which the invariant $R_a$ has been established, the original values in $\text{sp}$ and $\text{env}$ have been preserved and nothing is guaranteed about the contents of $\text{ret}$.

The parameterized relations $\text{RPre\_alloc}$ and $\text{RPost\_alloc}$ for the allocation routine are given by

**Definition RPre\_alloc ($R_a$ $R_c$; stateRel) ($n n'$ $n_5$ $n_5'$ $n_6$ $n_6'$ l 1 l': nat):**

$$
\begin{align*}
(\text{ret} \rightarrow 1, l') & \otimes R_a \otimes R_c \otimes (\text{arg} \rightarrow n, n') \\
& \otimes (\text{sp} \rightarrow n_5, n_5') \otimes (\text{env} \rightarrow n_6, n_6').
\end{align*}
$$

**Definition RPost\_alloc ($R_a$ $R_c$; stateRel) ($n n'$ $n_5$ $n_5'$ $n_6$ $n_6'$ : nat):**

$$
\begin{align*}
\text{Ex fb', Ex fb', } & ((\text{ret} \rightarrow \text{fb', fb'}) \otimes \\
& \text{ex}\Ra\text{Rc}\text{Ex}\text{fb' (n n'))} \\
& \otimes R_c \otimes R_a \otimes (\text{arg} \rightarrow -) \\
& \otimes (\text{sp} \rightarrow n_5, n_5') \otimes (\text{env} \rightarrow n_6, n_6').
\end{align*}
$$

**Definition total\_alloc Ra p p' alloc\' :=**

forall $R_c$ $n$ $n'$ $n_5 n_5'$ $n_6 n_6'$, total\_judgment

$$
\begin{align*}
& \text{RPre\_alloc Ra Rc } n n' n_5 n_5' n_6 n_6' \text{ p p' alloc\'}. \quad \text{Note that total judgements are more like those of traditional Hoare logics, and that this is less behavioural (i.e. more intensional) than our other form of specification: the relation } R_{\text{pre}} \text{ is explicitly parameterized by the return addresses, so we are asserting that particular program counters will be reached. We now define the prerelations and postrelations used in the specs of the three entry points of the allocator, initialization, allocation and deallocation. Each of these is parameterized by a relation } R_a \text{ which will be a private invariant relating two different, equivalent, allocators. } R_a \text{ will be existentially quantified (i.e. made abstract) on the outside of the whole module. Each of the other parameters will be universally quantified over the total judgement for that particular entry point.}.
\end{align*}
$$

We have slightly simplified these specifications in this presentation by leaving out parts that just mention unused pseudo-registers.

\footnote{We have slightly simplified these specifications in this presentation by leaving out parts that just mention unused pseudo-registers.}
ret pointing to disjoint blocks of memory of sizes $n$ and $n'$ on the respective sides.

The relations $\mathsf{RP_realloc}$ and $\mathsf{RPost_realloc}$ used to specify the deallocator are given by

**Definition $\mathsf{RP_realloc}$** ($R_a, R_c. \text{stateRel}$) ($fb \ fb'$ $n \ n'$ $n_5 \ n_5' \ n_6 \ n_6'$ $1 \ l' \ 1$: nat) :=

$$\begin{align*}
& (\text{ret} \rightarrow 1, l') \otimes (\text{arg} \rightarrow n, n') \otimes (\mathsf{sk} \rightarrow fb, fb') \\
& \otimes (\text{Block} \ \text{fb} \ \text{fb}' \ n' \ R_a \ \otimes R_c) \\
& \otimes (\text{sp} \rightarrow n_5, n_5') \otimes (\text{env} \rightarrow n_6, n_6')
\end{align*}$$

**Definition $\mathsf{RPost_realloc}$** ($R_a, R_c. \text{stateRel}$) ($n_5 \ n_5' \ n_6 \ n_6'$ nat) :=

$$\begin{align*}
& (\text{ret} \rightarrow -) \otimes (\text{arg} \rightarrow -) \otimes (\mathsf{sk} \rightarrow -) \\
& R_a \ \otimes R_c \ \otimes (\text{sp} \rightarrow n_5, n_5') \otimes (\text{env} \rightarrow n_6, n_6')
\end{align*}$$

**Definition total_realloc** $R_a$ p p' realloc dealloc' :=

$$\begin{align*}
& \text{forall } R_c \ \text{fb} \ \text{fb}' \ n \ n_5 \ n_5' \ n_6 \ n_6' \\
& \text{total}\_\text{judgment}
\end{align*}$$

Here the precondition is that there are blocks of memory of the appropriate sizes and disjoint from $R_a$, $R_c$ and the registers on the two sides. The postcondition just states that $R_a$, $R_c$ still hold and that the values in $\text{sp}$ and $\text{env}$ are preserved.

The relation between programs $p$ and $p'$ that says that they have allocator modules related by $R_a$ with entry points init, alloc and dealloc and init', alloc' and dealloc' is then just

**Definition AllocSpec** $p$ p' $R_a$ init' alloc alloc' dealloc dealloc' :=

$$\begin{align*}
& (\text{total}\_\text{init} \ R_a \ p \ p' \ \text{init}' \ ') \\
& \otimes (\text{total}\_\text{alloc} \ R_a \ p \ p' \ \text{alloc}') \\
& \otimes (\text{total}\_\text{dealloc} \ R_a \ p \ p' \ \text{dealloc}')
\end{align*}$$

### 5.2 Semantics of types

We can now define the low-level specifications corresponding to our high-level types. The basic idea is that for each source type $A$, we define $\mathbb{A} : \text{nat} \rightarrow \text{nat} \rightarrow \text{stateRel}$ such that $\mathbb{A} l' l$ relates pairs of states in which $l$ and $l'$ can be interpreted as pointing to equivalent values of type $A$. Since our values include closures, the dependency of $\text{stateRel}$ on the programs really gets used, as we will need to say that the values in the heap involve code pointers with certain behaviours. Using this notion of equivalent values in the state, we define when states contain equivalent environments and evaluation stacks of particular types. Finally, we specify compiled code fragments in terms of equitermination when started in sufficiently equivalent states, and linked with sufficiently equivalence-respecting continuations.

The inductive definition of $\text{semantics}\_\text{of}\_\text{types}$ is shown in Figure 3. Although this looks complex, it really amounts to a fairly familiar logical relational interpretation of types, but recast in a lower-level setting that naturally introduces more details.

The first clause says that two values $\mathsf{ptr}$ and $\mathsf{ptr}'$ are equal whenever considered at type $\text{Int}$. $P$ when they are equal natural numbers and also satisfy the predicate $P$. Just as one would expect. The second clause says that they are equal at $\text{Bool}$ $P$ when they both represent the same boolean value according to our chosen representation of booleans ($\mathsf{n2b}$ maps zero to false and successors to true), and that boolean satisfies the predicate $P$.

The third clause says $\mathsf{ptr}$ and $\mathsf{ptr}'$ represent equivalent values of type $A \times B$ in respective stores $s$ and $s'$ just when $\mathsf{ptr}$ is the address of two consecutive cells in $s$ with contents $\mathsf{value}$ and $\mathsf{value}'$, $\mathsf{ptr'}$ is the address of a pair of cells holding $\mathsf{value}'$ and $\mathsf{value}$ in the state $s'$, $\mathsf{value}$ and $\mathsf{value}'$ are equivalent values of type $B$, and $\mathsf{value}_2$ and $\mathsf{value}'_2$ are equivalent values of type $A$. So values are related at a product type if they are both pairs and their components are related pointwise.

The real work is in the fourth clause, which says what it means for values to be related at a function type. A functional value will contain some code, to which we will jump when we apply the function. So relatedness of functional values is a constraint on the behaviour of that code in certain contexts. The first thing to observe is that part of that contract will be that the code is allowed to assume that the allocator has been initialized, and must promise to maintain the allocator’s private invariant - that explains why the definition of the semantics of types is parameterized by the allocator’s private invariant $R_a$. The second thing to note is that all the functional values that are constructed by code produced by our compiler will be closures: pairs of a code pointer and an environment. But recall that we are trying to come up with maximally permissive *extensional* specifications, that will be satisfied by any code that behaves like a function when we apply it. So we certainly do not want to require that functions have environments containing values of some (even hidden) source language types. In fact, our calling convention does not even require that there is an environment at all – the function code gets passed a pointer to the function object itself, from which it can recover an environment if it has one, but the caller does not know anything about environments. So from an external point of view, the specification of what it means for $\mathsf{ptr}$ and $\mathsf{ptr}'$ to be related elements of $A \rightarrow B$ is just that there exists some private invariant relation $R_{\text{private}}$ such that $\mathsf{ptr}$ and $\mathsf{ptr}'$ point to code pointers that behave in a certain way and $R_{\text{private}}$ actually holds now (so the functions are set up and ready to call).

In the case of code produced by the compiler, that private invariant will get instantiated with assertions about the environment part of closures. Note that we have used the additive conjunction, so the private invariant is allowed to overlap/involve the memory locations $\mathsf{ptr}$ and $\mathsf{ptr}'$ themselves; this will actually happen when there is (direct or indirect) recursion.

So what are the details of the ‘certain way’ in which code of equivalent functions must behave? Firstly, this is where we use the ‘later’ modality discussed earlier, on which we rely when showing that the code produced for recursive functions satisfies the specification. The operational intuition is that for functions to be indistinguishable for $k$ steps, it suffices for their bodies to be indistinguishable for $k - 1$ steps, as testing them (i.e. applying them) involves jumping to them, which takes a step. Underneath the modality, the specification is a perp, a requirement that the two code pointers will equiterminate whenever they are jumped to in states related by the precondition $\mathsf{Pre\_arrow}$, which is parameterized by the private invariant, the two function values themselves, the invariant of the allocator and the semantics of the types $A$ and $B$.

At the risk of labouring the point, note that we pass in the semantic objects here, not the syntactic types, so the arrow construction works over arbitrary (appropriately parameterized) relations.

The precondition $\mathsf{Pre\_arrow}$ says under what conditions the entry points of the two functions have to promise to behave equivalently for them to be regarded as representing equal functions. We are essentially translating a CPS transformed version of the standard logical relations definition, that functions are related when they take related arguments to related results. So $\mathsf{Pre\_arrow}$ relates two states just when they represent calls to the two functions with $\mathbb{A}$-related arguments and $\mathbb{B}$-related continuations. The tops of the two stacks are expected to point to the original function objects (from which they can get to any local state, usually an environment, they might need). Below that on the stacks are two arguments, $\mathsf{ptr\_arg}$ and $\mathsf{ptr\_arg}'$ which are themselves equivalent according to $\mathbb{A}$. The allocator invariant $R_a$ is assumed to hold, disjoint from everything else. The environment registers point to
Definition Post_arrow b (Ra Rc: stateRel) Rc_cloud (n n' stack_ptr stack_ptr': nat):=
  Ex ptr_result, Ex ptr_result',
  (stack_ptr,stack_ptr' \mapsto ptr_result,ptr_result') \land
  ((b Ra ptr_result ptr_result') \land
  (stack_ptr+1,stack_ptr'+1\mapsto ptr_result,ptr_result') \land
  (a Ra Rc Rc_cloud) \land
  (workreg \mapsto -) \land
  (argreg \mapsto -) \land
  (retreg \mapsto -) \land
  (n4,n'+4 \mapsto (n,n')) \land
  (sreg\mapsto stack_ptr,stack_ptr') \land
  (envreg\mapsto n,n') \land
  unused_space.

Definition Pre_arrow R_private ptr_function ptr_function' Ra b:=
  Ex Rc, Ex Rc_cloud, Ex n, Ex n', Ex ptr_arg, Ex ptr_arg', Ex stack_ptr, Ex stack_ptr',
  (stack_ptr,stack_ptr' \mapsto ptr_arg,ptr_arg') \land
  (stack_ptr+1,stack_ptr'+1\mapsto ptr_function,ptr_function') \land
  ((n+4,n'+4 \mapsto (n,n')) \land
  (workreg \mapsto -) \land
  (argreg \mapsto -) \land
  (retreg \mapsto -) \land
  (n3 \mapsto -) \land
  (n4 \mapsto -) \land
  (n3 \mapsto -) \land
  (n4 \mapsto -) \land
  (sreg\mapsto stack_ptr+1,stack_ptr'+1) \land
  (envreg\mapsto n,n') \land
  unused_space.

Figure 3. Relational semantics of types

| Fixpoint semantics_of_types (t:ExpType) (Ra:stateRel) ptr ptr' struct t := | match t with |
| Int P \mapsto \lift (P ptr \land (ptr = ptr')) | |
| Bool P \mapsto \lift (P (n2b ptr) \land (n2b ptr = n2b ptr')) | |
| a \land b \mapsto Ex value, Ex value2, Ex value', Ex value2', (ptr,ptr'\mapsto value,value') \land
  (ptr+1,ptr'+1\mapsto value2,value2') \land
  (b Ra value value' \land
  (a Ra value2 value2')) | |

end

where "'[[ t ]]'" := (semantics_of_types t).

definition Post_arrow b (Ra Rc: stateRel) Rc_cloud (n n' stack_ptr stack_ptr': nat):=
  Ex ptr_result, Ex ptr_result',
  (stack_ptr,stack_ptr' \mapsto ptr_result,ptr_result') \land
  ((b Ra ptr_result ptr_result') \land
  (stack_ptr+1,stack_ptr'+1\mapsto ptr_result,ptr_result') \land
  (a Ra Rc Rc_cloud) \land
  (workreg \mapsto -) \land
  (argreg \mapsto -) \land
  (retreg \mapsto -) \land
  (n4,n'+4 \mapsto (n,n')) \land
  (sreg\mapsto stack_ptr,stack_ptr') \land
  (envreg\mapsto n,n') \land
  unused_space.

definition Pre_arrow R_private ptr_function ptr_function' Ra b:=
  Ex Rc, Ex Rc_cloud, Ex n, Ex n', Ex ptr_arg, Ex ptr_arg', Ex stack_ptr, Ex stack_ptr',
  (stack_ptr,stack_ptr' \mapsto ptr_arg,ptr_arg') \land
  (stack_ptr+1,stack_ptr'+1\mapsto ptr_function,ptr_function') \land
  ((n+4,n'+4 \mapsto (n,n')) \land
  (workreg \mapsto -) \land
  (argreg \mapsto -) \land
  (retreg \mapsto -) \land
  (n3 \mapsto -) \land
  (n4 \mapsto -) \land
  (n3 \mapsto -) \land
  (n4 \mapsto -) \land
  (sreg\mapsto stack_ptr+1,stack_ptr'+1) \land
  (envreg\mapsto n,n') \land
  unused_space.

The specifications for the two related return addresses, stored in the callers' frames, are once again modalized perped formulae, asking that those two return addresses behave equivalently whenever they are returned to with states that are appropriately related according to conditions that are made precise in the definition of Post_arrow. The return addresses can assume that the stack pointers point to equivalent returned values according to \([B]\) and that the local frame invariant \(Rc\cloud\) from the preconditions has been preserved, sharing storage with the return value. They can also assume that the allocators' private invariant has been maintained and that the frame condition on the rest of the state, \(Rc\), has been preserved. The environments will have been put back to what they were before the call (the preservation of \(n\) and \(n'\)) and the stack pointer will be one less than it was before the call (because we have popped the argument and the function object and pushed a return value).

The above specification of type-dependent relatedness of values in the heap of the machine is a stepping stone on our way to writing specifications for computations. The actual programs that manipulate and generate values are the things we ultimately want to verify. Given a context \(\Gamma\) and a type \(A\), we need to define a specification for low-level programs that corresponds to producing equal results of type \(A\) when stated in equal contexts of type \(\Gamma\). The definition of equal contexts of type \(\Gamma\) is the stateRel that says there are two linked lists (starting at particular locations) with elements that are pairwise related by the interpretation of the corresponding type in \(\Gamma\). This is a fairly straightforward induction over \(\Gamma\), using the semantics of types defined above:

| Fixpoint semantics_of_env (env:EnvType) Ra current current' struct env := | match env with |
| nil \mapsto Top | |
| h :: t \mapsto Ex ptr, Ex ptr', |
| Ex next, Ex next', |
| ((current,current' \mapsto ptr,ptr') \land
  (current+1,current'+1 \mapsto next,next') \land
  (ptr,ptr' \mapsto [t] Ra next next') | |
end

where "'[[ env ]]'" := (semantics_of_env env).

Note that we have used the additive conjunction again here, so elements of the environment can share heap storage with one another. Environments do not have to be terminated and are even allowed to be cyclic (directly through the link structure, or indirectly through closure values).

Compiled expressions also make use of a local evaluation stack (not to be confused with the call stack of activation records). This is separate from the language heap (cloud) and gets read and written as values are pushed and popped. The values on the stack, however, will generally be related by relations that involve the cloud and can overlap. To specify relatedness of stacks we therefore define a ‘fold’ operation that relates states in which there are particular sequences of values in consecutive memory locations and disjoint
regions of memory in which an additive conjunction of relations, indexed by the values in the sequences, holds.2

Definition lstack_type := list (nat → nat → stateRel) * (nat * nat)).

Fixpoint stack_description (stack_list:lstack_type) ptr ptr’ clouddel struct stack_list := match stack_list with
| nil ⇒ clouddel
| pair h (pair ptr_h ptr_h’) :: t ⇒ (ptr,ptr’ ←→ ptr_h,ptr_h’) ⊗ stack_description t (ptr-1) (ptr’-1)
end.

We then package up a description of entire related memory configurations like this:

Definition memory_specification Ra Rc Rc_cloud env stack_list stack_free stack_free’ stack_ptr
stack_ptr’ n n2 n3 n’ n2’ n3’:=
(spreg←stack_ptr,stack_ptr’) ⊗
Block (stack_ptr+1) (stack_ptr+1)
stack_free stack_free’ ⊗ (environ← n,n’) ⊗
storage space n n2 n3 n’ n2’ n3’ ⊗
stack_description stack_list stack_ptr stack_ptr’
(stack) Ra n n’ × Rc_cloud) ⊗ (workreg ←-)
⊗ (argreg ←-) ⊗ (retreg ←-) ⊗ Ra ⊗ Rc ⊗
(3→-) ⊗ (4→-) ⊗ unused space.

which relates two states when the ‘active’ parts of the stack are related according to stack_list, there is a Block of unused stack slots above that, the environments on the two sides are related according to the interpretation of the type environment env (with appropriate sharing), the allocator invariant $R_n$ holds and an invariant $R_{n'}$ holds on the rest of the state. The relation storing space describes the contents of the callee-saves slots in the current activation record, we also specify that the various registers point to arbitrary values.

5.3 Type Soundness

We are finally in a position to state our main result, that the compiler produces code that respects our relational interpretation of types. The theorem as stated in Coq is given in Figure 4. Although the statement looks rather complex, what it really says is not too hard to understand. The functions we have not defined, such as extract_code_from_globalcode, just project components from the result of compilation. We start with a source level expression $e$ which has type $a$ in source-level type environment env. We compile $e$ twice, once from the location start and linking against allocation and deallocation routines at addresses alloc and dealloc, and once starting at start’ linking against alloc’ and dealloc’.

Then for any complete program $p$ that extends the main and auxiliary code produced by the first compilation, and for any $p'$ that extends the code produced by the second compilation, if $p$ and $p'$ have $Ra$-related memory allocation routines at the entry points we used in the respective compilations, then we get the result about the relatedness of the behaviour of the two bits of compiled code.

The judgement about the compiled code says that executing from program counter start in program $p$ and from program counter start’ in program $p'$ yields equitermination provided that (a) the two initial states are related by a memory specification corresponding to the type environment env, together with some arbitrary other bits, and (b) the labels after the code compiled from $e$ on the two sides always equiterminate when they are started in states that are related by the same memory specification that we assumed at the entry points, modified to add $[\mathcal{A}]$-related values on top of the evaluation stack (with appropriate modifications to both the stack pointers and the size of the unused stack locations).

So what does that tell us? One consequence is about the behaviour of closed programs of ground type, such as Int P. The theorem says that if you compile such a program and link it against a well-behaved allocator then it will either always diverge or produce the same ground value, which will moreover satisfy the predicate refinement $P$. The observable behaviour will not change according to what locations are returned by the allocator, what the initial contents of any bits of memory are, where we initially put the stack, or anything else it should not depend upon. Moreover, the computation will not write to any areas of memory that it should not (because of the preservation of an arbitrary $Re$).

More importantly, the specification is entirely modular and semantic. The proof obligations for writing non-standard implementations of higher-order functions that are extensionally indistinguishable from (and so can interoperate with) those produced by our compiler, yet might be implemented quite differently, are made explicit and could be verified without the compiler source.

6. Remarks on the Coq formalization

Our Coq formalization covers everything discussed here: the low level machine, high level language, the compiler, two allocator modules, the general relational reasoning framework, the specifications and the proof of semantic type preservation. (The famous Knuth quote “Beware of bugs in the above code; I have only proved it correct, not tried it.” almost applies, though we have compiled and executed just a few simple programs within Coq and obtained the right answers.)

The entire development is around 14000 lines and relies heavily on our previous work, though few things have been left completely unchanged. The code for the memory allocators is the same, but moving to relations over partial states instead of using accessibility maps and changing the treatment of step indices have meant small changes throughout. In general, the proof assistant helps rather than hinders such evolution – one can change basic definitions and then update the scripts so that proofs of basic properties still go through surprisingly quickly and sometimes almost automatically.

We may heavy use of Setoid rewriting modulo the preorder $\preceq$, but have replace our earlier handcrafted reflective tactics for reorganising long sequences of $\otimes$-ed stateRel modulo associativity and commutativity with uses of the library-provided ring tactic. This removes the two-level (syntactic and semantic) structure of our explicitly reflective approach which we were never sufficiently disciplined to use cleanly before.

The proofs also make rather more use of specialized tactics than our earlier ones, and we have overall managed to keep the size of this formulation about the same as that of our previous one, for a simple imperative language with no procedures, despite the fact that the specifications and proofs here are much more complex and the compiler is considerably larger.

The general pattern of reasoning is, as in our earlier work, forward Hoare style proving, using rules for entailments on stateRel and judgements to set the goal up in the right form to apply instruction-specific lemmas such as the following, for an unconditional indirect branch:

**Lemma 2** (Branch). For all $p, p', l, l', m$ and $R$, if

$$p(l) = js[p \llbracket e \rrbracket], \quad p'(l') = js[p \llbracket e \rrbracket]$$
Theorem compiler_sound :
for all base base’ Ra init init’ alloc alloc’ dealloc dealloc’ Γ a e (t;Γ ⊢ e : a)
Rec Rc_cloud start start’ stack_ptr stack_ptr’ n n2 n3 n’ n2’ n3’ p p’ stack_list,
let global_code := compile e nil base dealloc in
let code := extract_code_from_globalcode global_code start in
let stack_free := extract_stacksize_from_code code in
let global_code’ := compile e nil base dealloc’ in
let code’ := extract_code_from_globalcode global_code’ start’ in
let stack_free’ := extract_stacksize_from_code code’ in

prog_extends_auxcode global_code p base →
prog_extends_code global_code p start →
prog_extends_auxcode global_code’ p’ base’ →
prog_extends_code global_code’ p’ start’ →

AllocSpec p p’ Ra init init’ alloc alloc’ dealloc dealloc’ →
(forall ptr ptr’, |= p p’ ⊃ (start+length (instruction_of_code)) (start’+length (instruction_of_code’)) : (memory_specification Ra Rc Rc_cloud Γ ([a] Ra, (ptr,ptr’)) :: stack_list)
(stack_free-i) (stack_free’-i) (stack_ptr+i) (stack_ptr’+i) n n2 n3 n’ n2’ n3’)) →
|= p p’ ⊃ start start’ : (memory_specification Ra Rc Rc_cloud Γ stack_list stack_free stack_free’
stack_ptr stack_ptr’ n n2 n3 n’ n2’ n3’)

Figure 4. Semantic Type Soundness

and

R ⊆ (m → □R)

then

|= p, p’ ▷ l, l’ : R

which says that we get equitermination by executing the jump instructions at l and l’ in states satisfying R if R entails that the location m through which we jump points to code pointers that later yield equitermination when jumped to in states satisfying R.

The crucial lemma for proving the recursive functions is the following, proved by appeal to the Lb rule we gave earlier:

Lemma recursion_continuation :
for all ptr ptr’ fcode fcode’ Ra a b R p p’,
judgment (Pre_arrow (((a→b) Ra ptr ptr’ × R)
ptr ptr’ Ra ([a]) ([b])) p p’ fcode fcode’) →
judgment (Pre Arrow ((ptr,ptr’→ fcode, fcode’)×R
ptr ptr’ Ra ([a]) ([b])) p p’ fcode fcode’).

The antecedent is what we initially prove about the code pointers fcode and fcode’: that they will equiterminate appropriately provided that ptr and ptr’ (which will be the heads of the two closure environments) point to equivalent functions. The consequent is what we want to conclude about the result of tying the knot: that we then get equitermination when ptr and ptr’ point, respectively, to fcode and fcode’ themselves.

7. Discussion

We have shown how to specify and verify a low-level relational interpretation of functional types. The construction involves a number of familiar ideas: the logical interpretation of types as relations, separation logic, orthogonality, step-indexing and so on, but putting them all together in the right way is far from trivial, and we certainly would not claim to have completely solved the problem.

The existentially-quantified private invariant Rpri vate at the top level of our interpretation of function types is a semantic generalization of the well-known use of existentially quantified types to abstract the type of environments in typed conversion (Minamide et al. 1996; Glew 1999; Ahmed and Blume 2008). Our general use of second order existential quantification over stateRel s to express private invariants generalizes the standard treatment of type abstraction via existential types (Mitchell and Plotkin 1988).

The use of ‘orthogonality’ or ‘perping’ in realizability was pioneered by Pitts and Stark (1998) and by Krivine, and has received much attention, notably by Younion and Mellies (2004). A general theory of such ‘tensorial negations’ is investigated in more detail in the thesis of the second author (Tabareau 2008), but there are still open questions about their use in low-level settings. One such question is whether, now we have an adjunction and hence a (−)⊤ closure operator on stateRel s, we really need explicit step indices too; in higher-level models, biorthogonal closed relations are automatically admissible. Another very important question is how one would adapt the kinds of specification used here to real machines with finite memory; it is not obvious what the ‘corresponding’ theorems should be.

Note that our interpretation of function types and general computations in context constrains them to be rather pure – the extent to which they can read or write the state is severely constrained. Our hope is to be able to do equational reasoning at the level of low-level code, proving that different bits of machine code are in the relation associated with a particular type. However, we have not yet managed to make this work, despite our move to total correctness specifications of the allocator. The CPS treatment of prerelations and postrelations seems to give us an equational theory which is roughly that of call-by-value CPS-transformed high-level functions. Whilst this is a strong constraint on the behaviour of machine code programs, it is still too weak to prove something as trivial as the high-level commutativity of addition, as the CPS transforms of let x = M in let y = N in x+y

and

let y = N in let x = M in x+y

are not generally observationally equivalent. A related annoyance is that our CPS interpretation does not seem to validate the well-known rule for conjunction types

(A → B₁) ∧ (A → B₂) ⊆ (A → B₁ ∧ B₂)

or the morally equivalent rule for introducing universal quantification over logical variables used in our refinement types:

[∀I] Γ ⊢ M : (A(i)) i /⊆ Γ
Γ ⊢ M : ∀ i . A(i)

It is well known that these rules can be unsound in the presence of effects (Davies and Pfenning 2000), but our language is sufficiently
pure that we should not require a value restriction here: we just have not quite captured that purity in our specifications.

Yet another wart on our otherwise beautiful theory is the way the treatment of total correctness makes explicit reference to particular code pointers. This is just about bearable for simple first-order procedures like the allocator, but would be untenable and insufficiently modular if we were to, say, try to interpret types of a normalizing lambda calculus. We are currently investigating some ideas about parametricity in the notion of ‘observation’ (here it was always equi-termination) with respect to which one takes perps, which may help here.

One of our initial goals was that our interpretations of types be expressed in a logic that was essentially independent of the source. This seems desirable in multilanguage PCC settings, but certainly introduces some complexity. An alternative approach would be to define a ‘represents’ logical relation between a high-level semantics for the source language and the low-level compiled code (this would then induce a partial equivalence relation on low-level programs). Chlipala (2007) has already done this for a simple total functional language, which can be given a straightforward type-theoretic denotational semantics; we plan to do something similar using a formalized domain-theoretic semantics for our partial language. Our hope is that this will help us better understand the shortcomings of the low-level relations.

Finally, of course, we want to look at various forms of compiler correctness for high-level languages with effects. We and others have successfully applied essentially the same mathematical machinery to reasoning about higher-order functions with encapsulated local state (Ahmed 2004; Benton and Leperchey 2005; Pitts and Stark 1998; Ahmed et al. 2009), so moving those ideas down to the low level looks very doable.

There is a great deal of related work on program logics, compiler correctness and the semantics of types, much of which we have plundered here. Formal verification of compilers goes back over four decades (McCarthy and Painter 1967; Dave 2003) and has recently received increased attention, with notable automated efforts including Leroy’s verification of an optimizing compiler for a C-like language (Leroy 2006). Appel’s Foundational Proof Carrying Code project (Appel 2001) at Princeton has very similar goals to this work, and many of the techniques we use were introduced by Appel and his coauthors. We mention in particular the use of step-indexing (Appel and McAllester 2001; Tan et al. 2004) and its modal refinement (Appel et al. 2007). Shao’s group at Yale have also done impressive work on formalizing and verifying specifications for low-level code in Coq, including memory managers, garbage collectors and other challenging pieces of systems code (Ni and Shao 2006; Yu et al. 2004). It will be interesting to see if we can do similar things in our relational style.

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References