Lithium Ion capacitor characterization and modelling
Hamid Gualous, G. Alcicek, Youssef Diab, A. Hammar, Pascal Venet, K. Adams, M. Akiyama, C. Marumo

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Abstract
To develop electrical busses for applications with a fast recharge system in stations, PVI has been testing some electrical energy components like supercapacitors and batteries with high power density. Nevertheless, supercapacitors limit average autonomy between two recharge points, due to their poor energy density. On the other side cycle life of batteries are very dependent on current. To surmount these problems, PVI in collaboration with the FCLAB laboratory and the AMPERE laboratory, are studying Lithium-ion capacitor (LIC) for applications with fast recharge.

We take to assess how the storage system meets busses power and energy requirements in heavy electric vehicles. We note that the advantage of LIC technology compared to conventional supercapacitor lies in the fact that the energy density and the nominal voltage are higher. In this study, the Li-ion capacitor is characterized and modelled. The characterization and modelling methods are the same of supercapacitor with double layer activated carbon technology. The LIC efficiency will be discussed.

I. INTRODUCTION
Lithium Ion capacitor is a new storage device which combines high power density and high energy density compared to conventional supercapacitor of the market. It has four time higher energy density than conventional supercapacitor. The structure of the LIC is composed by two electrodes. The positive one is formed by activated carbon as in double layer capacitor. The negative electrode uses lithium ion doped carbon. This new electrode technology boosts the capacity of the negative electrode and increases the electrical potential difference. The electrolyte is based on the Li Ion.

Figure 1 shows the elementary structure of EDLC and Li-ion capacitor structure. It can be seen that the negative LIC electrode is formed by Li doped Carbone. The equivalent capacitance is formed by the positive electrode capacitance Cdl in series with the negative one Cli. The equivalent capacitor can be expressed as following:

\[
\frac{1}{C_{eq}} = \frac{1}{C_{dl}} + \frac{1}{C_{li}}
\]

where \(C_{li} \gg C_{dl}\) ⇒

\[
C_{eq} \approx C_{dl}
\]

![EDLC and Li-ion capacitor structure](image)

The Li Ion capacitor studied in this paper (figure 2) is fabricated by JM Energy. Their parameters are: nominal capacitance: 2000F; volume 124ml; weight: 208g.
maximum operation voltage 3.8V and minimum voltage of 2.1V.

II. LI ON CAPACITOR CHARACTERIZATION

A. DC characterization

Li-ion capacitor is charged and discharged under constant current constraints for several current values. Figure 3 represents the Li-ion capacitor voltage evolution as a function of time. The current of charge and discharge is fixed at 100A at ambient temperature; the device voltage varies between its nominal voltage 3.8V and 2.2V. Charge discharge result shows that voltage curves can be fitted with linear curve.

The DC ESR and C measuring methods are based on the Li-ion capacitor discharge at constant current. At the first approximation, Li-ion capacitor can be characterized by an equivalent series resistance and by an equivalent capacitance. The leakage current and the Li-ion capacitor inductive behavior can be neglected. The method used to determine ESR and C is presented in figure 4 and 5.

\[
C = \frac{\Delta I}{\Delta V} \tag{3}
\]

where \( I \) is the current of discharge, \( \Delta t = t_1 \) and \( \Delta V = V_2 - V_1 \). Where \( V_1 = V_{\text{min}} + 40\% (V_{\text{max}} - V_{\text{min}}) \) and \( V_2 = V_{\text{min}} + 80\% (V_{\text{max}} - V_{\text{min}}) \).

In the equation (3), \( I \) is the current, \( \Delta t \) can be the duration of charge or discharge and \( \Delta V \) is the Li-ion capacitor voltage variation. Using this expression we have determined the equivalent capacitance of charge and discharge and for 50A, 100A, 150A and 200A. Table I gives the Li Ion capacitor of charge (\( C_{\text{eqch}} \)) and discharge (\( C_{\text{eqdis}} \)).

<table>
<thead>
<tr>
<th>Current (A)</th>
<th>( C_{\text{eqch}} ) (F)</th>
<th>( C_{\text{eqdis}} ) (F)</th>
<th>( C_{\text{eqdis}}/C_{\text{eqch}} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>50</td>
<td>2118</td>
<td>2100</td>
<td>99%</td>
</tr>
<tr>
<td>100</td>
<td>2083</td>
<td>2004</td>
<td>96%</td>
</tr>
<tr>
<td>150</td>
<td>2024</td>
<td>1955</td>
<td>96%</td>
</tr>
</tbody>
</table>

These results shows that Li-ion capacitor coulombic efficiency (\( C_{\text{eqdis}}/C_{\text{eqch}} \)) is very high (>96%) compared with battery.

For DC ESR measurement is based on the discharge at constant current (cf. discharge voltage figure 5). The time between the end of charge and the start discharge is fixed at 30 mn this duration can be reduced because of the very low Li-ion capacitor self discharge compared with double layer capacitor. The ESR is calculated by using the following expression:

\[
ESR = \frac{\Delta U^3}{I} \tag{4}
\]

\( \Delta U^3 \) is the drop voltage obtained from the intersection of the auxiliary line extended from the straight part and the time base at time of discharge start, I is the constant current of Li-ion capacitor discharge.
ESR variations with current for these three values can be neglected.

### B. AC characterization

The Li-ion capacitor AC characterization was realized using an Electrochemical Impedance Spectroscopy (EIS). To characterize the studied device, the sweep in frequency must be done for various voltage levels. EIS allows the study of the influence of frequency on the Li-ion capacitor. Figure 6 presents the variation of the negative imaginary part as a function of the real part for different voltage values. It can be seen that the Li-ion capacitor equivalent capacitance $C$ is not linear with voltage.

![Fig. 6: Li Ion capacitor imaginary part as a function of real part for 2.2V, 2.6V, 3V; 3.4V and 3.8V](image)

It assumed as a first approximation that Li-ion capacitor is modelled by a resistance in series with capacitance. Using the EIS results we deduced the $C$ evolutions as a function of DC voltage. Figure 7 represents the experimental results. It can be seen that Li-ion capacitor equivalent capacitance $C$ is not linear with voltage.

![Fig. 7: C variations with Li-ion capacitor voltage](image)

The dc voltage dependency of ESR is depicted in figure 8. No as the classical double layer capacitor, an increase in voltage leads to decrease the ESR. This means in high voltage, we can obtain best discharging power.

![Fig. 8: ESR v. frequency for different voltages](image)

Figure 9 shows the variation of capacitance versus the frequency for different voltage from this figure, it’s clear that the Li-ion capacitor equivalent capacitance $C$ is not linear with voltage.

![Fig. 9: Capacitance v. frequency for different voltages](image)

### III. LI-ON CAPACITOR MODELLING

To model the LIC components, we have chosen a “multipenetrability” [2] model presented on figure 10. It is composed of four elements, inductance $L$, series resistance and complex parallel pore impedances described by the equation below. In the presented model only $Z_{p1}$ and $Z_{p2}$ are considered. The model parameters are calculated using the experimental results of EIS. The comparison between simulated and measured Nyquit plot are presented on figure 11.
We have presented Li capacitor and its electrical parameters according to electrical model. It shows high capacitance density with relatively high resistance, it is due to the nature of electrolyte employed. This resistance limits efficiency of the component for high current values (>200A). Capacitance shows two linear behaviours, under and above 3V, it may be due to the lithium doping effect. Electrical model and characterization methods applied to supercapacitors kept valid in the case of Li-capacitor. In our future papers, we try to study thermal behaviour and ageing of Li-ion capacitors.

REFERENCES