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LTO - SiO₂ DEPOSITION IN A STAGNATION FLOW LPCVD SYSTEM

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Abstract

A cold wall low temperature oxide (LTO) process, by a stagnation flow technique for the deposition of device quality SiO₂ on silicon wafers at temperatures of 280°- 400°C is reported. The following properties of the SiO₂ films were investigated: Growth rate, deposition profile, refractive index, wet etch rate, chemical characterization by XPS, electrical properties and IR - spectra have been studied as function of substrate temperature, reaction pressure, carrier gas and O₂/SiH₄ mole ratio.

I. Introduction

The growth of thin silicon dioxide layers by chemical vapor deposition (CVD) has become one of the most important methods for semiconductor device processing and has been investigated by several authors /1/-/6/. The main advantage of LTO depositions is their considerably lower process temperature as compared to high temperature oxide (HTO: SiH₂Cl₂ + N₂O; = 850°C) and the tetraethylorthosilicate process (TEOS: = 700°C). Unlike plasma enhanced CVD, LTO processes avoid high concentrations of bonded hydrogen (in the form of Si-H and of OH groups) in the oxides. H-bonds in the SiO₂ films can produce recombination centers and cause deep trapping /7/. The LTO process avoids plasma induced surface damage, which can also lead to high H-concentrations /7/. In particular LTO processes with stagnation flow technique attain deposition rates, which exceed the deposition rates of other processes as e.g. LTO processes with LPCVD quartz tubes, by an order of magnitude. Stagnation flow deposition systems with high deposition rates produce advantageously high yields and good film quality. Their low deposition temperature and high deposition rates make SiO₂ films suitable for final passivation after aluminium metallization. Furthermore the high deposition rate and the option to use a vacuum loadlock make a throughput as high as in batch systems possible. Especially for R&D applications these systems provide a number of advantages like quick processing, high efficiency, easy investigation of the reaction products and simplified numerical simulation /8/.

In a stagnation flow reactor the deposition results depend mainly on: Gas flow, convection, diffusion and chemical reactions in the gas phase and at the wafer surface. Optimisation of these parameters is a crucial requirement for manufacturers (reactor design, process control and development) and for users (deposition profile, film properties). This work describes the dependency of the deposition results on variation of the process parameters.
1. Experimental

A specially designed cold wall reactor (Fig.1) was used for these investigations. The reactor consisted of a rotating infra-red heated wafer support and a shower head type gas inlet system. A two stage rotary pump (Leybold TRIVAC 40 BCS) in connection with a roots blower (Leybold RUVAC 250 WSU) provided the necessary low pressure (< 5000 Pascal) in the CVD reactor. The flow rates of the reaction gases (SiH\textsubscript{4}, O\textsubscript{2} and N\textsubscript{2} for inert gas) were adjusted by thermal mass flow controllers. The gas flow was led through a shower head perpendicularly onto the silicon wafer. Four halogene infra-red lamps generated the required wafer temperature, which was stabilized by feedback via a thermocouple and a self optimizing PID Eurotherm power controller. A purge gas was applied to expel reactive gases from the heat module in order to avoid deposition on the hot lamp surfaces. The reactor chamber and the shower head were made from stainless steel, and the wafer support from fused silica.

The SiO\textsubscript{2} films were deposited onto one-side polished 0.01 - 0.02 \textmu m (100) silicon substrates. Electronic grade chemicals (Merck, Selectipur) were used for substrate cleaning. The silicon substrates were cleaned by the following procedure:

- Successive washing (10 minutes each step) in:
  1. trichloretylene
  2. acetone
  3. isopropanole
  4. deionized water (=18x10\textsuperscript{6} \textmu cm)
  5. HNO\textsubscript{3} 65% (at room temperature)
  6. deionized water (=18x10\textsuperscript{6} \textmu cm)
  7. HF dip (5%)
  8. deionized water (=18x10\textsuperscript{6} \textmu cm)

- Treatment in a rinser dryer (15 minutes each step):
  9. deionized water (=18x10\textsuperscript{6} \textmu cm)
  10. hot N\textsubscript{2}
II. Results and Discussion

1. Influence of Temperature

Deposition experiments were carried out with the following parameters:

- total pressure 1000 Pascal
- reactive gases 10 sccm SiH₄, 160 sccm O₂
- carrier gas 500 sccm N₂
- deposition time 50 sec

During the heat up time, (approximately 2 min.) reactive gas was led through a bypass and adjusted to a constant flow rate. After the temperature had risen to the required value, the bypass was closed and the deposition started. Directly after the deposition process the system was pumped down to \( \times 10^{-1} \) Pascal. Afterwards the system was purged with N₂ until the wafer was cooled down below 40°C and then refilled to atmospheric pressure. Subsequently a new wafer was placed into the reactor and the next deposition could be started. The dependence of growth rate on the wafer temperature is shown in Fig. 2. In the temperature range between 300°C and 370°C the deposition rate increases by a rate of \( \approx 0.02 \) nm sec⁻¹/°C proportionally to the temperature. Above 370°C the growth rate vs. temperature curve flattens out.

![Fig. 2 Deposition rate vs. temperature](image1)

2. Influence of oxygen-to-silane ratio (\( \gamma \))

The oxygen-to-silane ratio \( \gamma \) was varied, while the total gas flow, the pressure, and the temperature was kept constant (720 sccm, 1000 Pa, 400°C). The results for \( \gamma \)-values from 1 to 32 are plotted in Fig. 3. For \( \gamma \)-ratios up to 8, the growth rate vs. \( \gamma \)-curve shows a steep rise; for higher \( \gamma \) ratios the growth rate comes to saturation at a value of about 2.9 nm/sec. The film thickness uniformity deteriorates considerably with decreasing \( \gamma \)-values, as is shown in Fig. 4.

![Fig. 3 Deposition rate vs. \( \gamma \)](image2)
3. Pressure Influence

The deposition rate versus total pressure (with gas flow, wafer temperature and carrier gas flow kept constant) is depicted in Fig. 5; the maximum rate is approximately 3.6 nm/sec at 1200 Pa. Fig. 6 shows ellipsometrically measured thickness profiles, for different total pressures. The thickness profiles show the best uniformity (< 2% deviation from the mean value, within a distance of 9 mm from the edges) at a pressure of 1200 Pa. Thickness uniformity and growth rate are thus simultaneously optimized. Operation at pressure levels higher than the optimum value (1200 Pa) lead to deteriorated thickness profiles. At lower pressures than 800 Pa the deposition rate becomes small and some particulate contamination can be observed.
4. Influence of Carrier Gas

The carrier gas flow is another important parameter which effects the deposition rate and particulate contamination. The formation of particulate SiO$_2$-deposits is well known /9/ and can be suppressed with optimized process parameters. In order to reduce the concentration of the reactive gas, N$_2$ was applied as diluting gas. We used highly pure nitrogen (H$_2$O and O$_2$ contents <0.1 ppm) to optimize deposition results and to suppress homogeneous gas phase nucleation. At high flow rates the SiO$_2$ deposition rate decreases and a film thickness deviation of greater than 12% from the mean value (measured at a distance of 9 mm from the edges) occurs. Fig. 7 shows the influence of the carrier gas on the deposition rate. We found that lower particulate contamination of the reactor walls, adequate deposition rates and profiles can be obtained at about 500 sccm carrier gas flow. Examples for SiO$_2$ profiles at different carrier gas flows are shown in Fig. 8.

![Graph showing deposition rate vs. flow rate of carrier gas](image1)

**Fig. 7** Deposition rate vs. flow rate of carrier gas

![Graph showing thickness uniformity for different carrier gas flows](image2)

**Fig. 8** Thickness uniformity for different carrier gas flows

5. Shower Head Variation

The film uniformity depends strongly on the gas flow from the gas inlet shower head. For instance the distance between substrate and gas shower head had a strong impact on the deposition results. A homogeneous distribution of holes over the shower head resulted in a poor uniformity of the layer thickness (Fig. 9a). To compensate this effect one can use an inhomogeneous distribution of holes. The resulting film uniformity is shown in Fig. 9b.
6. Etch Rate

A sensitive characterization of the silicon dioxide quality is the determination of the etch rate. Chemical structure of the films, mechanical stress, resistivity and breakdown field are in most cases related to the etch properties /10/. Etch rates of the SiO₂ films in 1.8 M HF are nearly constant for all applied deposition temperatures (300°C, 350°C, 400°C). We found an etch rate of ≈3nm/sec, which is approximately the fourfold value of the etch rate of thermal oxides. This value is comparable to the case of PECVD films /11/.

7. Film Composition

The physical properties of the SiO₂ layers were investigated by IR-absorption, X-ray photoelectron spectroscopy (XPS) and ellipsometry. A Perkin-Elmer 597 spectrometer (wave number 200 - 4000 cm⁻¹) was used for infra-red measurements. The film composition, was determined by XPS (ESCA LAB MK2 V.G. Scientific). IR and C-V measurements were carried out on films deposited onto one-side polished (111) 40 - 65 Ωcm wafers. In order to remove surface contaminants like carbon and oxygen, the as-deposited films were sputtered for 30 minutes with argon ions (corresponding to ≈30 nm film depth). After this treatment the observed composition of 31.8 at.% Si and 68.2 at.% O₂ corresponds closely to stoichiometric SiO₂. This agrees well with ellipsometric measurements of the refractive index for the 400°C sample, which gave a value of 1.452, close to the value 1.456 of thermal oxide. Fig. 10 shows typical IR-spectra for different deposition temperatures. For a deposition temperature of 400°C the SiO₂ spectra resemble the spectra from thermally grown SiO₂, except of an O-H stretching peak at 3620 cm⁻¹. For lower deposition temperatures like 300°C an additional peak appeared at 880 cm⁻¹. This peak is well known as corresponding to Si-H vibrations /12/. Si-H groups generate localized states within the energy gap of silicon dioxide /13/ and impair the quality of SiO₂ films.
8. Electrical Measurements  
(C-V characteristics and dielectric breakdown strength) 

MIS capacitors were utilized to characterize the electronic properties of as-deposited and thermally annealed (30 min, 450°C, forming gas) films. Aluminium dots were sputtered onto deposited SiO$_2$ films and structurized by photolithography to form MIS capacitors. Dots with an area of $1.0 \times 10^{-2}$ cm$^2$ were sputtered on films of about 200 nm thickness. The depositions were performed on a (100) n-Si substrate with a resistivity of 40-65 Ωcm. A resulting 1 MHz (at room temperature) C-V plot for an as-deposited sample is shown in Fig. 11. A flatband voltage shift of $-4$ V can be observed, caused by oxide and surface charges. The C-V curve shows a small amount of hysteresis this indicates mobile ion drift or occurrence of interface states $^{14}/^{15}$. After the described anealing procedure the interface damage is evidently reduced. The resulting C-V curve (Fig. 12b) is now comparable to the C-V characteristics of thermally grown SiO$_2$ (Fig. 12a).

![Fig. 11 C-V curve of a SiO$_2$ film deposited at 400°C (as-deposited). Using the formula $V_{FB} = \phi_{MS} + Q_F/C_I$ $^{15}$ and the measured value of $C_I$ the density of fixed interface charges was determined to be $Q_F/e = 9 \times 10^{11}$ cm$^{-2}$.](image1)

![Fig. 12 C-V curves of a post metallization annealed SiO$_2$ film (B) the fixed interface charges (see formula Fig. 11) was determined to be $Q_F/e = 1.4 \times 10^{11}$ cm$^{-2}$. For comparison a thermal grown SiO$_2$ film is shown in curve (A).](image2)

Breakdown data were taken on similar MIS structures with sputtered aluminium dots (area of $1.5 \times 10^{-3}$ cm$^2$). A slowly increasing voltage was applied to the capacitors till they broke down. Typically 70 measurements were taken for each sample. Histograms of the breakdown fields are plotted for two different deposition temperatures (300° and 400°C) in Fig. 13. The breakdown histograms of the two processes show a shift to higher breakdown voltages at higher deposition temperatures (400°C).
Fig. 13 Breakdown histograms of SiO\textsubscript{2} films deposited at 300\textdegree{} and 400\textdegree{}C

III. Summary and Conclusions

High-quality low temperature oxide has been deposited at high deposition rates (up to \( \approx 4\text{nm/sec} \)) by a stagnation flow reactor. The advantages of the stagnation flow gas kinetics, in comparison to diffusion limited processes like quartz tube reactors, are enhanced deposition rates and adequate thickness uniformity (\( \approx 2\% \) deviation from the mean). Breakdown experiments, C-V data, IR-spectra, refractive index and XPS results all demonstrate a good film quality, comparable to thermally grown oxides.

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